

## P-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
- 100	0.039at V <sub>GS</sub> = - 10 V	- 25	40 nC
	0.046 at V <sub>GS</sub> = - 4.5 V	- 21	

### FEATURES

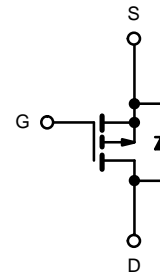
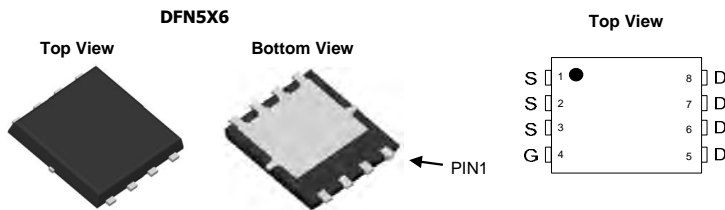
- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> Tested



RoHS  
COMPLIANT

### APPLICATIONS

- Notebook
- Load Switch



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	- 100	V
Gate-Source Voltage	V <sub>GS</sub>	± 20	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	I <sub>D</sub>	T <sub>C</sub> = 25 °C	- 25 <sup>a</sup>
		T <sub>C</sub> = 70 °C	- 18 <sup>a</sup>
		T <sub>A</sub> = 25 °C	- 5 <sup>b, c</sup>
		T <sub>A</sub> = 70 °C	- 3.3 <sup>b, c</sup>
Pulsed Drain Current	I <sub>DM</sub>	- 75	A
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	
		T <sub>A</sub> = 25 °C	- 6 <sup>b, c</sup>
Single Pulse Avalanche Current	I <sub>AS</sub>	- 30	mJ
Single Pulse Avalanche Energy	E <sub>AS</sub>	56	
Maximum Power Dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C	61
		T <sub>C</sub> = 70 °C	23
		T <sub>A</sub> = 25 °C	2.6 <sup>b, c</sup>
		T <sub>A</sub> = 70 °C	1.2 <sup>b, c</sup>
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C
Soldering Recommendations (Peak Temperature) <sup>d, e</sup>		260	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, f</sup>	t ≤ 10 s	R <sub>thJA</sub>	16	22	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	1.5	2.5	

Notes:

- Package limited.
- Surface Mounted on 1" x 1" FR4 board.
- t = 10 s.
- The DFN5x6 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.
- Maximum under Steady State conditions is 54 °C/W.

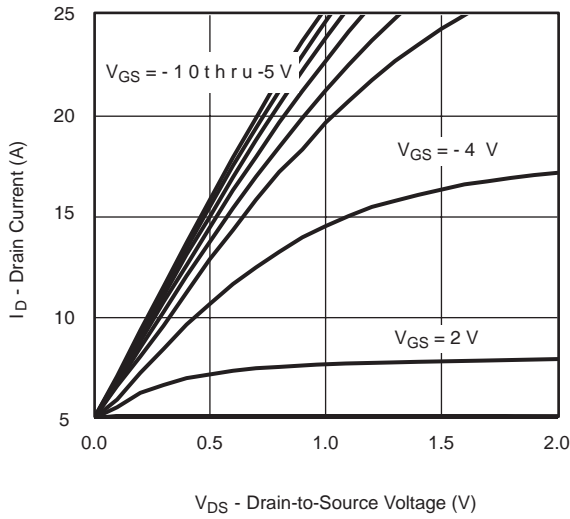
<b>SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-100			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250\text{ }\mu\text{A}$		-31		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$		6.5			
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-1.0		-3.0	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -80\text{ V}, V_{GS} = 0\text{ V}$			-1	$\mu\text{A}$
		$V_{DS} = -80\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			-10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} = -5\text{ V}, V_{GS} = -10\text{ V}$	-75			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -15\text{ A}$		0.039	0.045	$\Omega$
		$V_{GS} = -4.5\text{ V}, I_D = -10\text{ A}$		0.046	0.053	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -15\text{ V}, I_D = -15\text{ A}$		52		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = -80\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		6450		pF
Output Capacitance	$C_{oss}$		1075			
Reverse Transfer Capacitance	$C_{rss}$		415			
Total Gate Charge	$Q_g$	$V_{DS} = -80\text{ V}, V_{GS} = -10\text{ V}, I_D = -15\text{ A}$		40		nC
				32		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -80\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -10\text{ A}$		9		
Gate-Drain Charge	$Q_{gd}$			12		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		1.1		$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -80\text{ V}, R_L = 15\text{ }\Omega$ $I_D \cong -10\text{ A}, V_{GS} = -10\text{ V}, R_g = 1\text{ }\Omega$		15		ns
Rise Time	$t_r$			12		
Turn-Off Delay Time	$t_{d(off)}$			27		
Fall Time	$t_f$			10		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -80\text{ V}, R_L = 15\text{ }\Omega$ $I_D \cong -10\text{ A}, V_{GS} = -4.5\text{ V}, R_g = 1\text{ }\Omega$		19		
Rise Time	$t_r$			15		
Turn-Off Delay Time	$t_{d(off)}$			35		
Fall Time	$t_f$			10		
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			25	A
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				75	
Body Diode Voltage	$V_{SD}$	$I_S = -10\text{ A}$		-0.7	-1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		30		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			35		nC
Reverse Recovery Fall Time	$t_a$			16		ns
Reverse Recovery Rise Time	$t_b$			14		

Notes:

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.

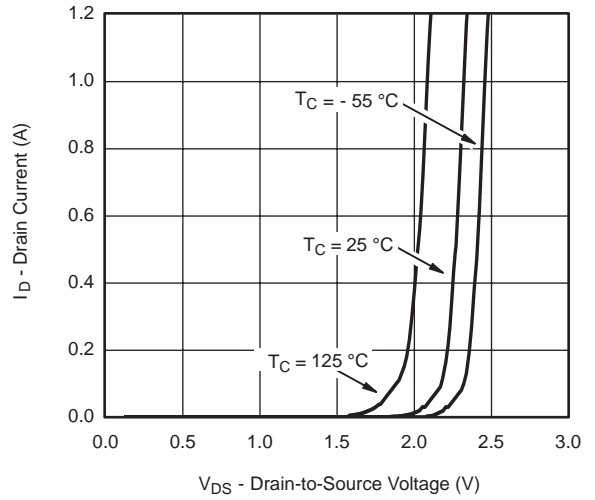
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



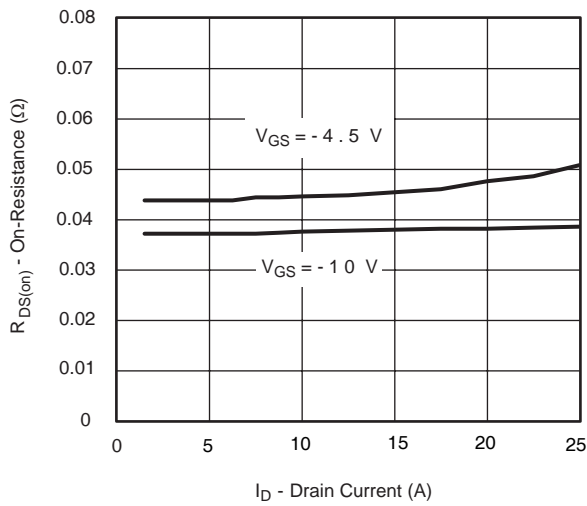
$V_{DS}$  - Drain-to-Source Voltage (V)

**Output Characteristics**



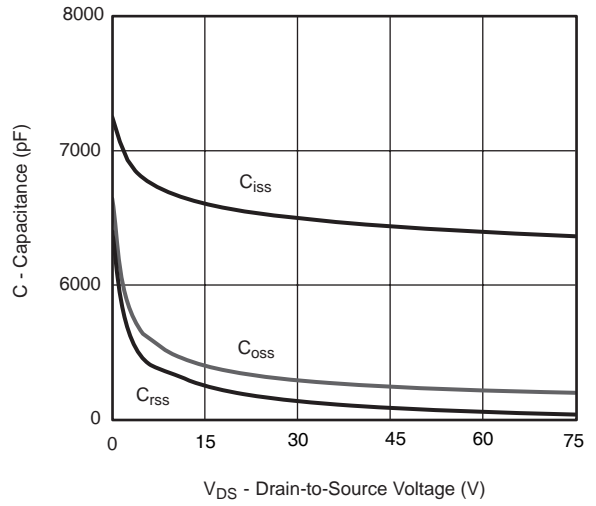
$V_{DS}$  - Drain-to-Source Voltage (V)

**Transfer Characteristics**



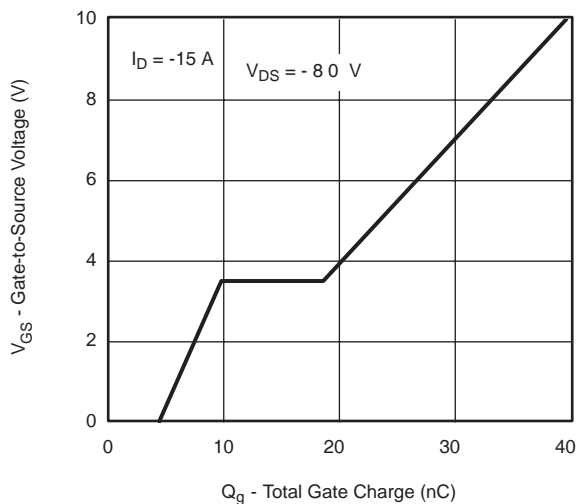
$I_D$  - Drain Current (A)

**On-Resistance vs. Drain Current and Gate Voltage**



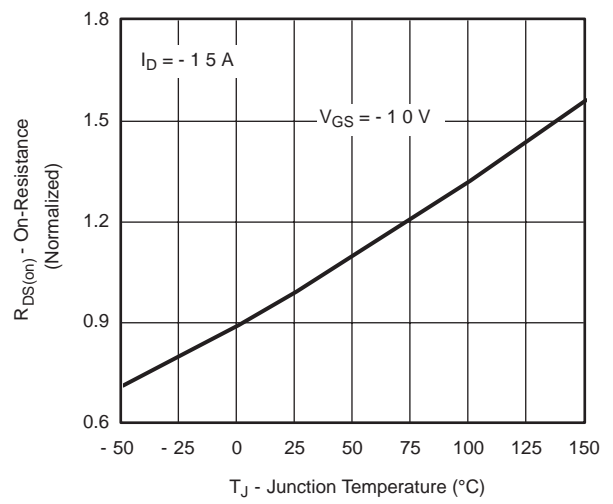
$V_{DS}$  - Drain-to-Source Voltage (V)

**Capacitance**



$Q_g$  - Total Gate Charge (nC)

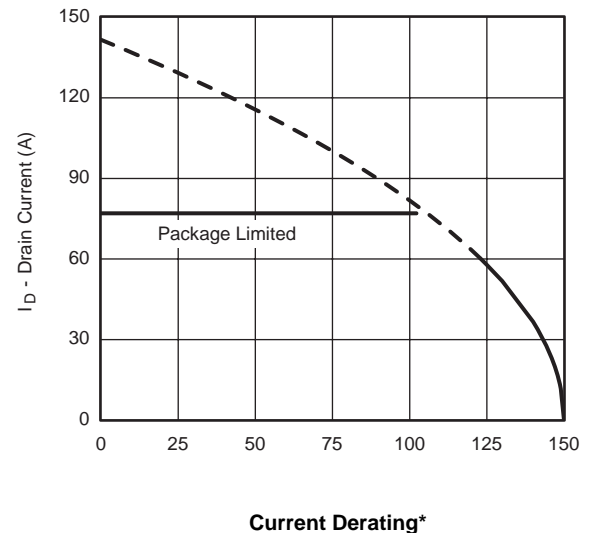
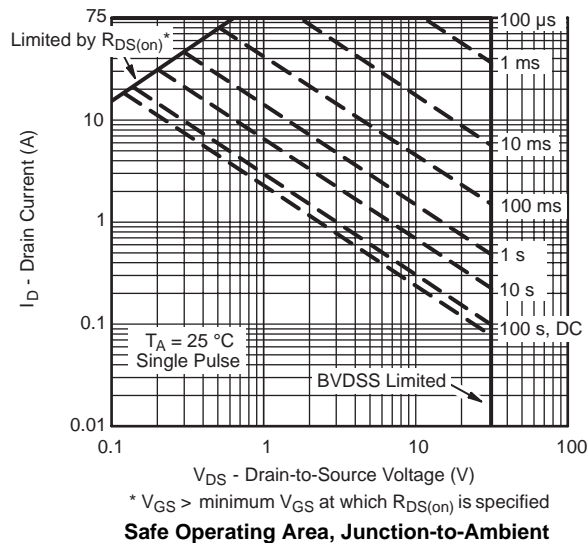
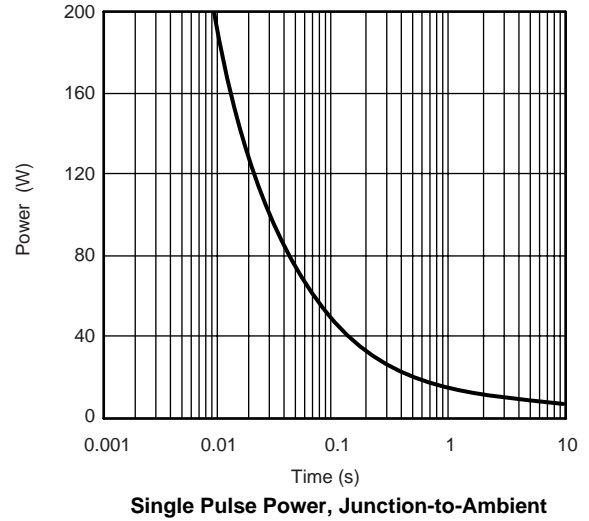
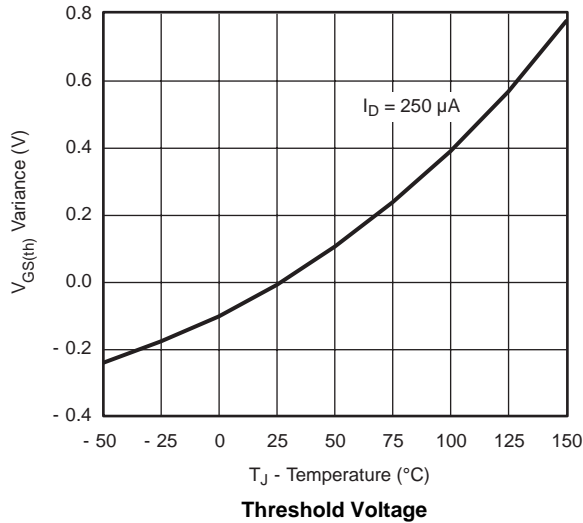
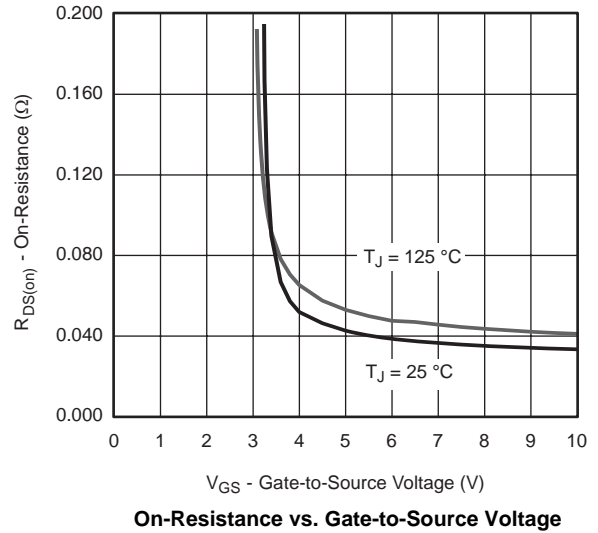
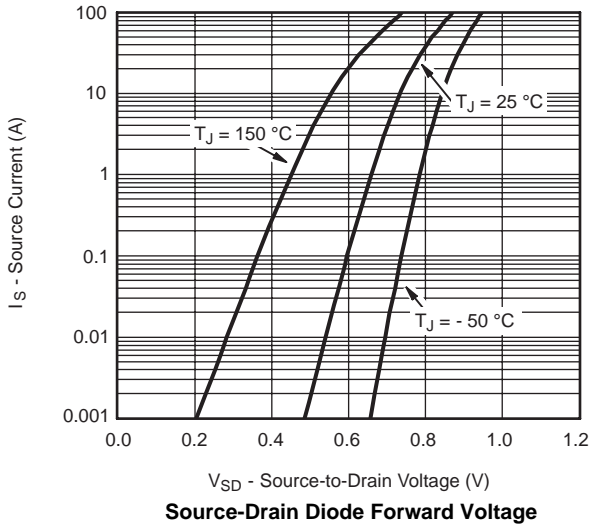
**Gate Charge**



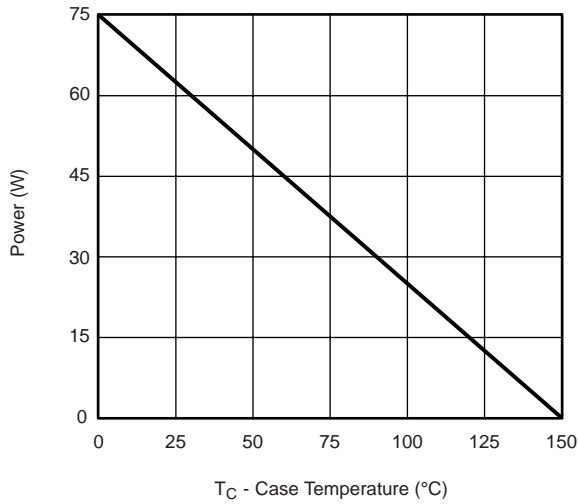
$T_J$  - Junction Temperature ( $^\circ\text{C}$ )

**On-Resistance vs. Junction Temperature**

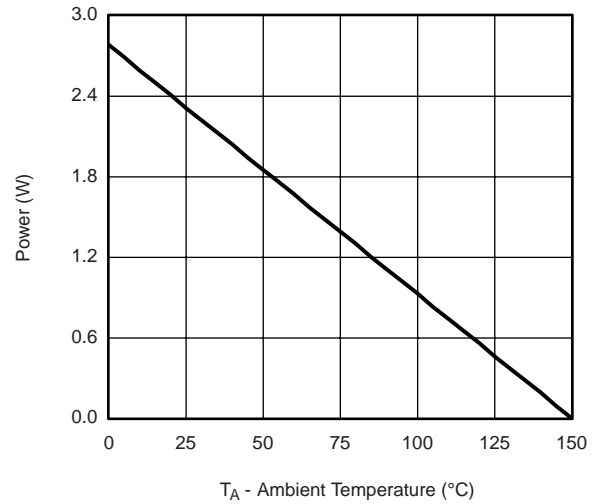
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



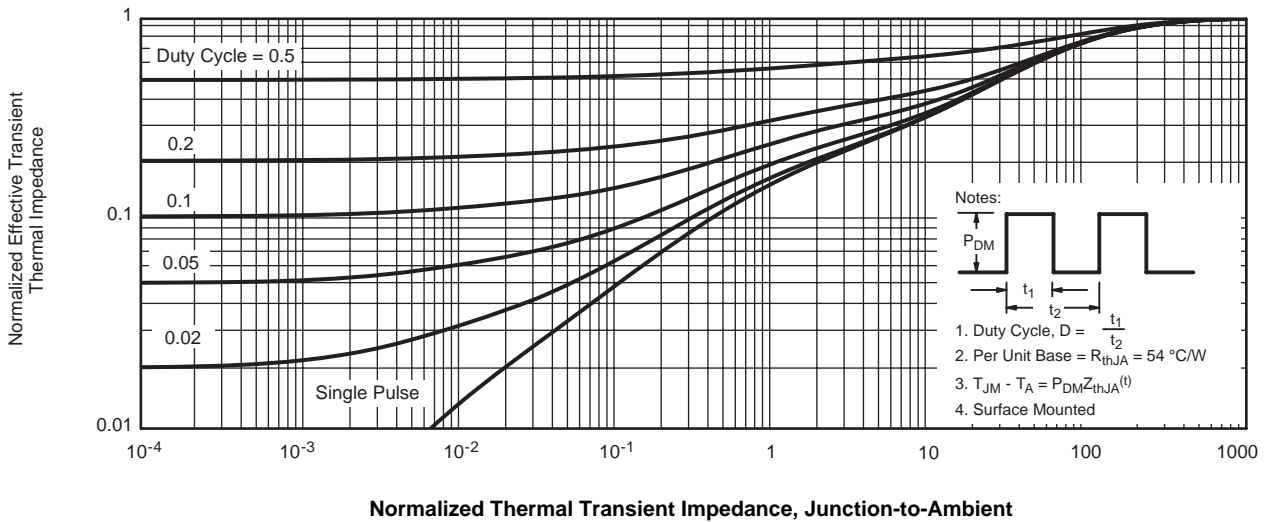
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



**Power, Junction-to-Case**

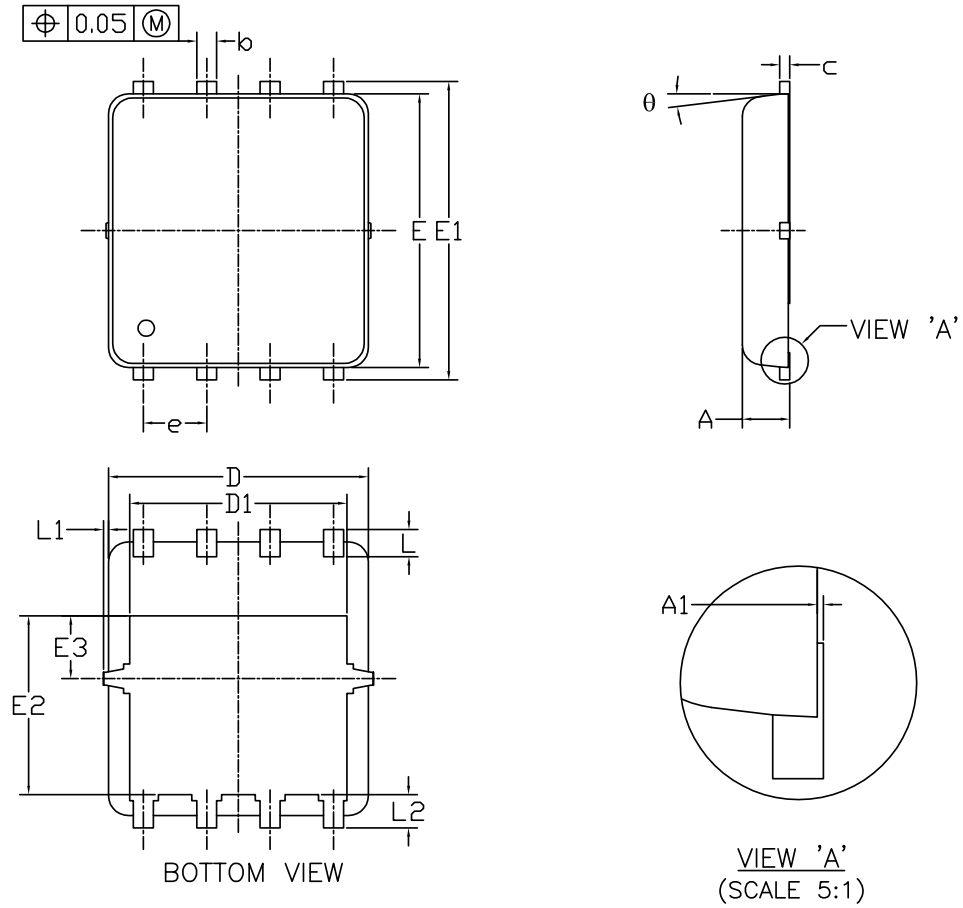


**Power, Junction-to-Ambient**

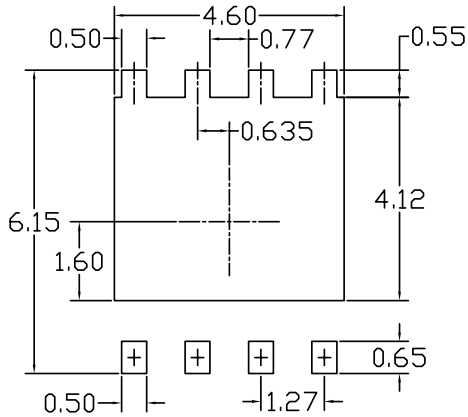


\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150 \text{ }^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

DFN5x6\_8L\_EP1\_P PACKAGE OUTLIN



RECOMMENDED LAND PATTERN



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.85	0.95	1.00	0.033	0.037	0.039
A1	0.00	---	0.05	0.000	---	0.002
b	0.30	0.40	0.50	0.012	0.016	0.020
c	0.15	0.20	0.25	0.006	0.008	0.010
D	4.80	5.20	5.30	0.201	0.205	0.209
D1	4.25	4.35	4.45	0.167	0.171	0.175
E	5.45	5.55	5.65	0.215	0.219	0.222
E1	5.95	6.05	6.15	0.234	0.238	0.242
E2	3.525	3.625	3.725	0.139	0.143	0.147
E3	1.175	1.275	1.375	0.046	0.050	0.054
e	1.27 BSC			0.050 BSC		
L	0.45	0.55	0.65	0.018	0.022	0.026
L1	0	---	0.15	0	---	0.006
L2	0.68 REF			0.027 REF		
$\theta$	0°	---	10°	0°	---	10°

NOTE

UNIT: mm

- PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.  
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- CONTROLLING DIMENSION IS MILLIMETER.  
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

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