

## Dual N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY			
$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A) <sup>d</sup>	$Q_g$ (Typ.)
60	0.012 at $V_{GS} = 10$ V	38	23 nC

### FEATURES

- DT-Trench Power MOSFET
- 100 %  $R_g$  and UIS Tested

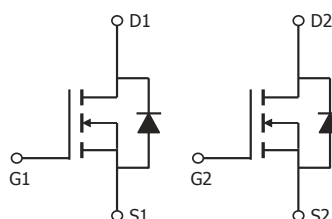
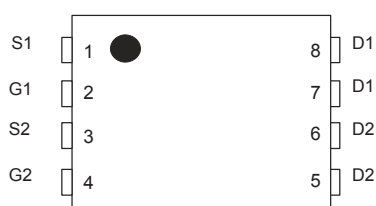


**RoHS**  
COMPLIANT

### APPLICATIONS

- 12 V Automotive systems
- Motors, lamps and solenoid control
- Transmission control
- Ultra high performance power switching

Top View



ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		$V_{DS}$	60	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150$ °C)	$T_C = 25$ °C	$I_D$	38 <sup>a</sup>	A
	$T_C = 70$ °C		32	
	$T_A = 25$ °C		25 <sup>b, c</sup>	
	$T_A = 70$ °C		18 <sup>b, c</sup>	
Pulsed Drain Current		$I_{DM}$	164	
Continuous Source-Drain Diode Current	$T_C = 25$ °C	$I_S$	30	
	$T_A = 25$ °C		25 <sup>b, c</sup>	
Avalanche Current	L = 0.1 mH	$I_{AS}$	31	
Single-Pulse Avalanche Energy		$E_{AS}$	56	mJ
Maximum Power Dissipation	$T_C = 25$ °C	$P_D$	55	W
	$T_C = 70$ °C		35	
	$T_A = 25$ °C		35 <sup>b, c</sup>	
	$T_A = 70$ °C		23 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	$t \leq 10$ s	$R_{thJA}$	35	45	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	3	8	

Notes:

- Package limited.
- Surface mounted on 1" x 1" FR4 board.
- $t = 10$  s.
- Maximum under Steady State conditions is 85 °C/W.

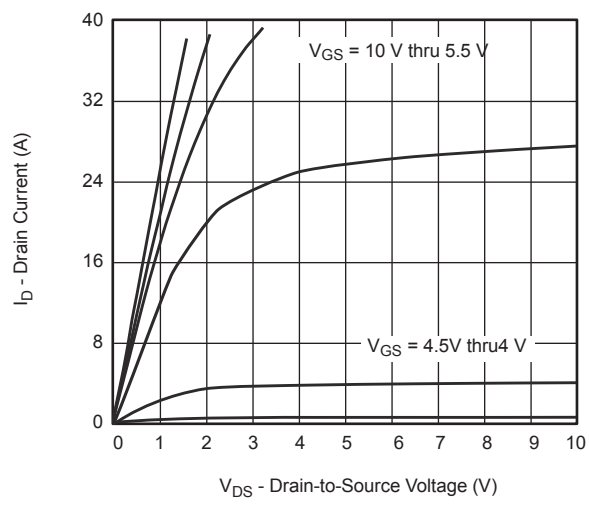
<b>SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	60			V
$V_{DS}$ Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		55		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			-6.3		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1		3	V
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}$			1	$\mu\text{A}$
		$V_{DS} = 48\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	38			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$		0.012	0.017	$\Omega$
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 10\text{ V}, I_D = 10\text{ A}$		50		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		1850		$\mu\text{F}$
Output Capacitance	$C_{oss}$			255		
Reverse Transfer Capacitance	$C_{rss}$			103		
Total Gate Charge	$Q_g$	$V_{DS} = 48\text{ V}, V_{GS} = 10\text{ V}, I_D = 10\text{ A}$		23		nC
Gate-Source Charge	$Q_{gs}$			4.5		
Gate-Drain Charge	$Q_{gd}$			8.2		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		4.3	5.5	$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 48\text{ V}, R_L = 5.4\text{ }\Omega$ $I_D \cong 10\text{ A}, V_{GEN} = 4.5\text{ V}, R_g = 1\text{ }\Omega$		12	15	ns
Rise Time	$t_r$			30	55	
Turn-Off Delay Time	$t_{d(off)}$			20	30	
Fall Time	$t_f$			20	26	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 48\text{ V}, R_L = 5.4\text{ }\Omega$ $I_D \cong 10\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$		8	15	
Rise Time	$t_r$			13	15	
Turn-Off Delay Time	$t_{d(off)}$			15	20	
Fall Time	$t_f$			10	15	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$			38	A
Pulse Diode Forward Current <sup>a</sup>	$I_{SM}$				164	
Body Diode Voltage	$V_{SD}$	$I_S = 2\text{ A}$		0.7	1.2	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 5.5\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		25	50	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			25	50	nC
Reverse Recovery Fall Time	$t_a$			19		ns
Reverse Recovery Rise Time	$t_b$			6		

Notes:

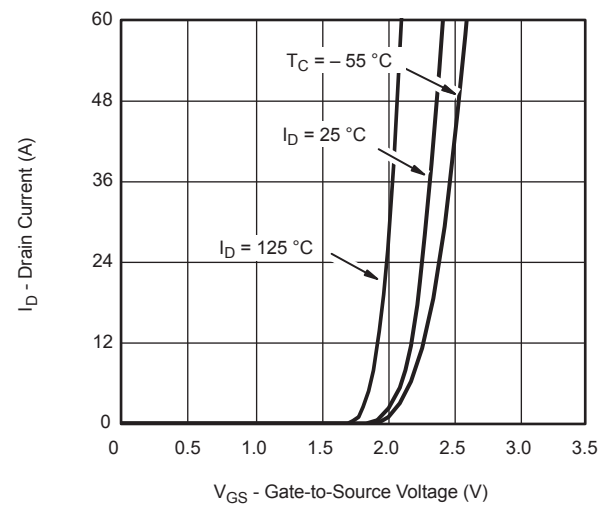
- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

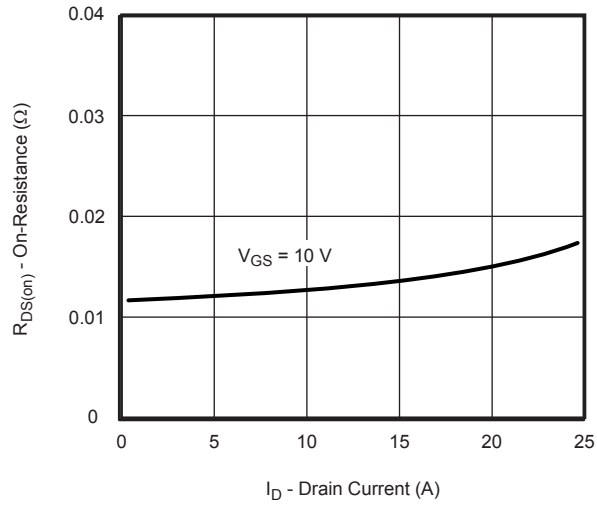
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



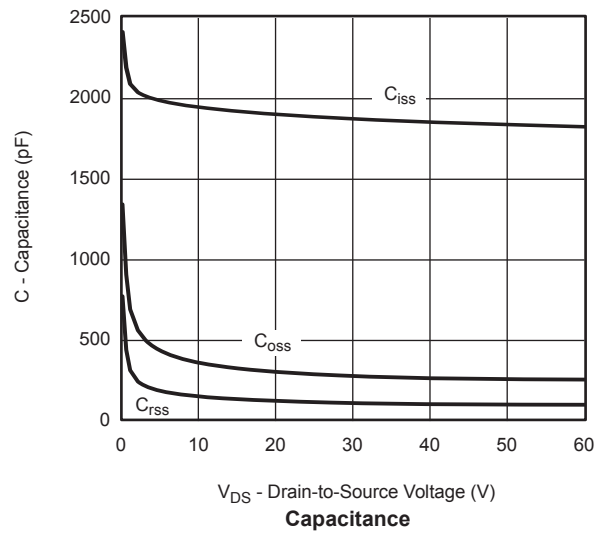
**Output Characteristics**



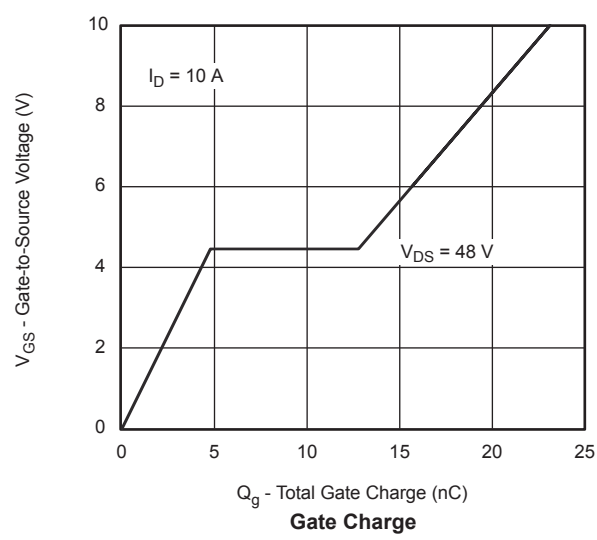
**Transfer Characteristics**



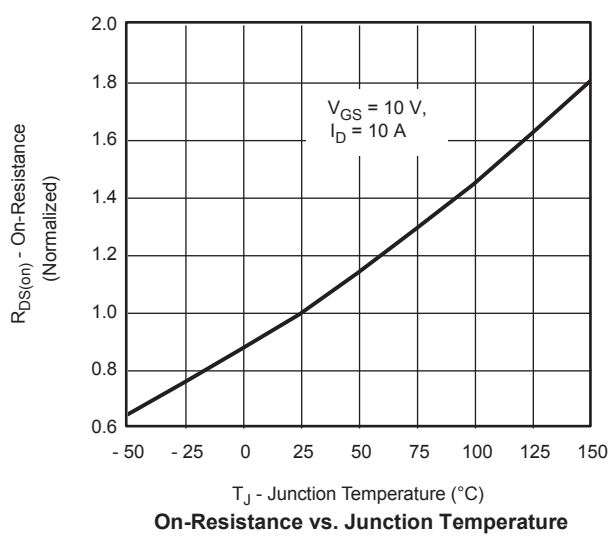
**On-Resistance vs. Drain Current**



**Capacitance**

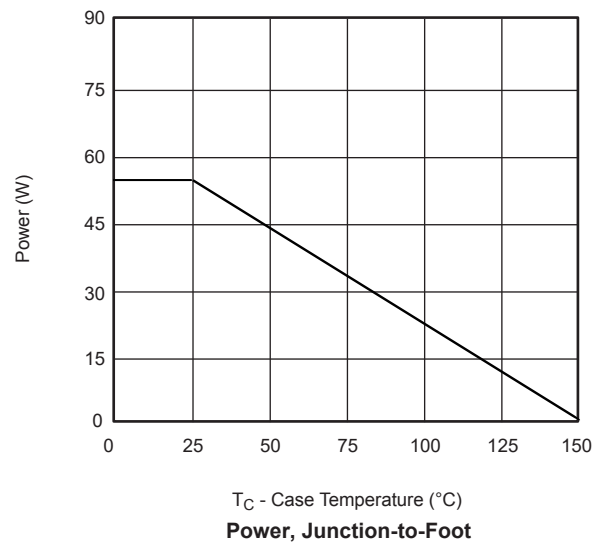
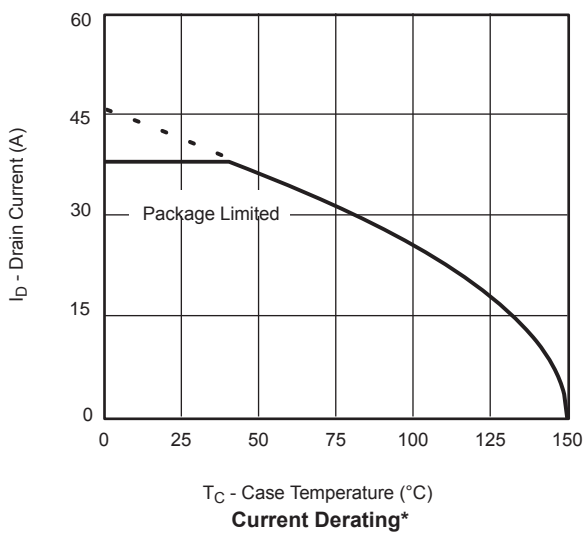
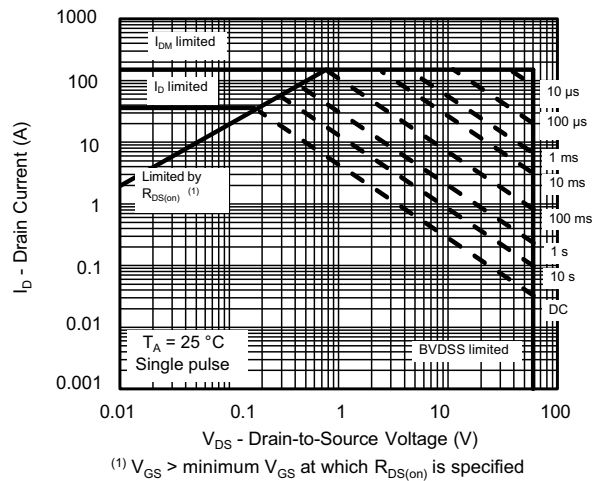
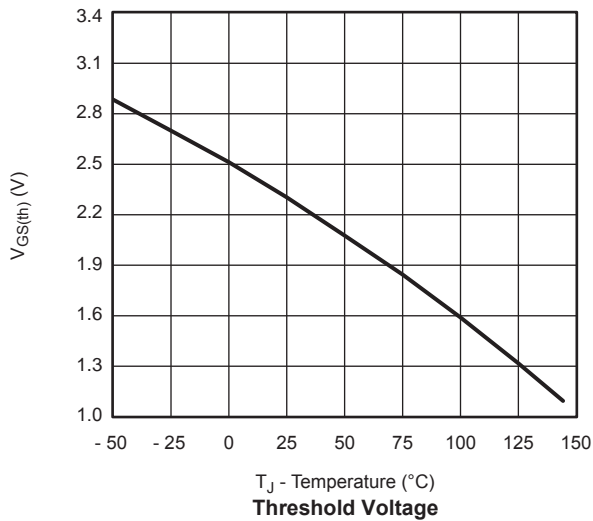
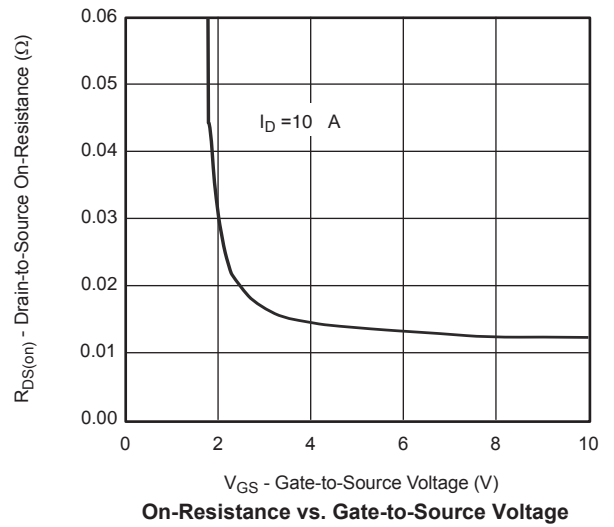
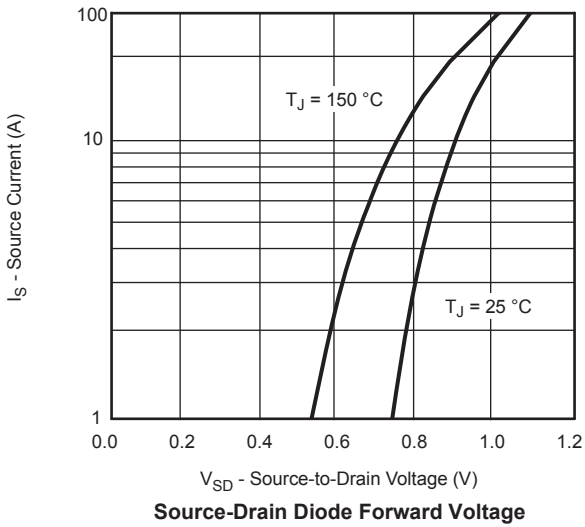


**Gate Charge**

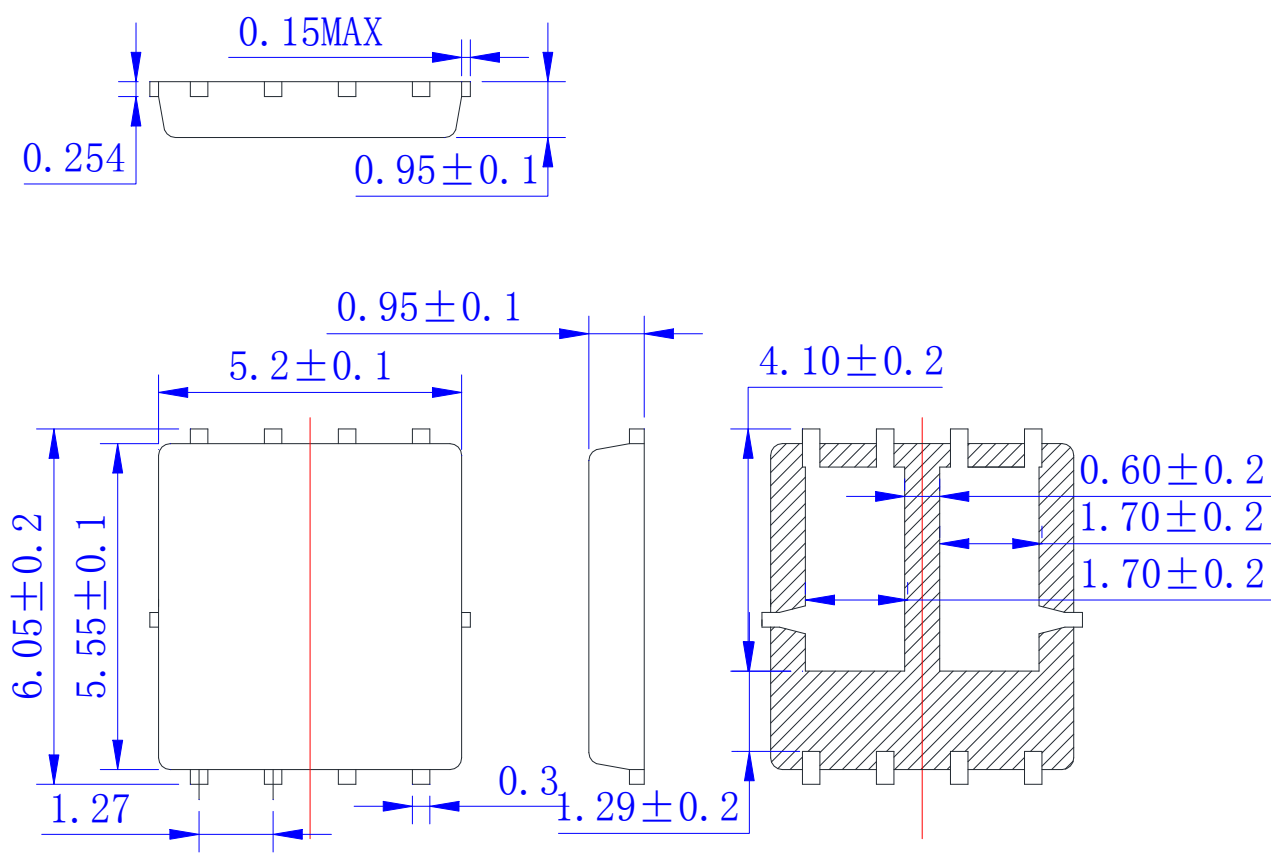


**On-Resistance vs. Junction Temperature**

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



DFN5x6 PACKAGE OUTLINE



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