

P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (Ω) Max.	I _D (A) ^a	Q _g (Typ.)
- 20	0.045 at V _{GS} = - 4.5 V	- 5.5	13.8 nC
	0.060 at V _{GS} = - 2.5 V	- 3.7	

FEATURES

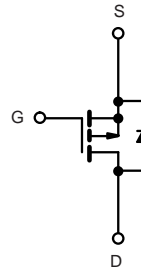
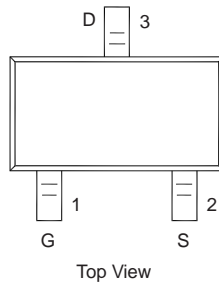
- DT-Trench Power MOSFET
- 100 % R_g Tested


RoHS
 COMPLIANT

APPLICATIONS

- Power Management for Portable and Consumer
 - Load Switches
 - DC/DC Converters

(SOT-23-3L)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	- 20	V
Gate-Source Voltage	V _{GS}	± 12	
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	- 5.5
		T _C = 70 °C	- 3.7
		T _A = 25 °C	- 3.5 ^{b, c}
		T _A = 70 °C	- 2.6 ^{b, c}
Pulsed Drain Current (t = 300 μs)	I _{DM}	- 18	A
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	
		T _A = 25 °C	- 1 ^{b, c}
Maximum Power Dissipation	P _D	T _C = 25 °C	1.7
		T _C = 70 °C	1.1
		T _A = 25 °C	1 ^{b, c}
		T _A = 70 °C	0.6 ^{b, c}
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C
Soldering Recommendations (Peak Temperature) ^{d, e}		260	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R _{thJA}	65	80	°C/W
Maximum Junction-to-Foot (Drain)	R _{thJF}	40	55	

Notes:

- T_C = 25 °C.
- Surface mounted on 1" x 1" FR4 board.
- t = 5 s.
- Maximum under steady state conditions is 175 °C/W.

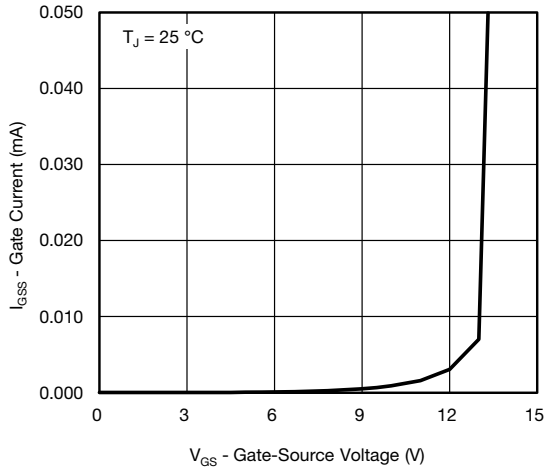
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-20			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250\text{ }\mu\text{A}$		-14		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			2.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-0.4		-1.5	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 12\text{ V}$			± 10	μA
		$V_{DS} = 0\text{ V}, V_{GS} = \pm 4.5\text{ V}$			± 1	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20\text{ V}, V_{GS} = 0\text{ V}$			-1	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \leq -5\text{ V}, V_{GS} = -4.5\text{ V}$	-15			A
		$V_{GS} = -4.5\text{ V}, I_D = -2\text{ A}$		0.045	0.050	Ω
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -2.5\text{ V}, I_D = -2\text{ A}$		0.060	0.070	
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = -10\text{ V}, V_{GS} = -12\text{ V}, I_D = -4.5\text{ A}$		23.8	36	nC
				13.8	21	
				1.9		
Gate-Source Charge	Q_{gs}	$V_{DS} = -10\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -4.5\text{ A}$				
Gate-Drain Charge	Q_{gd}			3		
Gate Resistance	R_g	$f = 1\text{ MHz}$	2.2	11	22	Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10\text{ V}, R_L = 2.8\text{ }\Omega$ $I_D \cong -3.6\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 1\text{ }\Omega$		22	33	ns
Rise Time	t_r			21	32	
Turn-Off Delay Time	$t_{d(off)}$			62	93	
Fall Time	t_f			14	21	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -10\text{ V}, R_L = 2.8\text{ }\Omega$ $I_D \cong -3.6\text{ A}, V_{GEN} = -8\text{ V}, R_g = 1\text{ }\Omega$		9	18	
Rise Time	t_r			6	12	
Turn-Off Delay Time	$t_{d(off)}$			65	98	
Fall Time	t_f			15	23	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			-2.4	A
Pulse Diode Forward Current	I_{SM}				-18	
Body Diode Voltage	V_{SD}	$I_S = -3.6\text{ A}, V_{GS} = 0\text{ V}$		-0.8	-1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -3.6\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		13	20	ns
Body Diode Reverse Recovery Charge	Q_{rr}			5	10	nC
Reverse Recovery Fall Time	t_a			8		ns
Reverse Recovery Rise Time	t_b			5		

Notes:

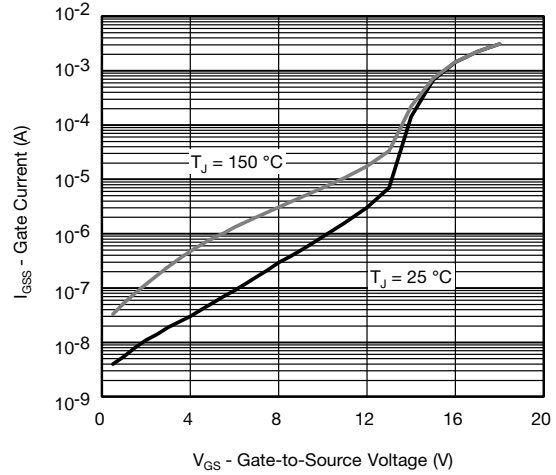
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

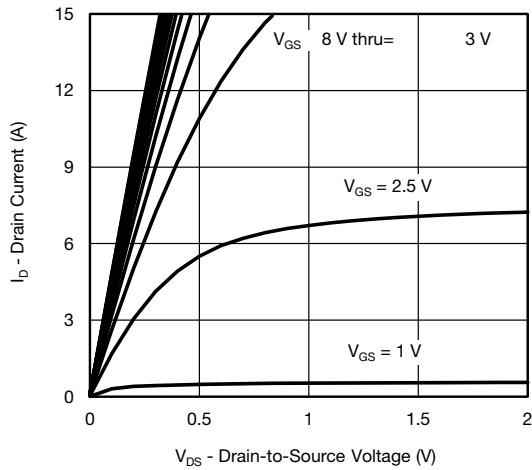
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



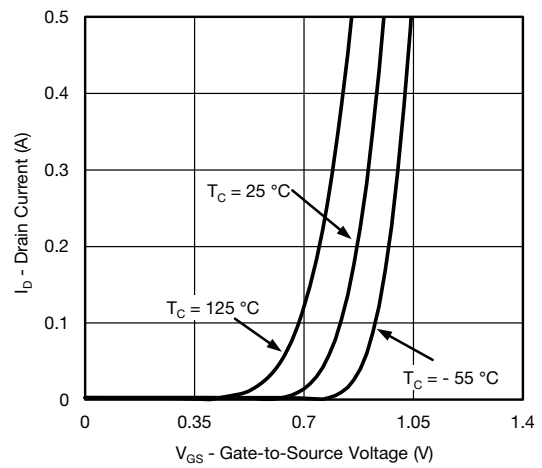
Gate Current vs. Gate-Source Voltage



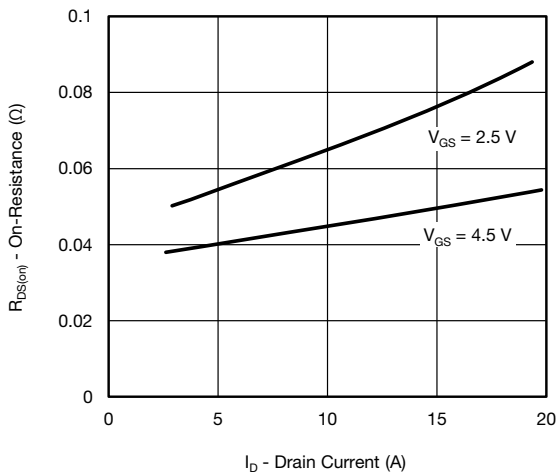
Gate Current vs. Gate-Source Voltage



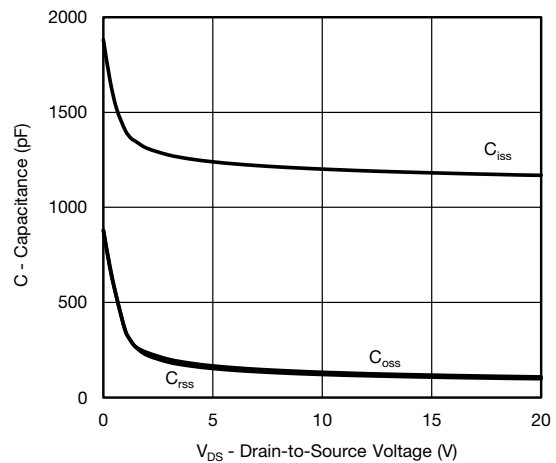
Output Characteristics



Transfer Characteristics

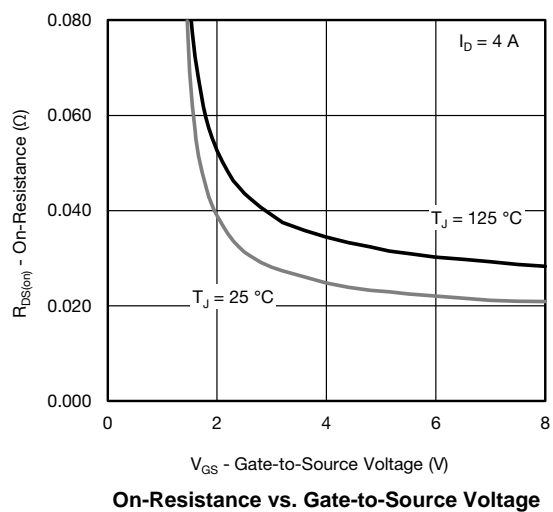
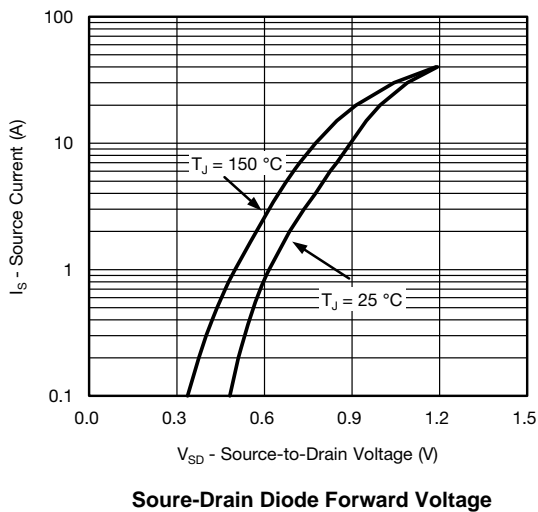
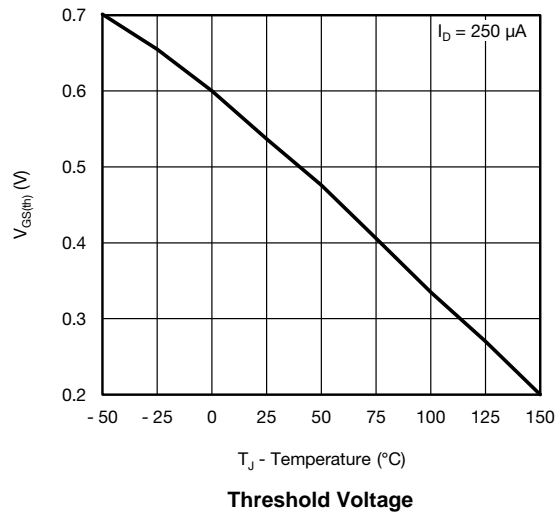
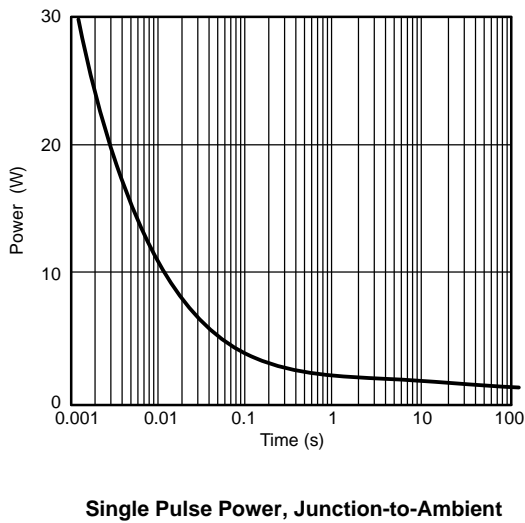
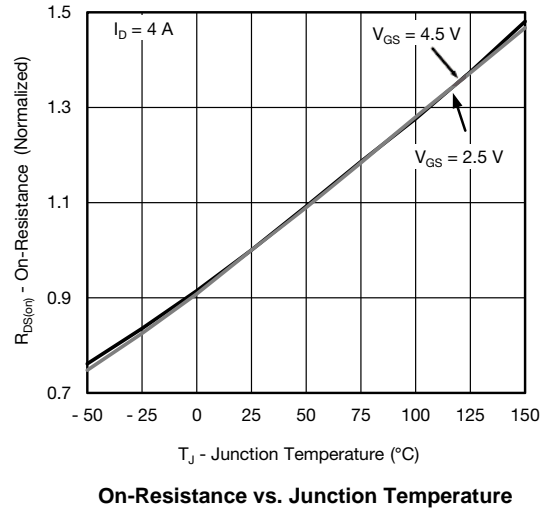
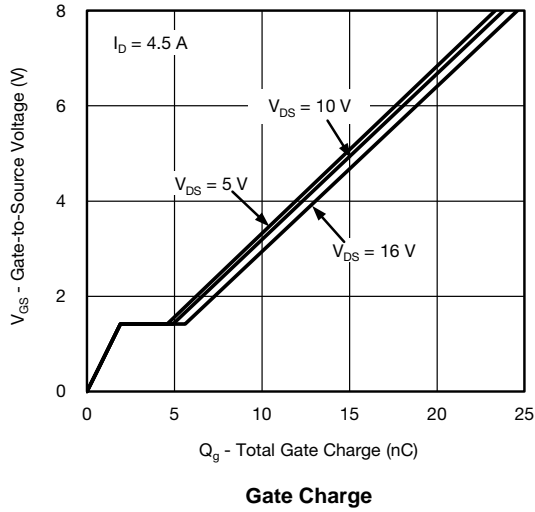


On-Resistance vs. Drain Current

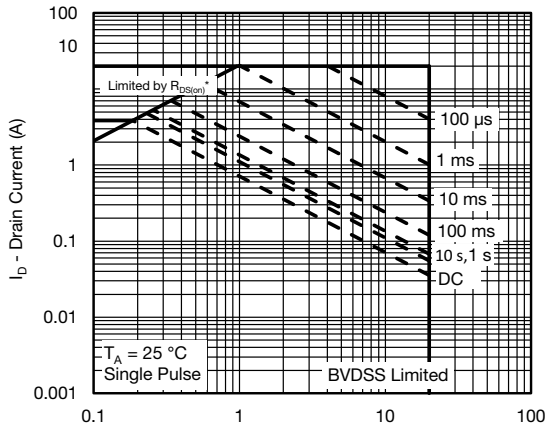


Capacitance

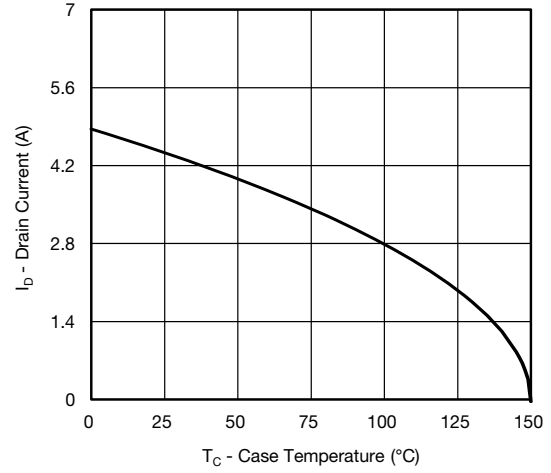
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



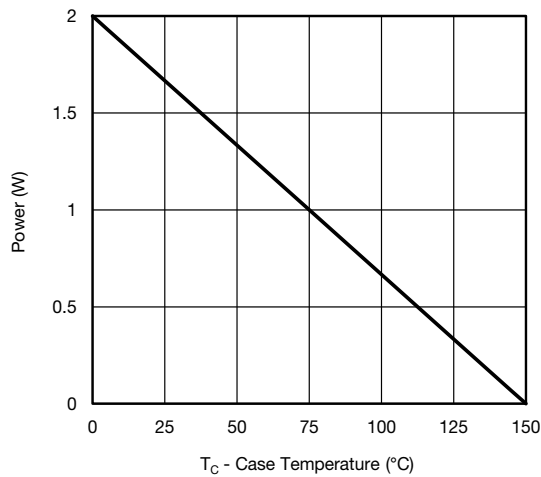
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



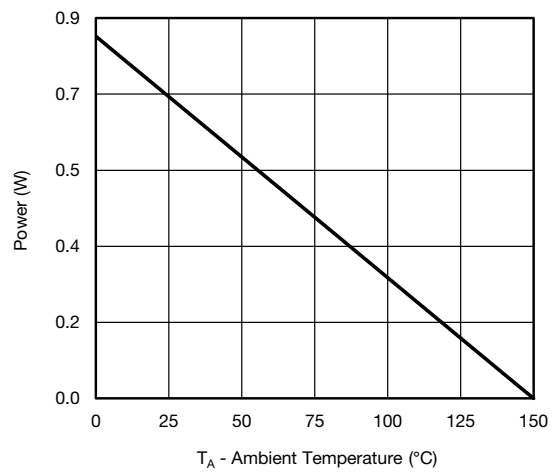
V_{DS} - Drain-to-Source Voltage (V)
* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified
Safe Operating Area, Junction-to-Ambient



Current Derating*



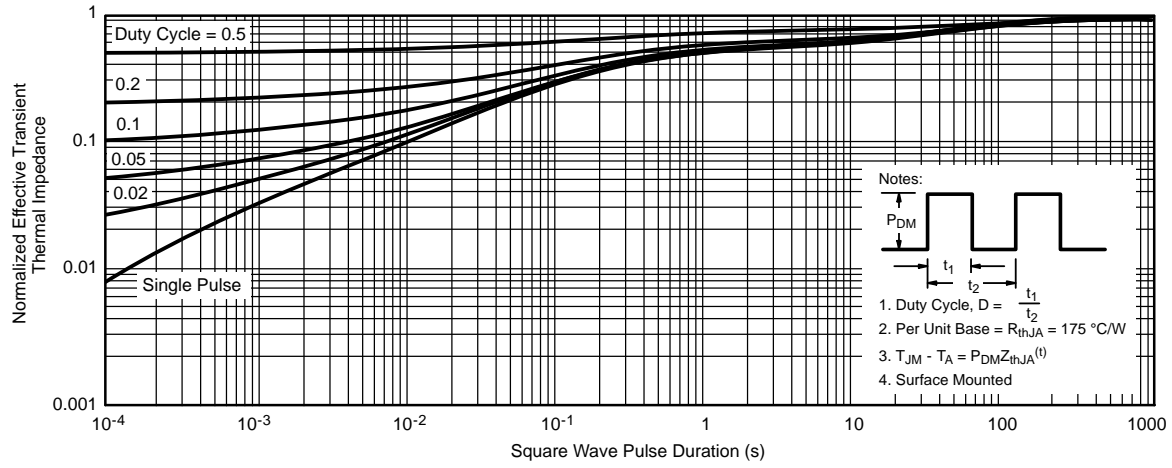
Power Junction-to-Case



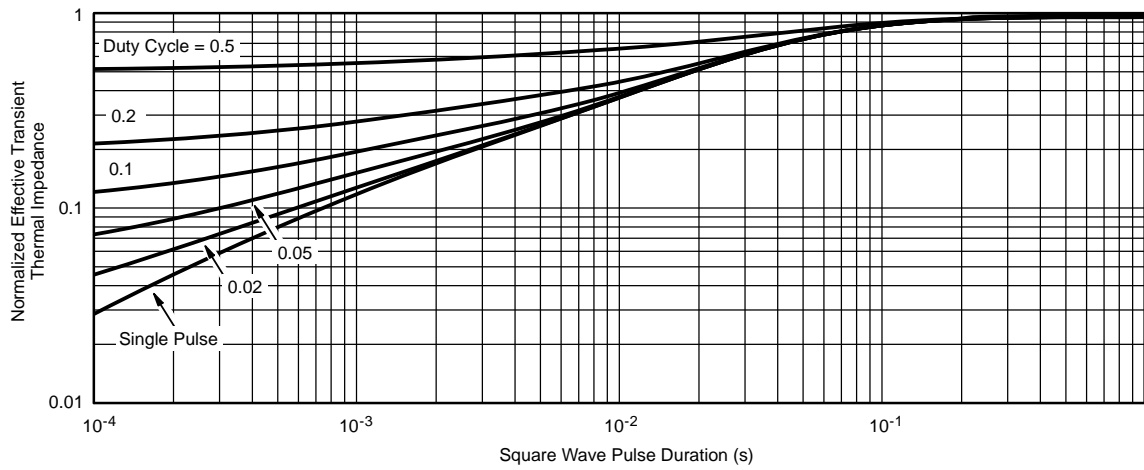
Power Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

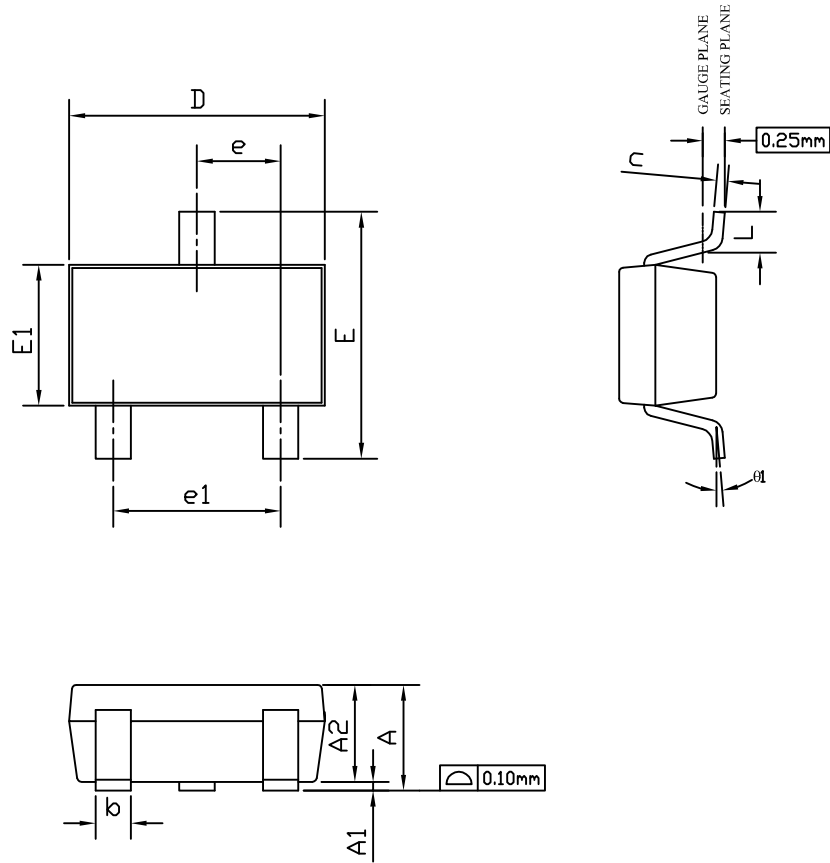


Normalized Thermal Transient Impedance, Junction-to-Ambient

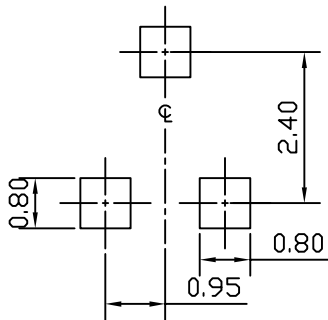


Normalized Thermal Transient Impedance, Junction-to-Foot

SOT-23-3L PACKAGE OUTLINE



RECOMMENDED LAND PATTERN



UNIT: mm

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.85	---	1.25	0.033	---	0.049
A1	0.00	---	0.13	0.000	---	0.005
A2	0.70	1.00	1.15	0.028	0.039	0.045
b	0.30	0.40	0.50	0.012	0.016	0.020
c	0.08	0.13	0.20	0.003	0.005	0.008
D	2.80	2.90	3.10	0.110	0.114	0.122
E	2.60	2.80	3.00	0.102	0.110	0.118
E1	1.40	1.60	1.80	0.055	0.063	0.071
e	0.95 BSC			0.037 BSC		
e1	1.90 BSC			0.075 BSC		
L	0.30	---	0.60	0.012	---	0.024
θ1	0°	5°	8°	0°	5°	8°

NOTE

1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH OR GATE BURRS.
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 5 MILS EACH.
2. TOLERANCE ± 0.100 mm (4 mil) UNLESS OTHERWISE SPECIFIED.
3. DIMENSION L IS MEASURED IN GAUGE PLANE.
4. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.
5. ALL DIMENSIONS ARE IN MILLIMETERS.

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