

P-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)
- 40	0.0096 at V _{GS} = - 10 V	- 70 ^d
	0.012 at V _{GS} = - 4.5 V	-60 ^d

FEATURES

- DT-Trench Power MOSFET
- 100 % Rg and UIS Tested

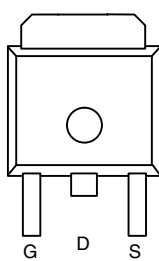


RoHS
COMPLIANT

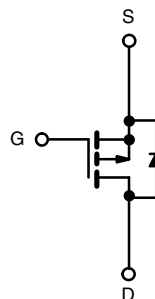
APPLICATIONS

- Load Switch

TO-252



Top View



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 40	V	
Gate-Source Voltage	V _{GS}	± 20		
Continuous Drain Current (T _J = 175 °C)	I _D	T _C = 25 °C	- 70 ^d	
		T _C = 125 °C	- 41	
Pulsed Drain Current	I _{DM}	- 280	A	
Avalanche Current	I _{AR}	- 63		
Repetitive Avalanche Energy ^a	L = 0.1 mH	E _{AR}	125	mJ
Power Dissipation	P _D	T _C = 25 °C	88 ^c	W
		T _A = 25 °C	2.9 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit	
Junction-to-Ambient ^b	R _{thJA}	t ≤ 10 s	20	30	°C/W
		Steady State	40	60	
Junction-to-Case	R _{thJC}	-	2.1		

Notes:

- Duty cycle ≤ 1 %.
- When mounted on 1" square PCB (FR-4 material).
- See SOA curve for voltage derating.
- Package limited.

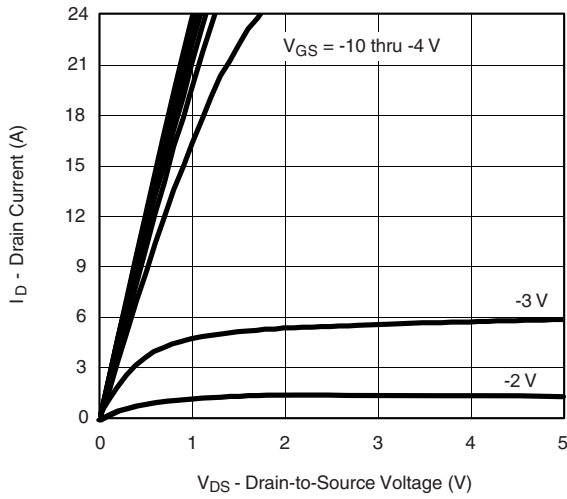
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	- 40			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	- 1		- 3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -40\text{ V}, V_{GS} = 0\text{ V}$			- 1	μA
		$V_{DS} = -32\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$			- 50	
		$V_{DS} = -32\text{ V}, V_{GS} = 0\text{ V}, T_J = 150\text{ }^\circ\text{C}$			- 100	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5\text{ V}, V_{GS} = -10\text{ V}$	- 70			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -30\text{ A}$		0.0096	0.0125	Ω
		$V_{GS} = -4.5\text{ V}, I_D = -20\text{ A}$		0.012	0.016	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15\text{ V}, I_D = -30\text{ A}$		66		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = -20\text{ V}, f = 1\text{ MHz}$		4910		μF
Output Capacitance	C_{oss}			392		
Reverse Transfer Capacitance	C_{rss}			103		
Total Gate Charge ^c	Q_g	$V_{DS} = -20\text{ V}, V_{GS} = -10\text{ V}, I_D = -30\text{ A}$		55		nC
Gate-Source Charge ^c	Q_{gs}			21		
Gate-Drain Charge ^c	Q_{gd}			7		
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = -20\text{ V}, R_L = 0.6\text{ }\Omega$ $I_D \cong -5\text{ A}, V_{GEN} = -10\text{ V}, R_G = 6\text{ }\Omega$		10		ns
Rise Time ^c	t_r			30		
Turn-Off Delay Time ^c	$t_{d(off)}$			62		
Fall Time ^c	t_f			22		
Source-Drain Diode Ratings and Characteristics $T_C = 25\text{ }^\circ\text{C}^b$						
Continuous Current	I_S				- 55	A
Forward Voltage ^a	V_{SD}	$I_F = -1\text{ A}, V_{GS} = 0\text{ V}$		- 0.7	- 1.2	V
Reverse Recovery Time	t_{rr}	$I_F = -10\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}$		39		ns
Reverse Recovery Charge	Q_{rr}	$I_F = -10\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}$		47		nC

Notes:

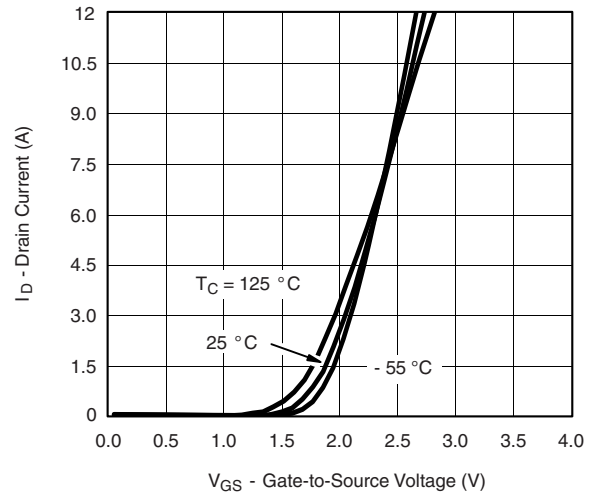
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

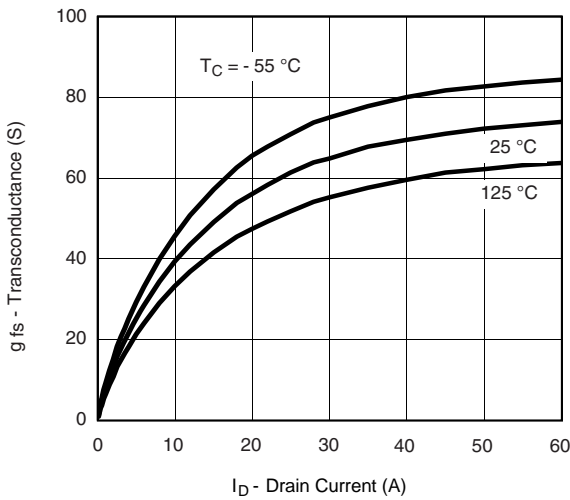
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



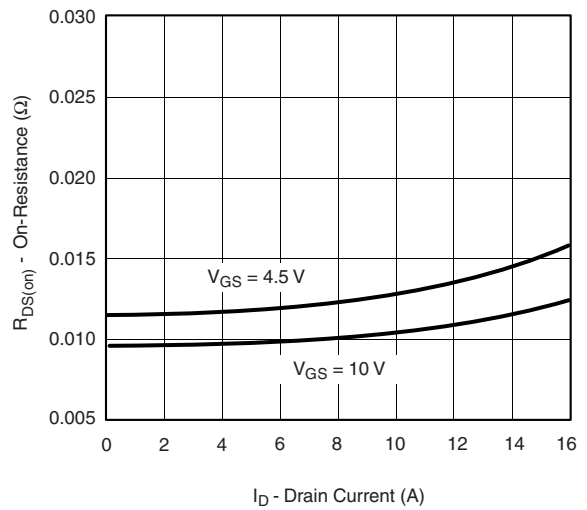
Output Characteristics



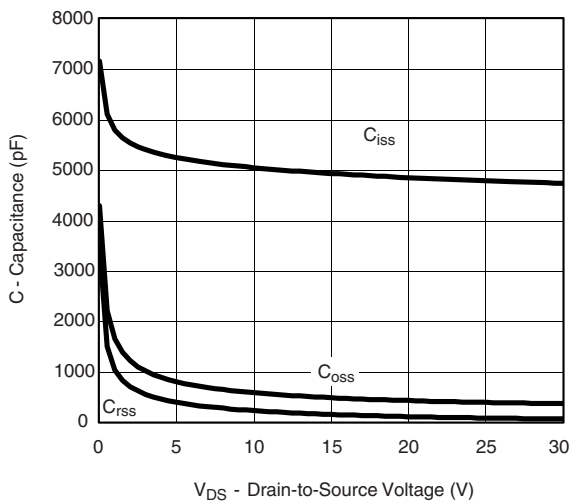
Transfer Characteristics



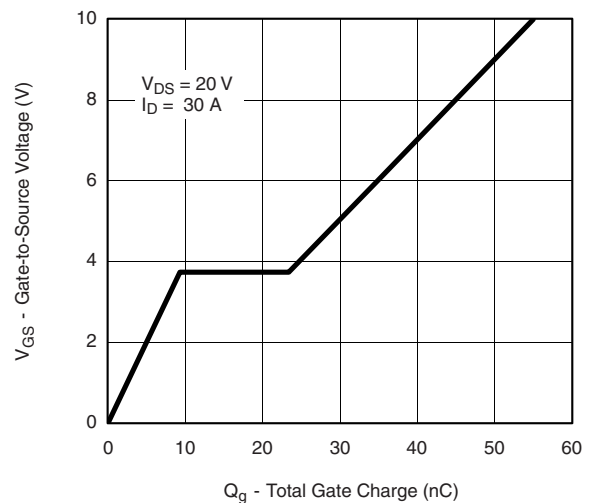
Transconductance



On-Resistance vs. Drain Current

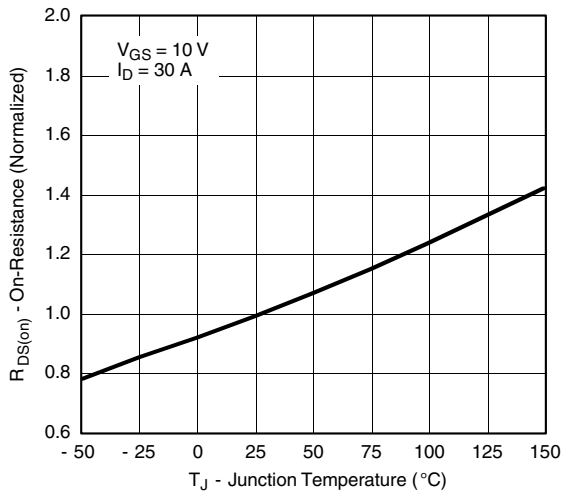


Capacitance

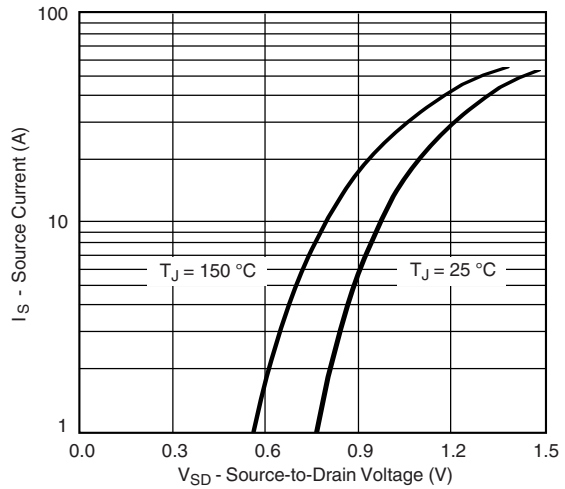


Gate Charge

TYPICAL CHARACTERISTICS

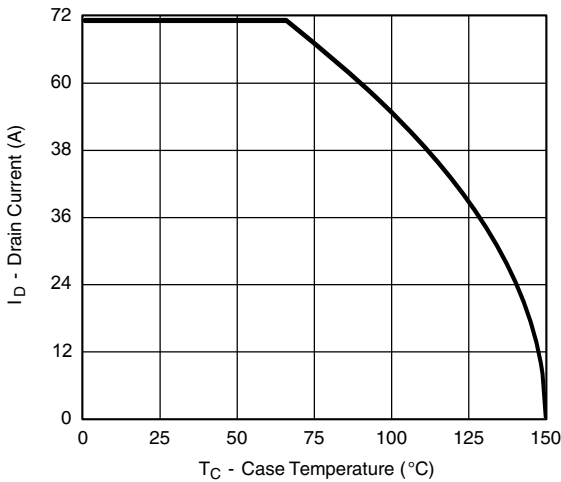


On-Resistance vs. Junction Temperature

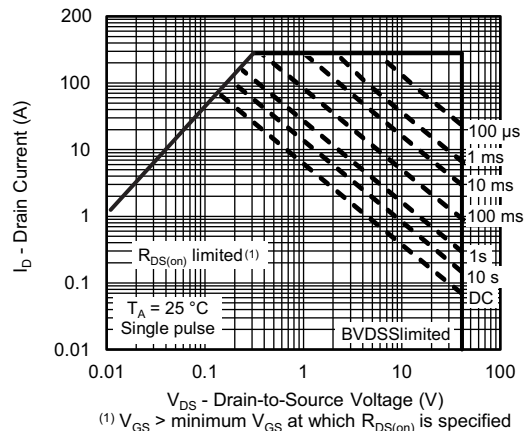


Source-Drain Diode Forward Voltage

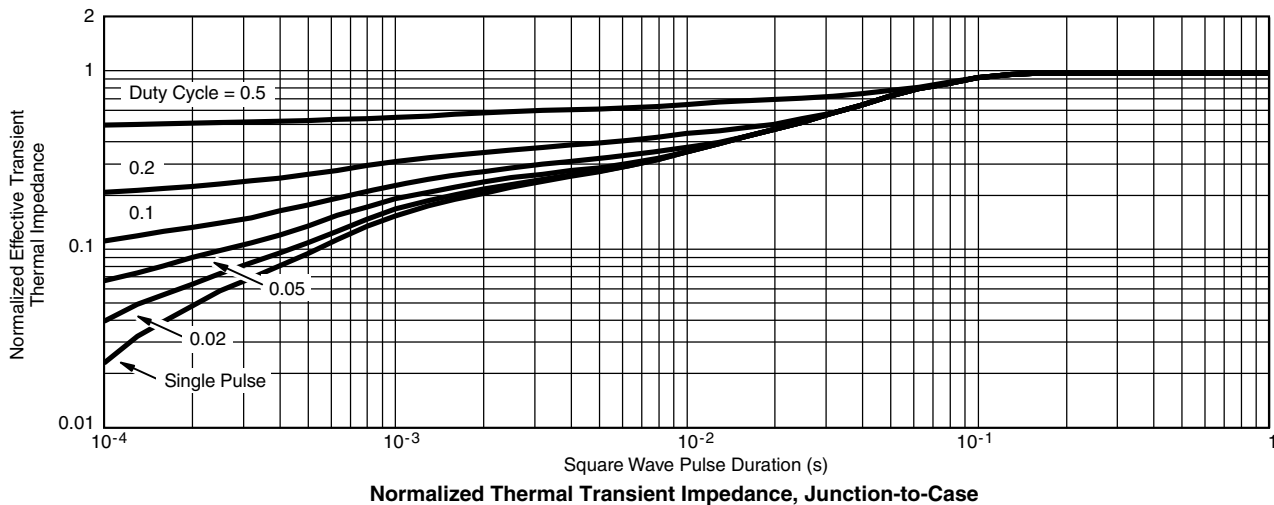
THERMAL RATINGS (25 °C, unless otherwise noted)



Drain Current vs. Case Temperature



Safe Operating Area, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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