N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (mΩ)(Typ.)	I_D (A)[a, d]	Q_g (Typ.)
100	4 at V_{GS} = 10 V	120	135nC

RoHS COMPLIANT

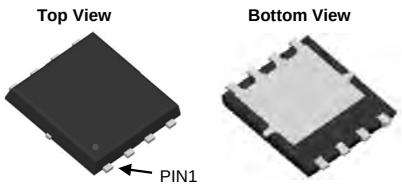
FEATURES

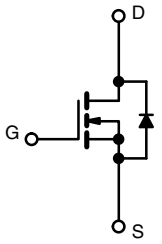
- TrenchFET IIPower MOSFET
- 100 % Rgand UIS Tested

APPLICATIONS

- Synchronous rectification
- Primary side switch
- DC/DC converters
- OR-ing
- Power supplies
- Motor drive control
- Battery and load switch

DFN5X6





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	100	V
Gate-source voltage		V_{GS}	± 20	
Continuous drain current (T_J = 150 °C)	T_C = 25 °C	I_D	120 [a]	A
	T_C = 70 °C		90.6	
	T_A = 25 °C		43.2 [b, c]	
	T_A = 70 °C		28.5 [b, c]	
Pulsed drain current (t = 100 µs)		I_{DM}	480	
Continuous source-drain diode current	T_C = 25 °C	I_S	120 [a]	
	T_A = 25 °C		6.9 [b, c]	
Single pulse avalanche current	L = 0.1 mH	I_{AS}	78	
Single pulse avalanche energy		E_{AS}	126	mJ
Maximum power dissipation	T_C = 25 °C	P_D	187	W
	T_C = 70 °C		119.7	
	T_A = 25 °C		7.98 [b, c]	
	T_A = 70 °C		5.1 [b, c]	
Operating junction and storage temperature range		T_J, T_{stg}	-55 to +150	°C
Soldering recommendations (peak temperature) [c]			260	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient [b]	t ≤ 10 s	R_{thJA}	11	20	°C/W
Maximum junction-to-case (drain)	Steady state	R_{thJC}	0.7	1	
Maximum junction-to-case (source)	Steady state	R_{thJC}	1.0	1.4	

Notes:
a. Based on T_C = 25 °C.
b. Surface mounted on 1" x 1" FR4 board.
c. t = 10 s.
d. Calculated based on maximum junction temperature.

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static						
Drain-source breakdown voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	100	-	-	V
V_{DS} temperature coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA	-	56	-	mV/°C
$V_{GS(th)}$ temperature coefficient	$\Delta V_{GS(th)}/T_J$	I_D = 250 µA	-	-6	-	
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1	-	2.5	V
Gate-source leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V	-	-	100	nA
Zero gate voltage drain current	I_{DSS}	V_{DS} = 80 V, V_{GS} = 0 V	-	-	1	µA
		V_{DS} = 80 V, V_{GS} = 0 V, T_J = 70 °C	-	-	10	
On-state drain current [a]	$I_{D(on)}$	V_{DS} ≥ 5 V, V_{GS} =10 V	120	-	-	A
Drain-source on-state resistance [a]	$R_{DS(on)}$	V_{GS} =10 V, I_D = 20 A	-	4	4.9	mΩ
Forward transconductance [a]	g_{fs}	V_{DS} = 10 V, I_D = 20 A	-	66	-	S
Dynamic [b]						
Input capacitance	C_{iss}	V_{DS} = 50 V, V_{GS} = 0 V, f = 1 MHz	-	4924	-	pF
Output capacitance	C_{oss}		-	1688	-	
Reverse transfer capacitance	C_{rss}		-	165	-	
Total gate charge	Q_g	V_{DS} = 50 V, V_{GS} = 10 V, I_D = 20 A	-	135	-	nC
Gate-source charge	Q_{gs}		-	12	-	
Gate-drain charge	Q_{gd}		-	10.9	-	
Output charge	Q_{oss}	V_{DS} = 50 V, V_{GS} = 0 V	-	68	113	
Gate resistance	R_g	f = 1 MHz	0.3	1	2.5	Ω
Turn-on delay time	$t_{d(on)}$	V_{DD} = 50 V, R_L = 2.5 Ω, I_D ≅ 20 A, V_{GEN} = 10 V, R_g = 1 Ω	-	16	28	ns
Rise time	t_r		-	19	37	
Turn-off delay time	$t_{d(off)}$		-	37	79	
Fall time	t_f		-	9	18	
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I_S	T_C = 25 °C	-	-	120	A
Pulse diode forward current (t_p = 100 µs)	I_{SM}		-	-	480	
Body diode voltage	V_{SD}	I_S = 5 A, V_{GS} = 0 V	-	0.7	1.2	V
Body diode reverse recovery time	t_{rr}	I_F = 20 A, di/dt = 100 A/µs, T_J = 25 °C	-	55	106	ns
Body diode reverse recovery charge	Q_{rr}		-	77	143	nC
Reverse recovery fall time	t_a		-	27	-	ns
Reverse recovery rise time	t_b		-	32	-	

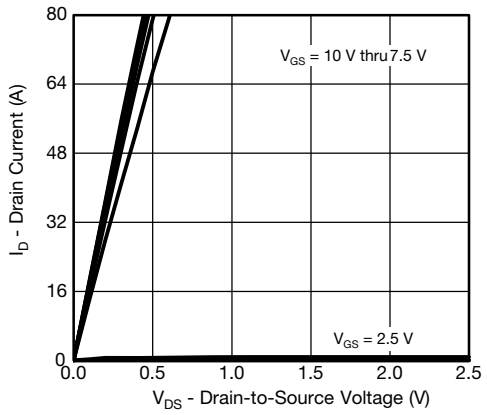
Notes

a.P ulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %

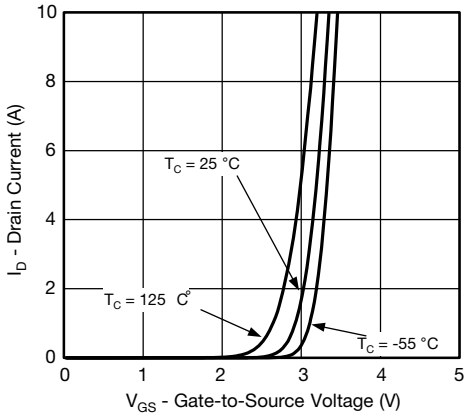
b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

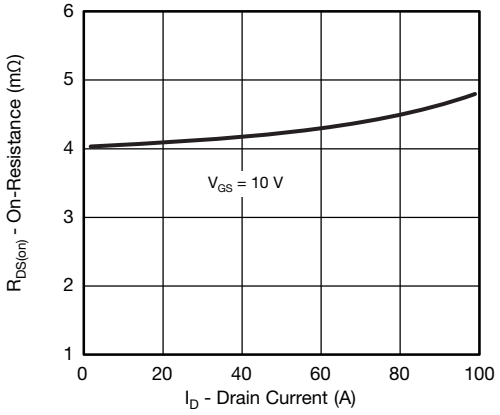
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



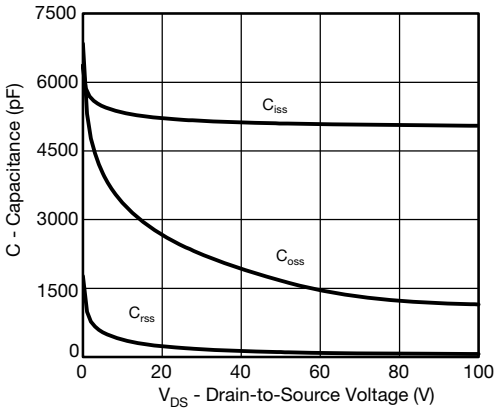
Output Characteristics



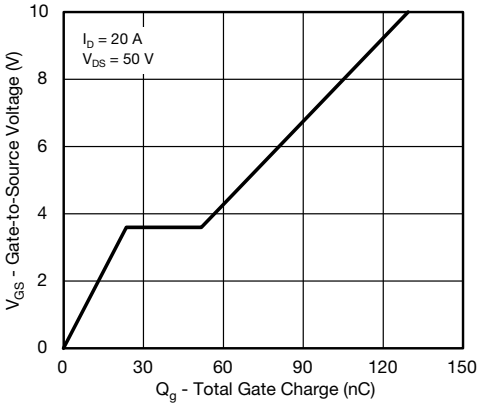
Transfer Characteristics



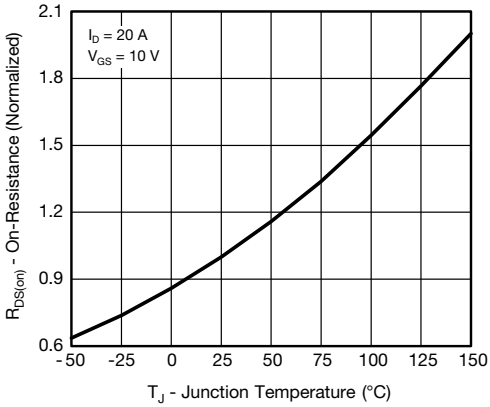
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

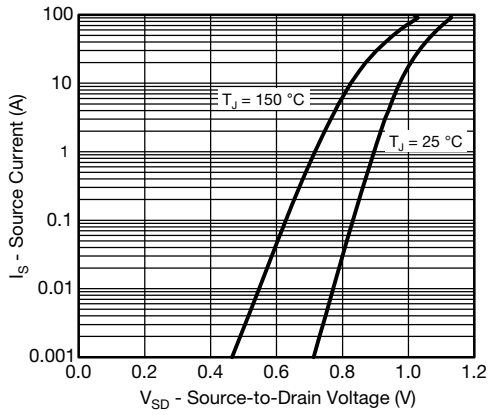


Gate Charge

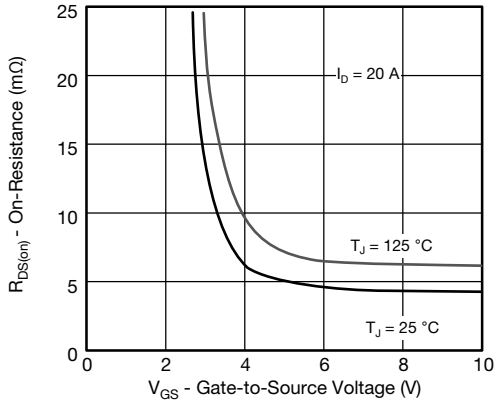


On-Resistance vs. Junction Temperature

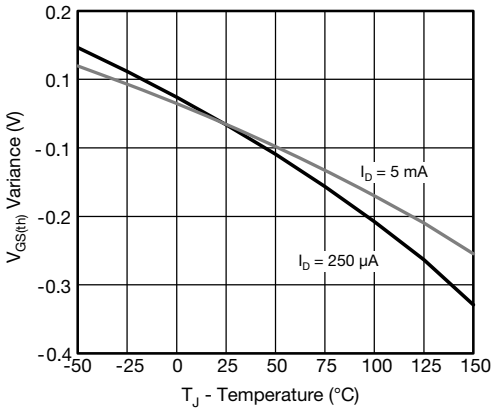
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



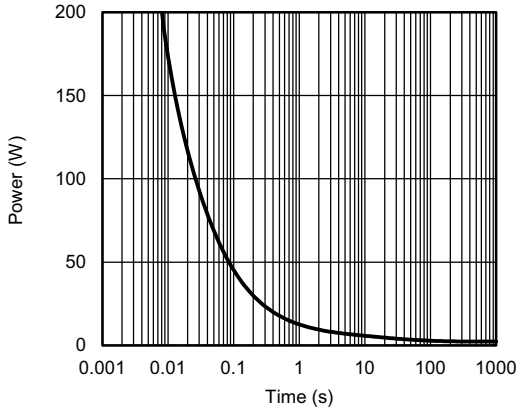
Source-Drain Diode Forward Voltage



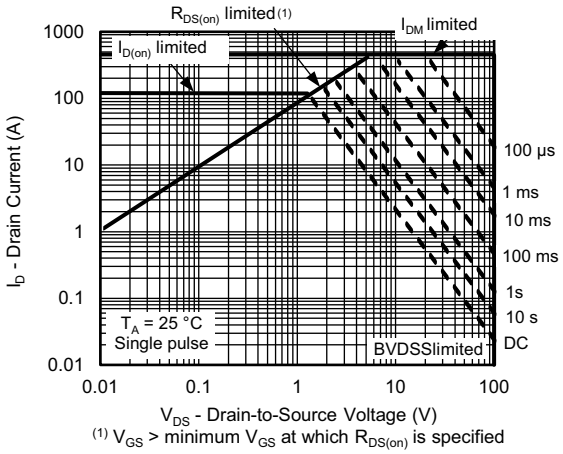
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

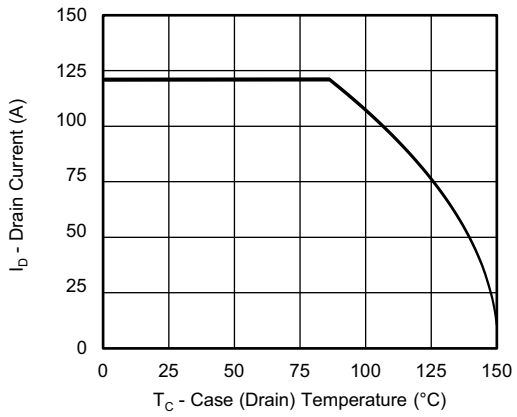


Single Pulse Power, Junction-to-Ambient

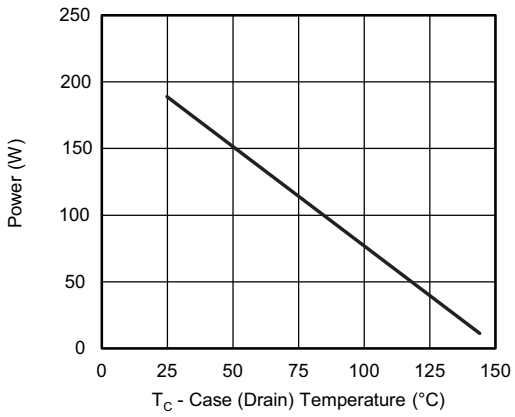


Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



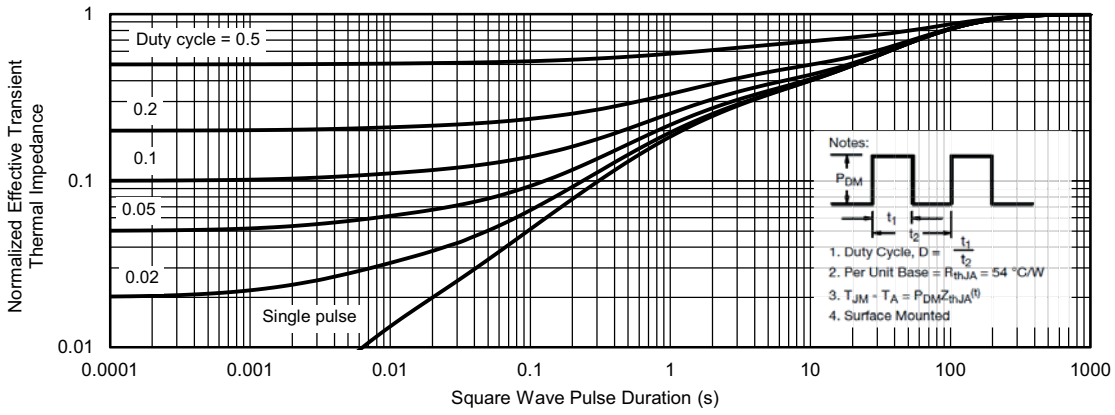
Current Derating [a]



Power, Junction-to-Case

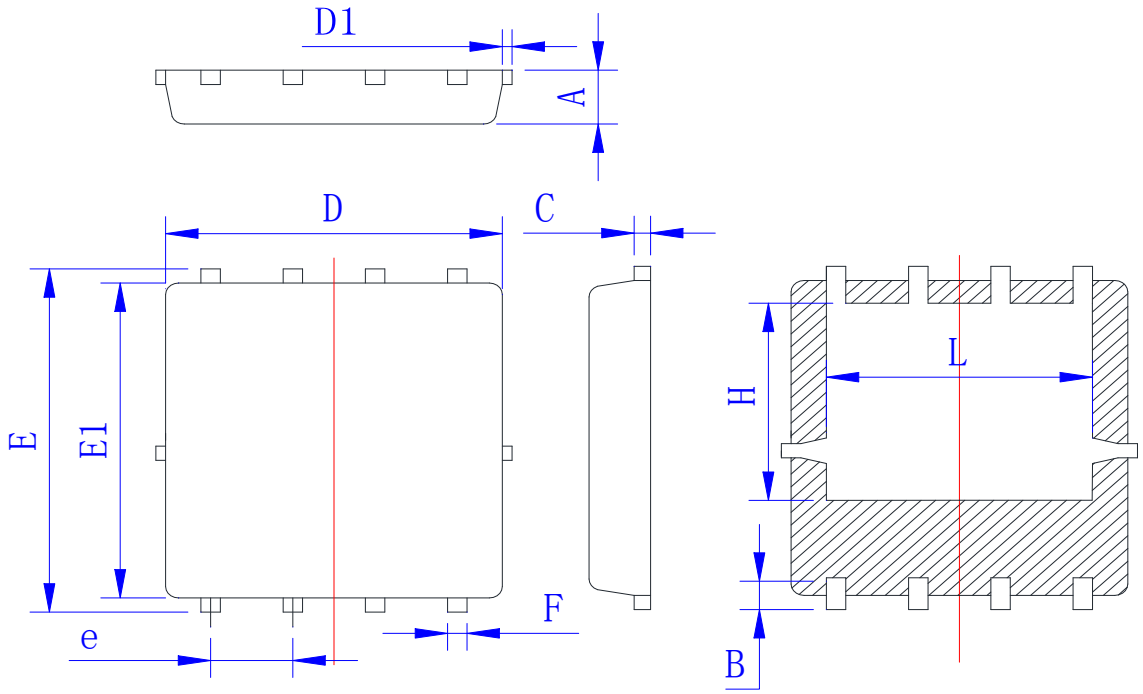
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



Normalized Thermal Transient Impedance, Junction-to-Ambient

DFN5X6_8L PACKAGE OUTLINE



Symbol	Min	Typ	Max
A	0.90	0.95	1.00
B	0.48	0.58	0.68
C	0.20	0.254	0.30
D	5.00	5.20	5.40
D1			0.15
E	5.90	6.05	6.20
E1	5.40	5.55	5.70
e	1.22	1.27	1.32
F	0.25	0.30	0.35
H	3.27	3.47	3.67
L	3.80	4.00	4.20

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