

N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^{a, e}	Q _g (Typ.)
60	0.96 at V _{GS} = 10 V	220	70 nC
	1.55 at V _{GS} = 4.5 V	160	

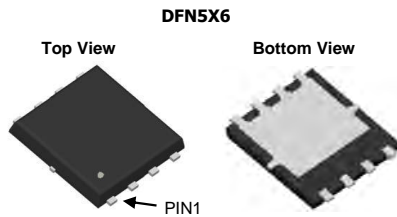
FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

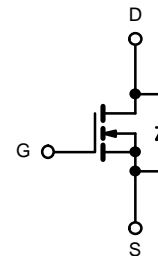
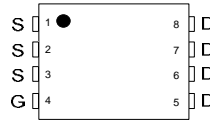


APPLICATIONS

- Notebook PC Core
- VRM/POL



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	60	V	
Gate-Source Voltage	V _{GS}	± 20		
Continuous Drain Current (T _J = 175 °C)	I _D	T _C = 25 °C	220 ^{a, e}	
		T _C = 70 °C	155 ^e	
		T _A = 25 °C	38 ^{b, c}	
		T _A = 70 °C	27 ^{b, c}	
Pulsed Drain Current	I _{DM}	800	A	
Avalanche Current Pulse ^e	L = 0.1 mH	I _{AS}		86
Single Pulse Avalanche Energy	E _{AS}	798		mJ
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	200 ^{a, e}	
		T _A = 25 °C	18 ^{b, c}	
Maximum Power Dissipation	P _D	T _C = 25 °C	298 ^a	
		T _C = 70 °C	208	
		T _A = 25 °C	4.7 ^{b, c}	
		T _A = 70 °C	3.29 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R _{thJA}	10	13	°C/W
Maximum Junction-to-Case	R _{thJC}	0.5	1	

Notes:

- Based on T_C = 25 °C.
- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Calculated based on maximum junction temperature.
- Single pulse width limited by junction temperature T_J(MAX)=150°C.

SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min .	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	60			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		15		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			-6.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1		3	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 60\text{ V}, V_{GS} = 0\text{ V}$			1	μA
		$V_{DS} = 60\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	220			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 50\text{ A}$		0.96	1.2	m Ω
		$V_{GS} = 4.5\text{ V}, I_D = 30\text{ A}$		1.55	1.9	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 5\text{ V}, I_D = 20\text{ A}$		88		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		8900		pF
Output Capacitance	C_{oss}			5100		
Reverse Transfer Capacitance	C_{rss}			115		
Total Gate Charge	Q_g	$V_{DS} = 48\text{ V}, V_{GS} = 10\text{ V}, I_D = 50\text{ A}$		70		nC
Gate-Source Charge	Q_{gs}			28		
Gate-Drain Charge	Q_{gd}			13		
Gate Resistance	R_g	$f = 1\text{ MHz}$		2		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 48\text{ V}, R_L = 2.5\text{ }\Omega$ $I_D \cong 50\text{ A}, V_{GEN} = 10\text{ V}, R_g = 2\text{ }\Omega$		14		ns
Rise Time	t_r			60		
Turn-Off Delay Time	$t_{d(off)}$			56		
Fall Time	t_f			40		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			200	A
Pulse Diode Forward Current ^a	I_{SM}				800	
Body Diode Voltage	V_{SD}	$I_S = 50\text{ A}$		0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 50\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		68		ns
Body Diode Reverse Recovery Charge	Q_{rr}			259		nC
Reverse Recovery Fall Time	t_a			47		ns
Reverse Recovery Rise Time	t_b			55		

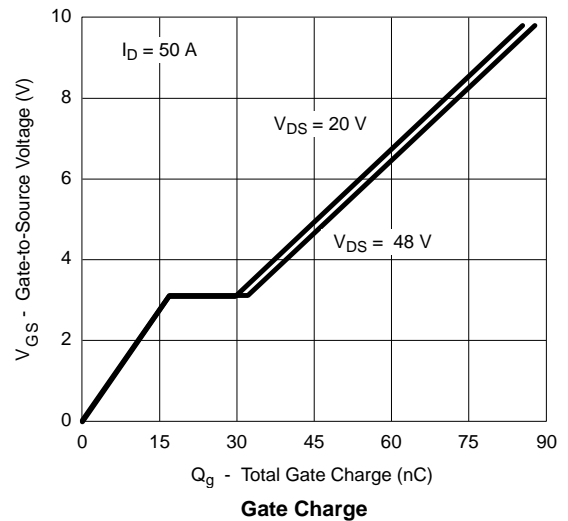
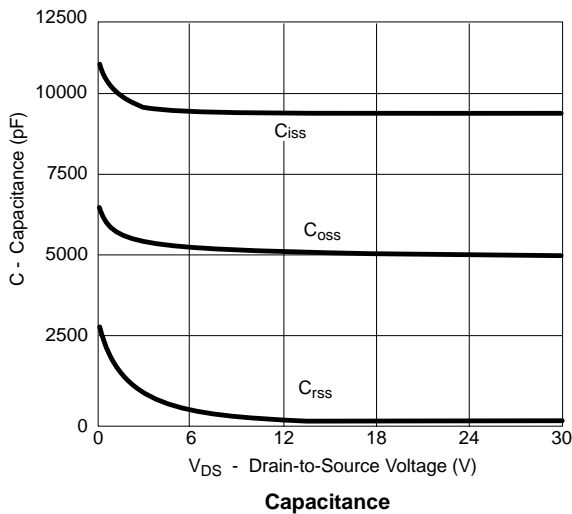
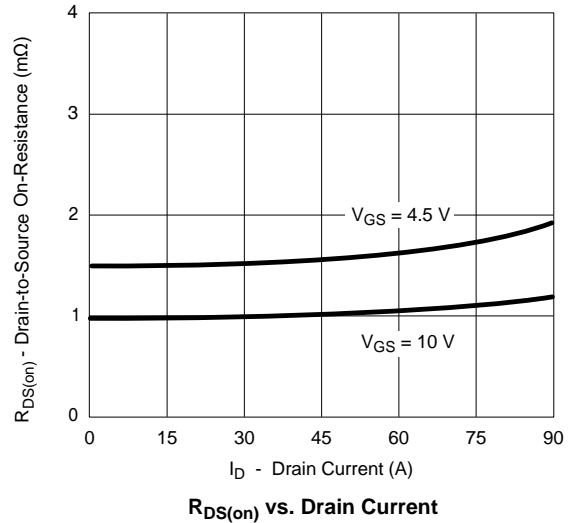
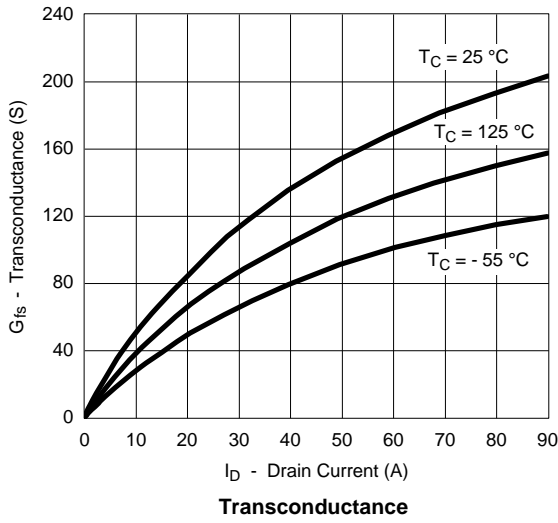
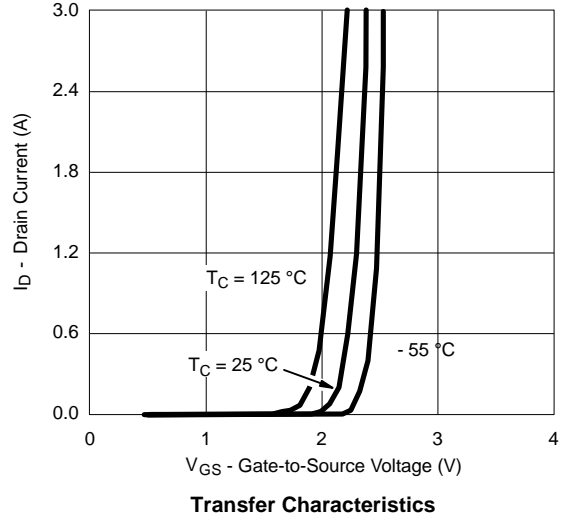
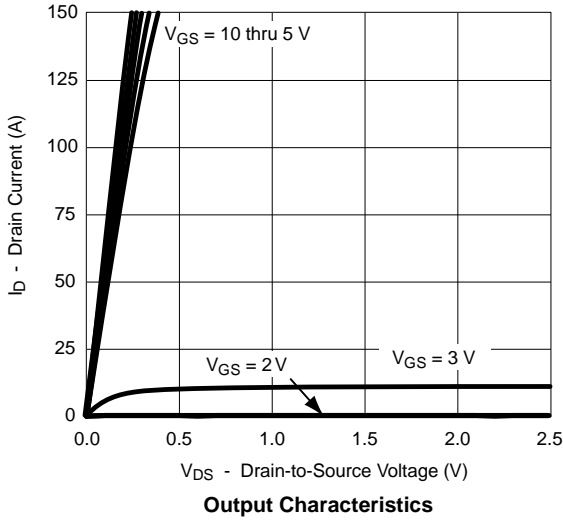
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

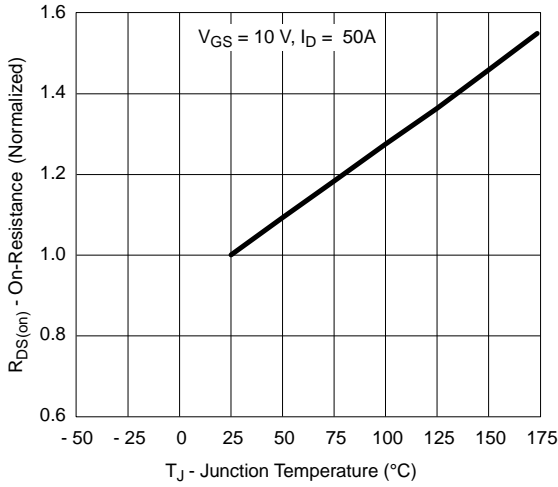
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

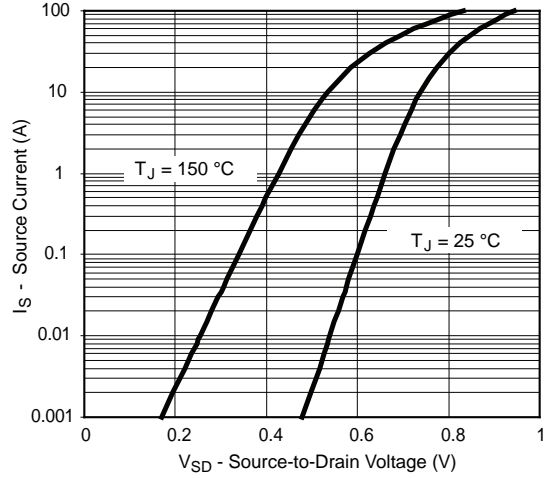
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



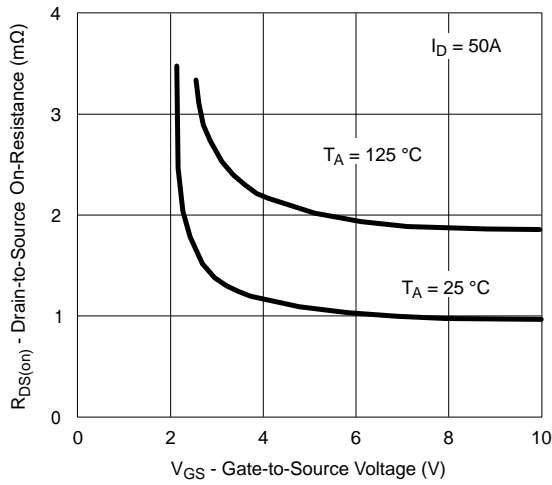
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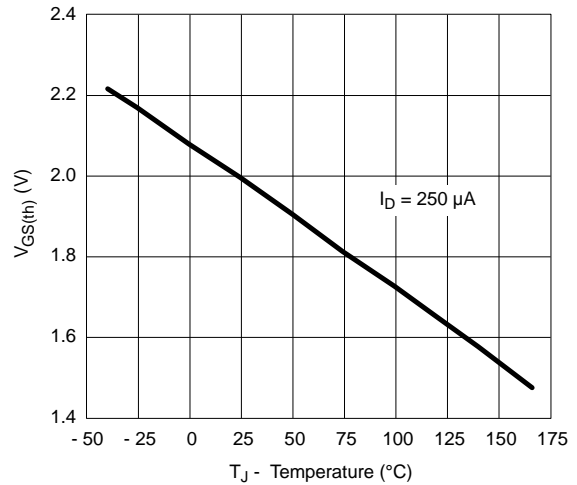
On-Resistance vs. Junction Temperature



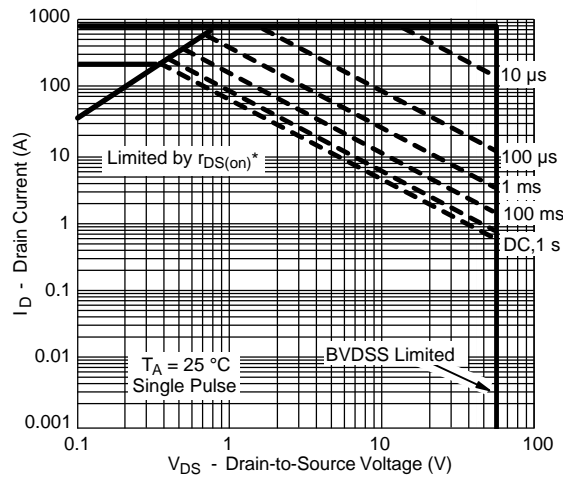
Forward Diode Voltage vs. Temperature



R_{DS(on)} vs. V_{GS} vs. Temperature



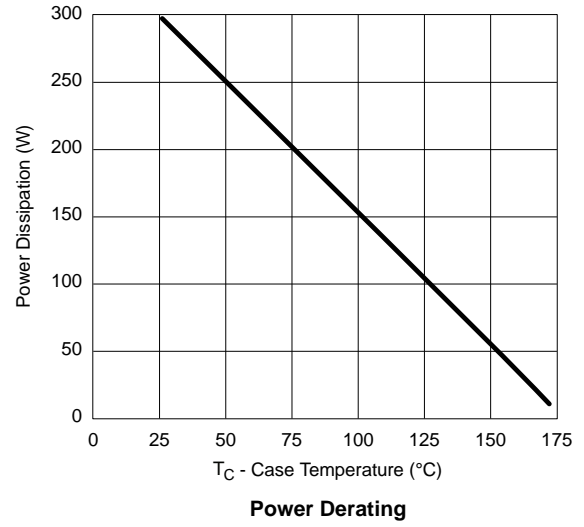
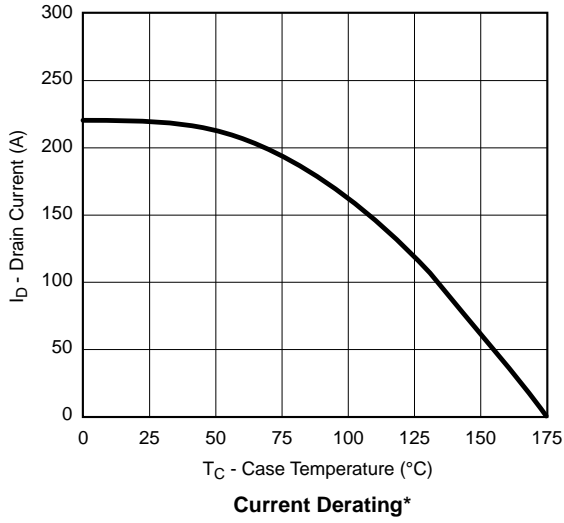
Threshold Voltage



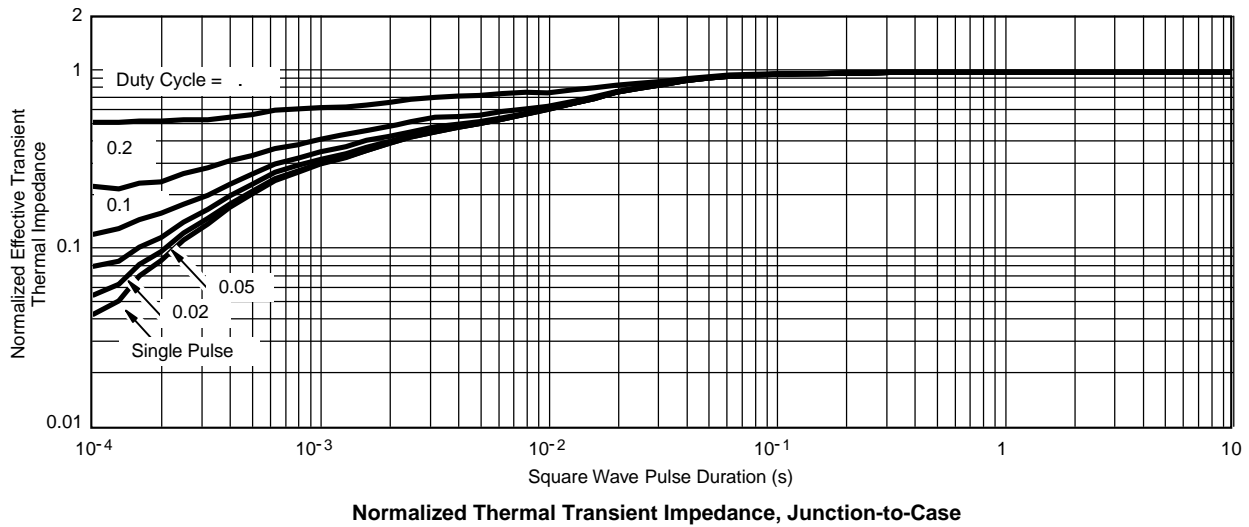
*V_{GS} > minimum V_{GS} at which r_{DS(on)} is specified

Safe Operating Area, Junction-to-Ambient

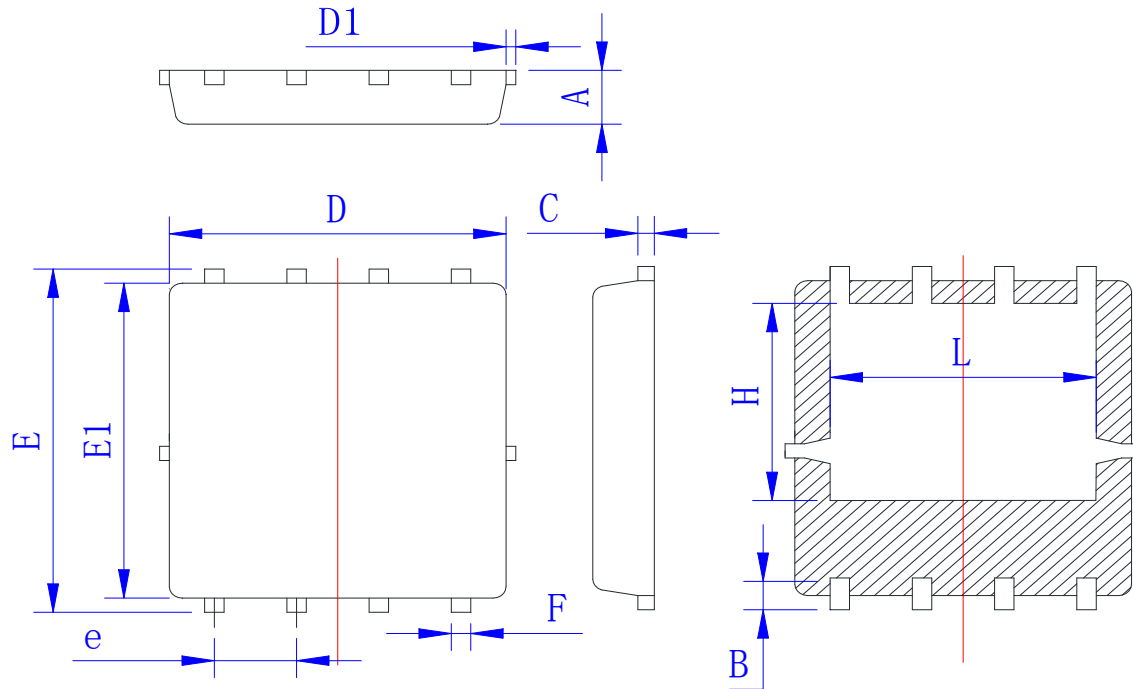
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



* The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



DFN5X6_8L PACKAGE OUTLIN



Symbol	Min	Typ	Max
A	0.90	0.95	1.00
B	0.48	0.58	0.68
C	0.20	0.254	0.30
D	5.00	5.20	5.40
D1			0.15
E	5.90	6.05	6.20
E1	5.40	5.55	5.70
e	1.22	1.27	1.32
F	0.25	0.30	0.35
H	3.27	3.47	3.67
L	3.80	4.00	4.20

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