

P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)
- 60	0.049 at V _{GS} = - 10 V	- 6	7.6 nC
	0.059 at V _{GS} = - 4.5 V	- 5	

FEATURES

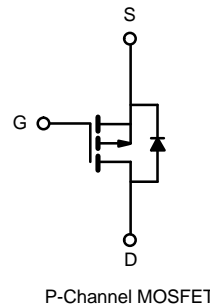
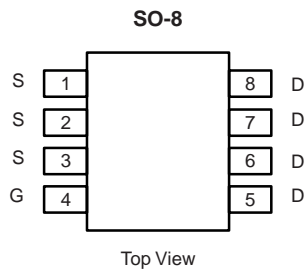
- DT-Trench Power MOSFET
- 100 % UIS Tested

APPLICATIONS

- Load Switch



RoHS
COMPLIANT



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	- 60	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	- 6 ^a
		T _C = 70 °C	- 5.5
		T _A = 25 °C	5.2 ^b
		T _A = 70 °C	- 4.1 ^b
Pulsed Drain Current	I _{DM}	- 18	A
Avalanche Current Pulse	I _{AS}	- 4.5	
Single Pulse Avalanche Energy	E _{AS}	10.1	mJ
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	5.9 ^a
		T _A = 25 °C	3.1 ^b
Maximum Power Dissipation	P _D	T _C = 25 °C	7.4 ^a
		T _C = 70 °C	4.6 ^a
		T _A = 25 °C	2 ^b
		T _A = 70 °C	1.1 ^b
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^b	R _{thJA}	33	40	°C/W
Maximum Junction-to-Case				

Notes:

a. Based on T_C = 25 °C.

b. Surface mounted on 1" x 1" FR4 board.

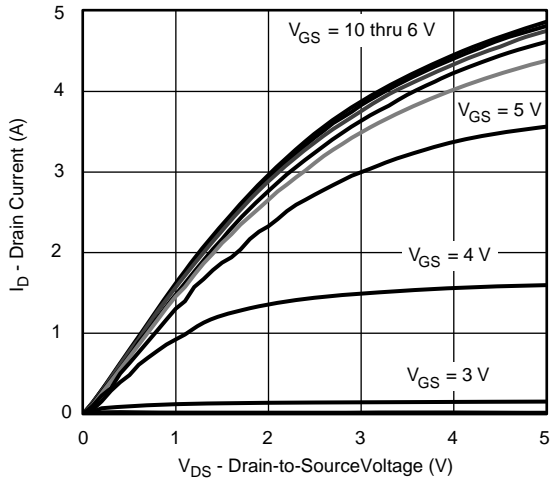
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-60			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = -250\text{ }\mu\text{A}$		68		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			-5.2		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-1		-3	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
		$V_{DS} = -60\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			-10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = -5\text{ V}, V_{GS} = -10\text{ V}$	-12			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -3\text{ A}$		0.049	0.065	Ω
		$V_{GS} = -4.5\text{ V}, I_D = -2\text{ A}$		0.059	0.080	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -15\text{ V}, I_D = -3\text{ A}$	20			S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = -25\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		3100		pF
Output Capacitance	C_{oss}			320		
Reverse Transfer Capacitance	C_{rss}			210		
Total Gate Charge	Q_g	$V_{DS} = -60\text{ V}, V_{GS} = -10\text{ V}, I_D = -3\text{ A}$		46	105	nC
				32	40	
Gate-Source Charge	Q_{gs}	$V_{DS} = -60\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -3\text{ A}$		16		
Gate-Drain Charge	Q_{gd}			19		
Gate Resistance	R_g	$f = 1\text{ MHz}$		5.2		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -2\text{ V}, R_L = 2\text{ }\Omega$ $I_D \cong -3\text{ A}, V_{GEN} = -10\text{ V}, R_g = 1\text{ }\Omega$		10	11	ns
Rise Time	t_r			7	11	
Turn-Off Delay Time	$t_{d(off)}$			70	100	
Fall Time	t_f			40	50	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			-5.9	A
Pulse Diode Forward Current ^a	I_{SM}				-18	
Body Diode Voltage	V_{SD}	$I_S = -3\text{ A}$		-1	-1.5	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -3\text{ A}, di/dt = 10\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		45	68	ns
Body Diode Reverse Recovery Charge	Q_{rr}			59	120	nC
Reverse Recovery Fall Time	t_a			29		ns
Reverse Recovery Rise Time	t_b			16		

Notes:

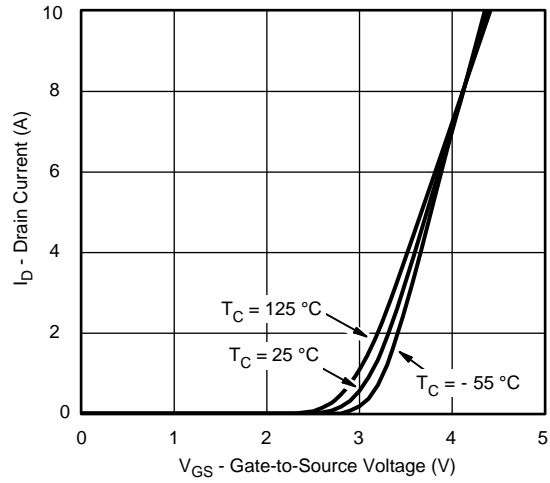
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



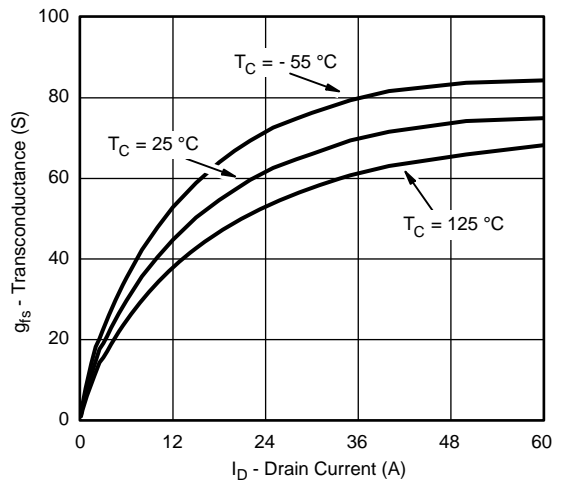
Output Characteristics



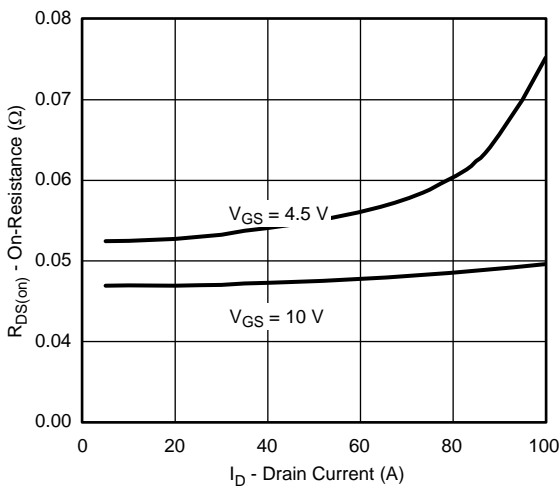
Transfer Characteristics



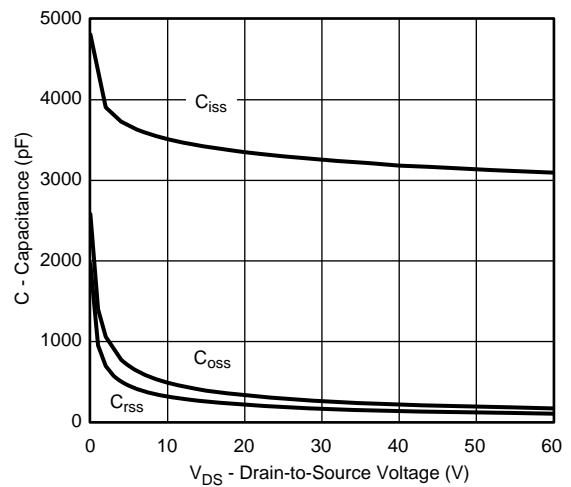
Transfer Characteristics



Transconductance

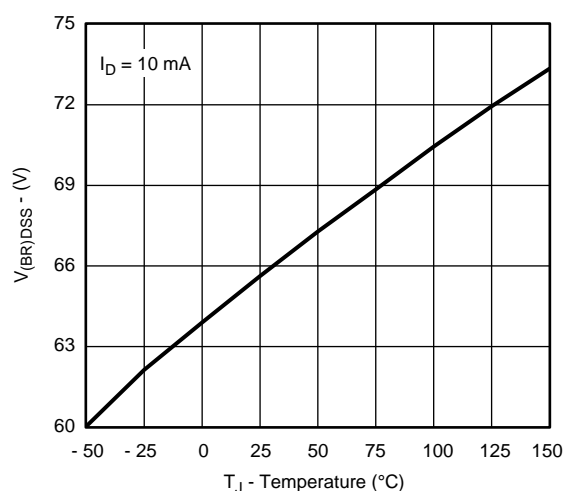
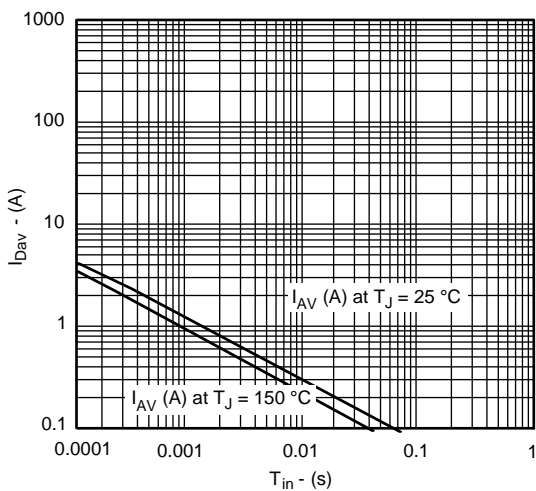
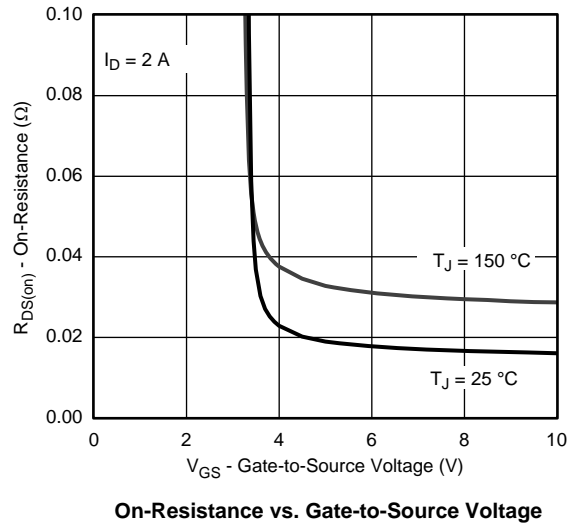
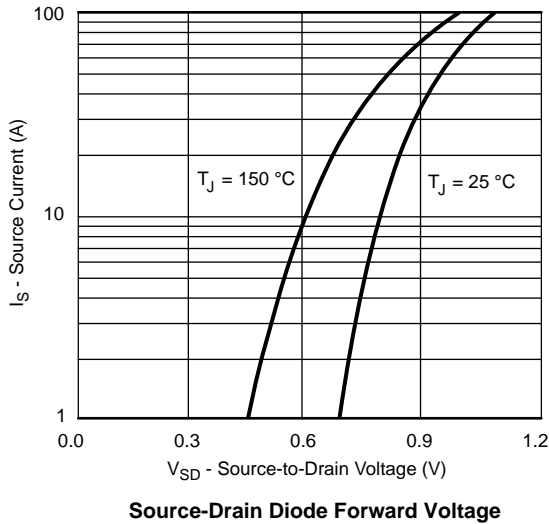
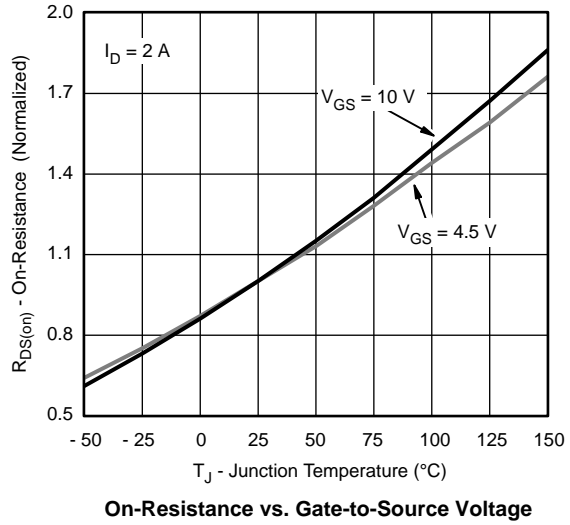
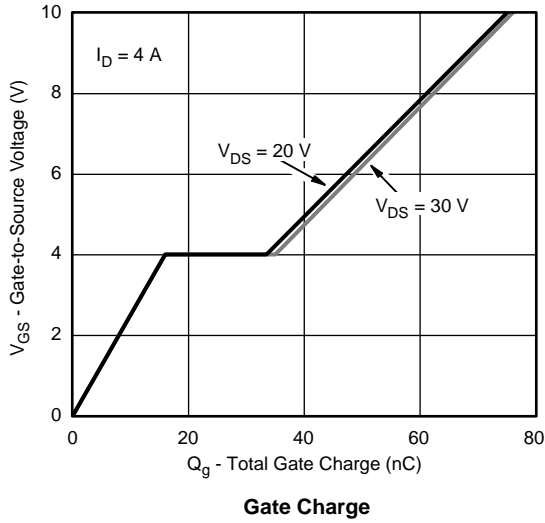


On-Resistance vs. Drain Current



Capacitance

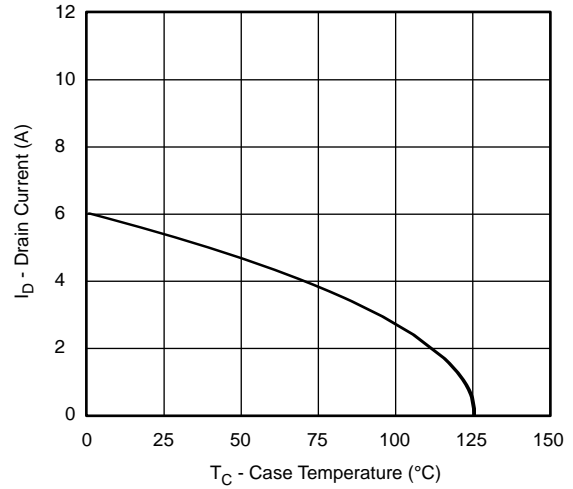
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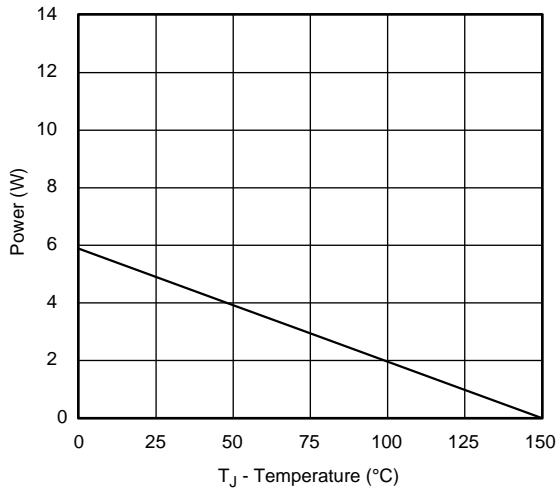
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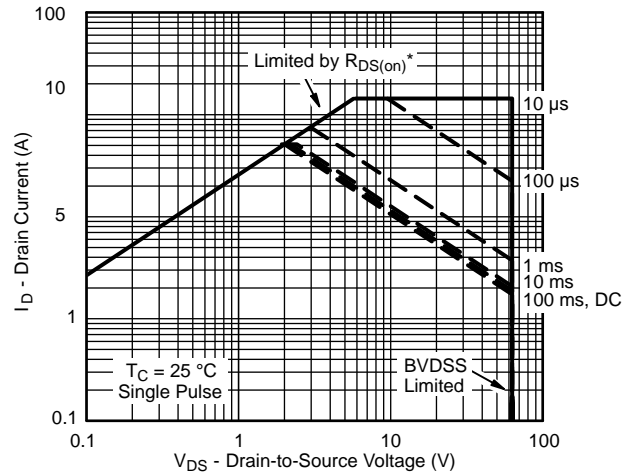
Threshold Voltage



Max. Drain Current vs. Case Temperature



Power Derating, Junction-to-Case

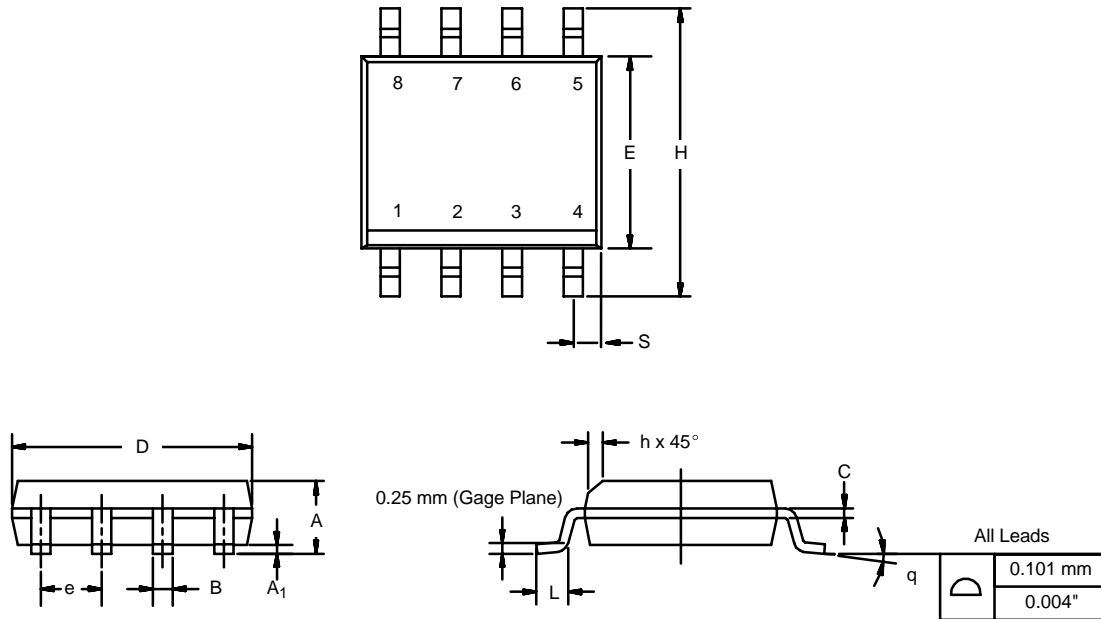


Safe Operating Area, Junction-to-Case
* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified



Normalized Thermal Transient Impedance, Junction-to-Case

SOIC (NARROW): 8-LEAD
JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads
Dimensions in Inches/(mm)

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