

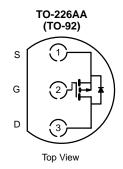
# P-Channel 20-V (D-S) MOSFET

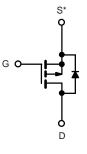
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)			
- 20	0.045 at $V_{GS}$ = - 4.5 V	- 4.2			
	0.072 at V <sub>GS</sub> = - 2.5 V	- 3.6			

#### **FEATURES**

- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested







P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25 \text{ °C}$ , unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 20		V	
Gate-Source Voltage		V <sub>GS</sub>	± 12			
Continuous Drain Current (T 150 °C)	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 4.2	- 4.0		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 3.9	- 3.2		
Pulsed Drain Current (10 µs Pulse Width)		I <sub>DM</sub>	- 20		A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	- 1.35	- 0.95		
Mariana Dissisati sa	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.5	1.05	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.0	0.67		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Mauianum lunation to Ambienta	t ≤ 10 s	- R <sub>thJA</sub>	65	83	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		100	120	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	43	52	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted									
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit			
Static									
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	- 0.6		- 1.5	V			
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ± 8 V			± 100	nA			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -12 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = -12 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 70 \text{ °C}$			- 1	μA			
					- 25				
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS}$ = - 5 V, $V_{GS}$ = - 4.5 V	- 20			А			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -4.2 \text{ A}$		0.032	0.045				
		V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 3.6 A		0.053	0.072	Ω			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 4.2 A		14		S			
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = - 1.35 A, V <sub>GS</sub> = 0 V		- 0.77	- 1.1	V			
Dynamic <sup>b</sup>									
Total Gate Charge	Qg			10	15				
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 6 V, $V_{GS}$ = - 4.5 V, $I_{D}$ = - 4.2 A		1.8		nC			
Gate-Drain Charge	Q <sub>gd</sub>			3					
Gate Resistance	Rg	f = 1 MHz		7.7		Ω			
Turn-On Delay Time	t <sub>d(on)</sub>			45	70				
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 6 V, $R_L$ = 6 $\Omega$		60	90				
Turn-Off Delay Time	t <sub>d(off)</sub>	$\rm I_D$ $\cong$ - 1 A, $\rm V_{GEN}$ = - 4.5 V, $\rm R_g$ = 6 $\Omega$		70	110	ns			
Fall Time	t <sub>f</sub>			35	55				
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.35 A, di/dt = 100 A/μs		65					

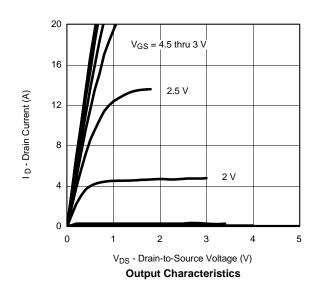
Notes:

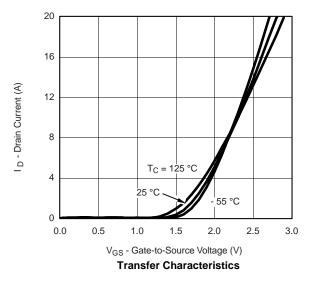
a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



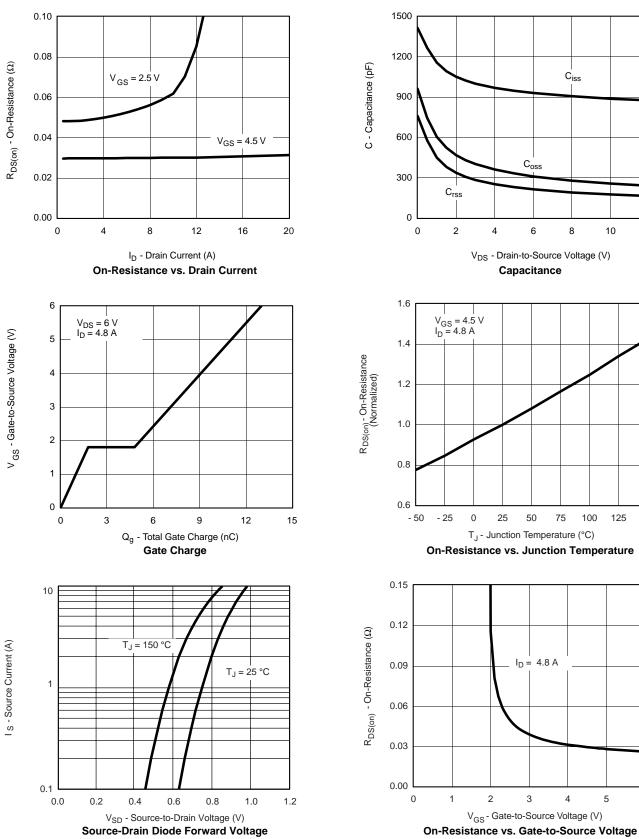


# **DTE2311**

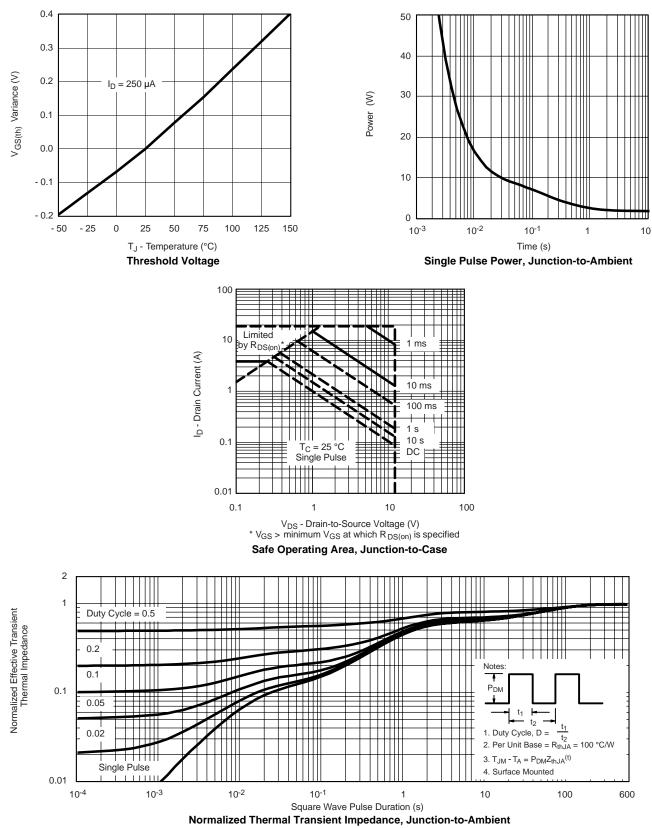
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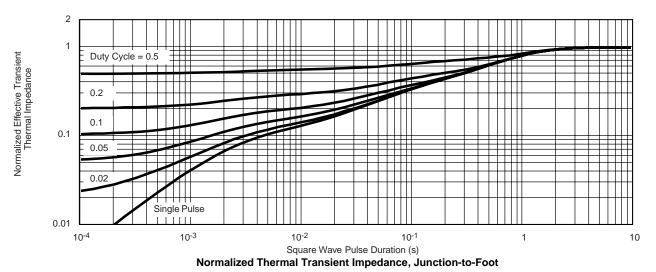


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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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