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P-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY							
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)				
- 60	5.7 at V _{GS} = - 10 V	- 110	142 nC				
	6.5 at V _{GS} = - 6 V	- 110					

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested



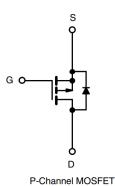
APPLICATIONS

Load Switch

TO-263 Pin Configuration



Top View



ABSOLUTE MAXIMUM RATINGS ($T_C =$	25 °C, unless otherw	vise noted)		
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V _{DS}	- 60	V	
Gate-Source Voltage	V _{GS}	± 20	7 v	
Continuous Drain Current	T _C = 25 °C		- 110	
$(T_J = 150 ^{\circ}\text{C})$	T _C = 100 °C	I _D	- 73	T .
Pulsed Drain Current	I _{DM}	- 330	- A	
Avalanche Current	L = 0.1 mH		- 70	
Single Pulse Avalanche Energy ^a	L = 0.1 IIII	E _{AS}	253	mJ
Power Discipation	T _C = 25 °C °C	В	298	w
Power Dissipation	T _A = 25 °C b	P_{D}	2.05	¬
Operating Junction and Storage Temperature Range	T _J , T _{sta}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS							
PARAMETER	SYMBOL	TYPICAL	UNIT				
Junction-to-Ambient	PCB mount ^b	R _{thJA}	60	°C/W			
Junction-to-Case		R _{thJC}	0.42	C/VV			

Notes

- a. Duty cycle ≤ 1 %.
- b. When mounted on 1" square PCB (FR4 material).
- c. See SOA curve for voltage derating.

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Parameter	Symbol Test Conditions			Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			V	
V _{DS} Temperature Coefficient	ΔV _{DS} /T	I _D = - 250 μA		48		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 5.2		lillv/ C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Drain Current	1	V _{DS} = - 60 V, V _{GS} = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	DSS	V _{DS} = - 48 V, V _{GS} = 0 V, T _J = 55 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 110			Α	
	В	V _{GS} = - 10 V, I _D = - 30 A		5.7	7.5	mΩ	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 6 V, I _D = - 30 A		6.5	8.6		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 30 A		80		S	
Dynamic ^b							
Input Capacitance	C _{iss}			6180		pF	
Output Capacitance	C _{oss}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1003			
Reverse Transfer Capacitance	C _{rss}			20			
Total Gate Charge	Qg			142			
Gate-Source Charge	Q_{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -30 \text{ A}$		16		nC	
Gate-Drain Charge	Q _{gd}			23			
Gate Resistance	R _g	f = 1 MHz		8		Ω	
Turn-On Delay Time	t _{d(on)}			20			
Rise Time	t _r	$V_{DD} = -30 \text{ V}, I_{D} = -30 \text{ A},$		85]	
Turn-Off Delay Time	t _{d(off)}	$V_{GEN} = -10 \text{ V}, \text{ Rg} = 2.5 \Omega$		140		ns	
Fall Time	t _f			116		1	
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 110	Α	
Pulse Diode Forward Current ^a	I _{SM}				- 330		
Body Diode Voltage	V _{SD}	I _S = - 1 A			- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			95		ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 30 A, di/dt = 100 A/μs, T _{.I} = 25 °C		59		nC	
Reverse Recovery Fall Time	t _a	$I_{\rm F} = -30$ A, $u/ut = 100$ A/ μ s, $I_{\rm J} = 25$ °C		29		T	
Reverse Recovery Rise Time	t _b			16		ns	

Notes:

- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

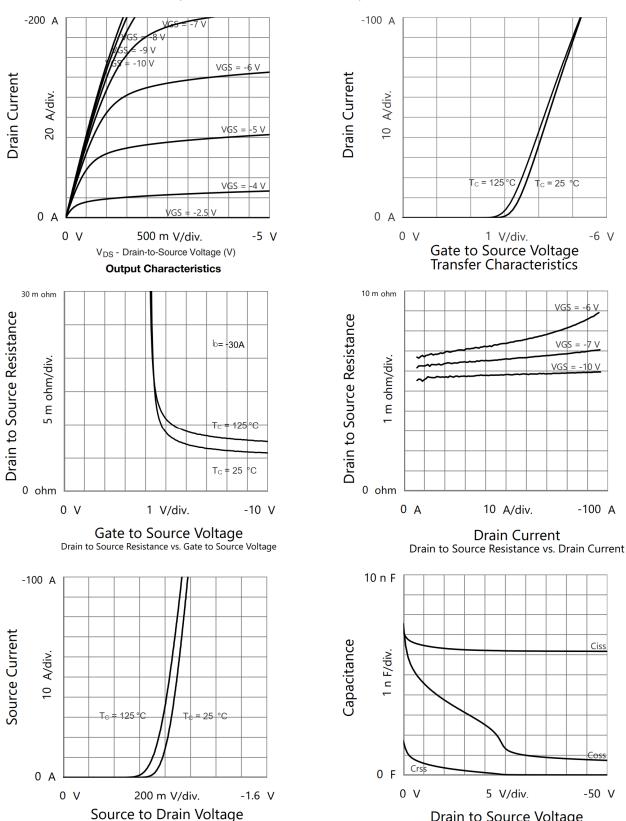
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Body Diode Forward Characteristics

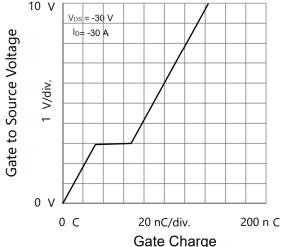


Drain to Source Voltage Capacitances

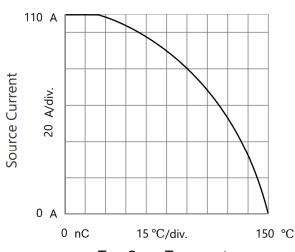




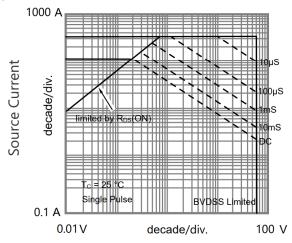
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



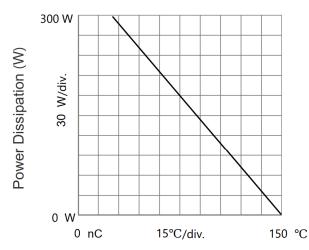
Gate to Source Voltage vs. GateCharge



 $T_C \textbf{ - Case Temperature}_{\text{Current Derating}}$

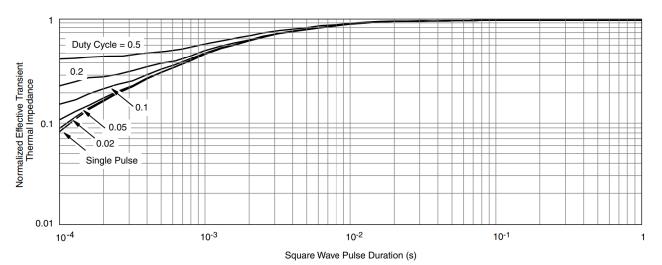


Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



T_C - Case Temperature

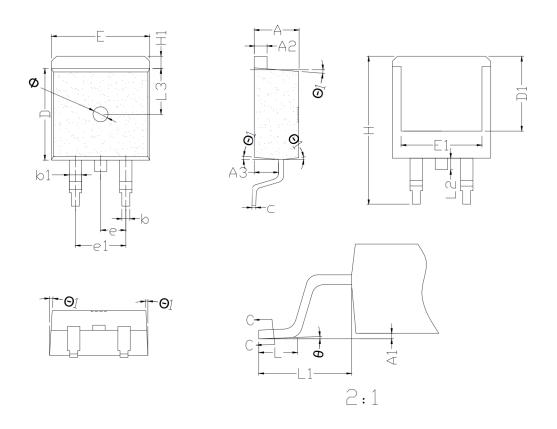
Current Derating



Normalized Thermal Transient Impedance, Junction-to-Case



TO-263 PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX	SYMBOL	MIN	TYP	MAX
Α	4.10	4.50	4.80	е	2.35	2.54	2.75
A1	0.00	0.10	0.30	e1	5.08REF		
A2	1.10	1.30	1.50	Н	14.50 15.15 16.00		
A3	2.15	2.50	3.10	H1	1.00	1.28	1.75
b	0.60	0.80	1.05	L	1.80	2.23	2.90
b1	1.05	1.33	1.50	L1	4.30	4.75	5.50
С	0.33	0.50	0.66	L2	1.00	1.30	1.85
D	8.40	9.20	9.60	L3	0.90	4.65	9.00
D1	7.50REF			ф	0°	2°	5°
E	9.60	10.02	10.80	φ1	2°	-	7°
E1	7.60	9.88	10.30	Φ	1.5BSC		



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