

N-Channel 650 V (D-S) MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)
650	700 at V _{GS} = 10 V	12	37.2nC

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- High Input Resistance
- Excellent FoM (figure of merit)

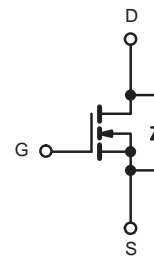
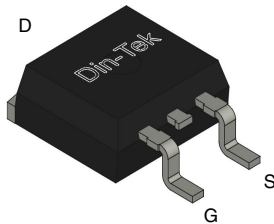


RoHS
COMPLIANT

APPLICATIONS

- Electronic Ballast
- Electronic Transformer
- Switch Mode Power Supply

TO-263 Pin Configuration
Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	650	V
Gate-Source Voltage	V _{GS}	± 30	
Continuous Drain Current (T _J = 150 °C) ^a	I _D	T _C = 25 °C	12
		T _C = 100 °C	7.7
Pulsed Drain Current ^b	I _{DM}	48	A
Single Avalanche Energy	E _{AS}	600	mJ
Maximum Power Dissipation ^c	P _D	T _C = 25 °C	126
		T _C = 100 °C	50.5
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to+150	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	62.5	°C/W
Junction-to-Case (Drain)	R _{thJC}	0.99	

Notes

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- P_d is based on max. junction temperature, using junction-case thermal resistance.
- The value of R_{thJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.

SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	650	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2.0	-	4.0	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 30\text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	μA
		$V_{DS} = 520\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	12	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 5.0\text{ A}$	-	700	900	m Ω
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15\text{ V}, I_D = 5.0\text{ A}$	-	12.8	-	S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 25\text{ V}, f = 1\text{ MHz}$	-	1700	-	pF
Output Capacitance	C_{oss}		-	178	-	
Reverse Transfer Capacitance	C_{rss}		-	8	-	
Total Gate Charge ^c	Q_g	$V_{DS} = 520\text{ V}, V_{GS} = 10\text{ V}, I_D = 10\text{ A}$	-	37.2	-	nC
Gate-Source Charge ^c	Q_{gs}		-	7.6	-	
Gate-Drain Charge ^c	Q_{gd}		-	12.2	-	
Gate Resistance	R_g	$f = 1\text{ MHz}$	-	5.4	-	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 325\text{ V}, I_D = 10\text{ A}, R_g = 25\Omega$	-	18	-	ns
Rise Time ^c	t_r		-	36	-	
Turn-Off Delay Time ^c	$t_{d(off)}$		-	50	-	
Fall Time ^c	t_f		-	30	-	
Drain-Source Body Diode Ratings and Characteristics ^b ($T_J = 25\text{ }^\circ\text{C}$)						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$	-	-	12	A
Pulsed Current	I_{SM}		-	-	48	A
Forward Voltage ^a	V_{SD}	$I_F = 10\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.4	V
Reverse Recovery Time	t_{rr}	$I_F = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$	-	420	-	ns
Reverse Recovery Charge	Q_{rr}		-	4.2	-	μC

Notes

- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARAC TERISTICS (25 °C, unless otherwise noted)

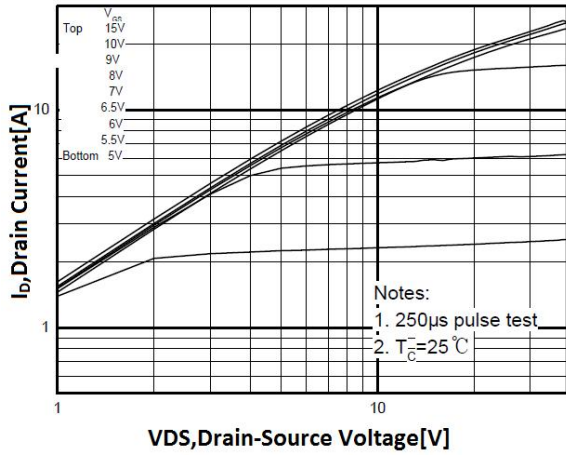


Fig1 Typical Output Characteristics, Tc=25°C

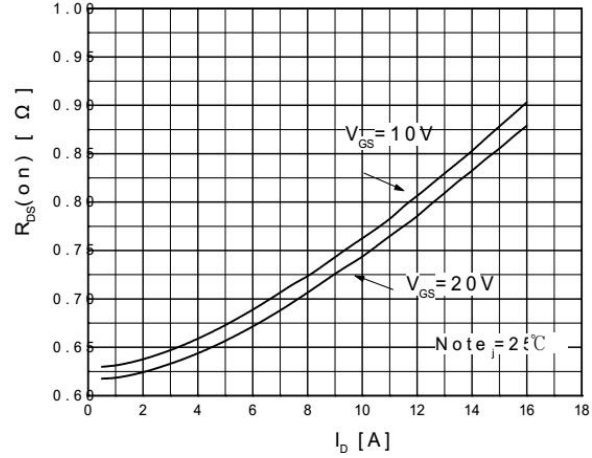


Fig2 On-Resistance Vs. Drain Current and Gate Voltage

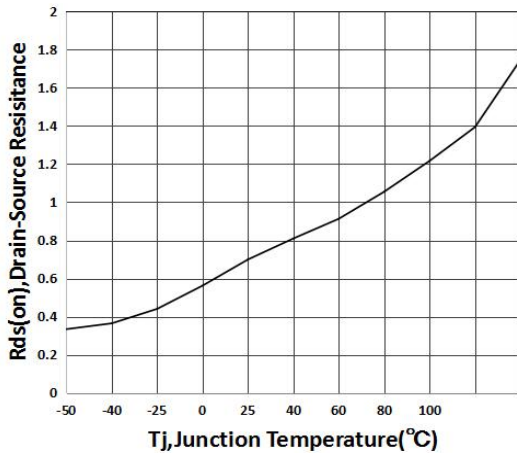


Fig3 On-Resistance Vs. Temperature

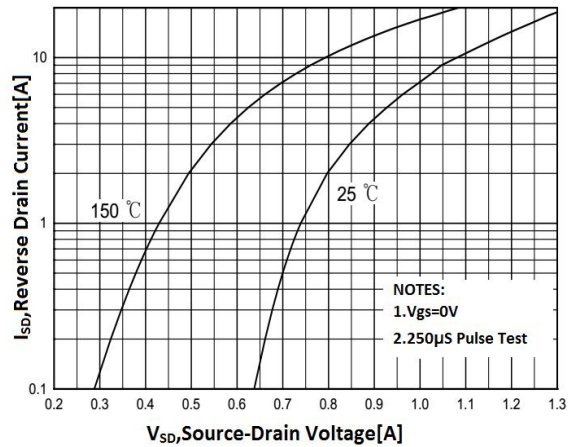


Fig4 Typical Source-Drain Diode Forward Voltage

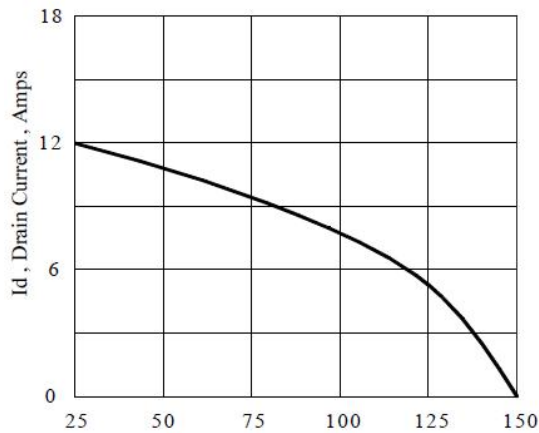


Fig5 Maximum Drain Current Vs. Case Temperature

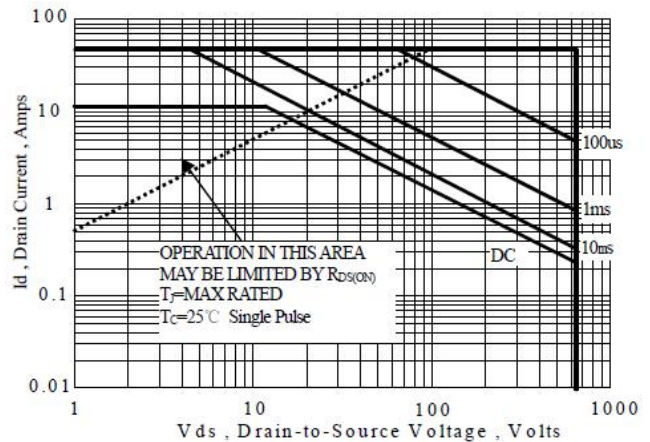
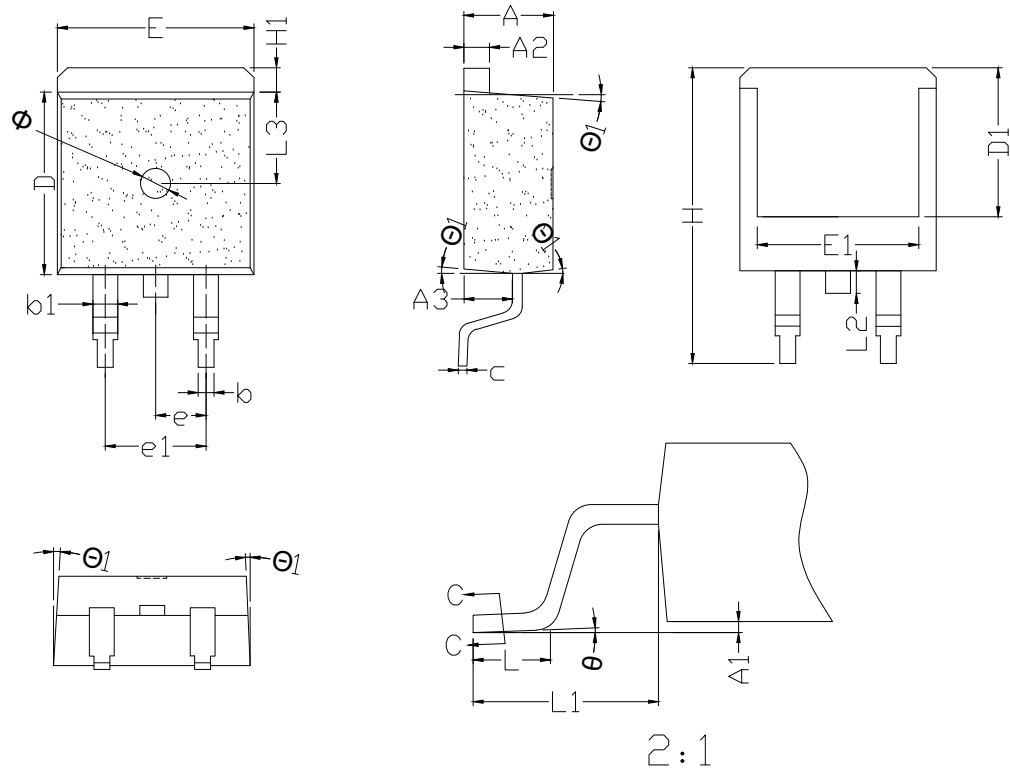


Fig6 Maximum Safe Operating Area

TO-263 PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX	SYMBOL	MIN	TYP	MAX
A	4.10	4.50	4.80	e	2.35	2.54	2.75
A1	0.00	0.10	0.30	e1	5.08REF		
A2	1.10	1.30	1.50	H	14.50	15.15	16.00
A3	2.15	2.50	3.10	H1	1.00	1.28	1.75
b	0.60	0.80	1.05	L	1.80	2.23	2.90
b1	1.05	1.33	1.50	L1	4.30	4.75	5.50
c	0.33	0.50	0.66	L2	1.00	1.30	1.85
D	8.40	9.20	9.60	L3	0.90	4.65	9.00
D1	7.50REF			phi	0°	2°	5°
E	9.60	10.02	10.80	phi1	2°	-	7°
E1	7.60	9.88	10.30	Phi	1.5BSC		

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