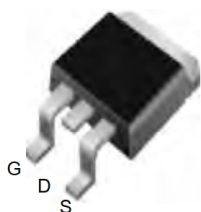


N-Channel 100 V (D-S) MOSFET

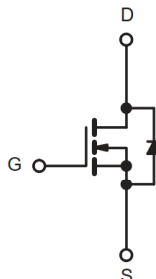
PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (m Ω)(TYP.)	I_D (A)	Q_g (TYP.)
100	3.2at $V_{GS} = 10$ V	185	83.4 nC

D²PAK
(TO-263)



Top View



N-Channel MOSFET

FEATURES

- Super Trench III
- Maximum 175 °C junction temperature
- 100 % R_g and UIS tested



RoHS
COMPLIANT

APPLICATIONS

- Power supplies:
 - Uninterruptible power supplies
 - AC/DC switch-mode power supplies
 - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Battery management

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	I_D	$T_C = 25$ °C	A
		$T_C = 100$ °C	
Pulsed Drain Current ($t = 100$ μ s)	I_{DM}	702	
Avalanche Current	I_{AS}	180	
Single Avalanche Energy ^a	E_{AS}	970	mJ
Maximum Power Dissipation ^a	P_D	$T_C = 25$ °C	W
		$T_C = 125$ °C	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^c	R_{thJA}	33	°C/W
Junction-to-Case (Drain)	R_{thJC}	0.7	

Notes

- Duty cycle ≤ 1 %.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR4 material).

SPECIFICATIONS ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

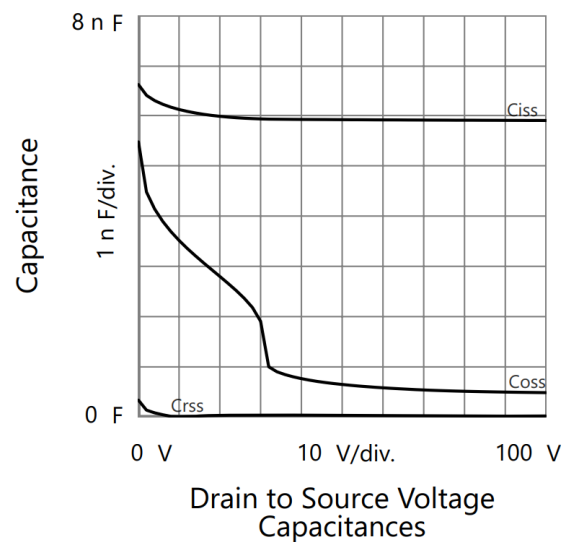
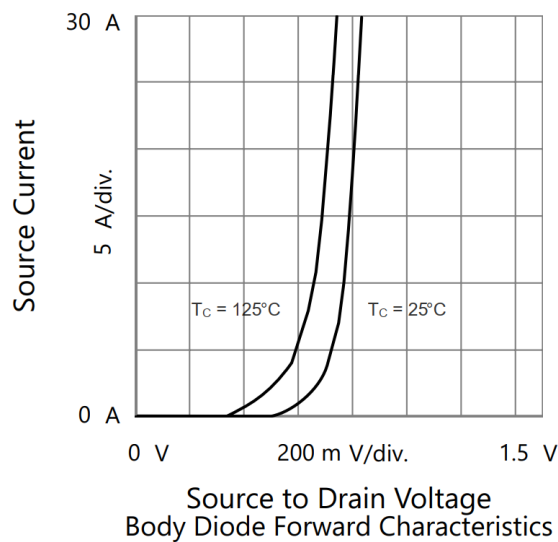
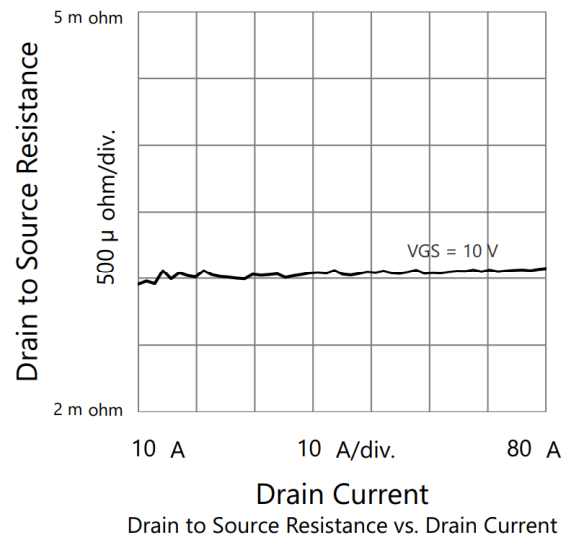
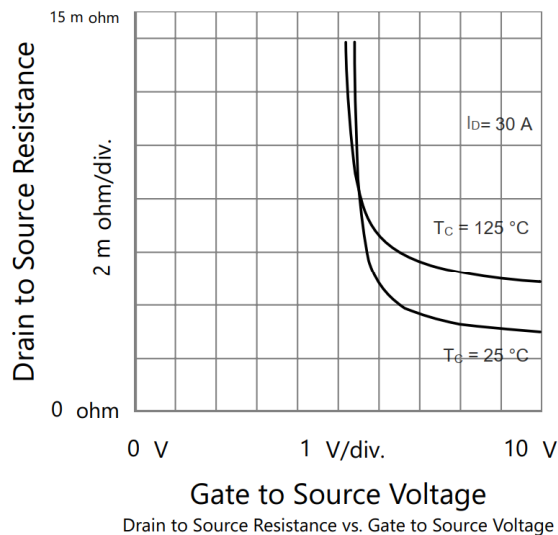
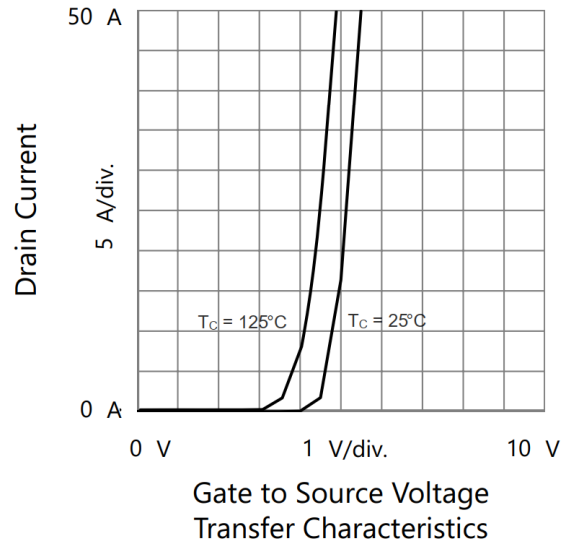
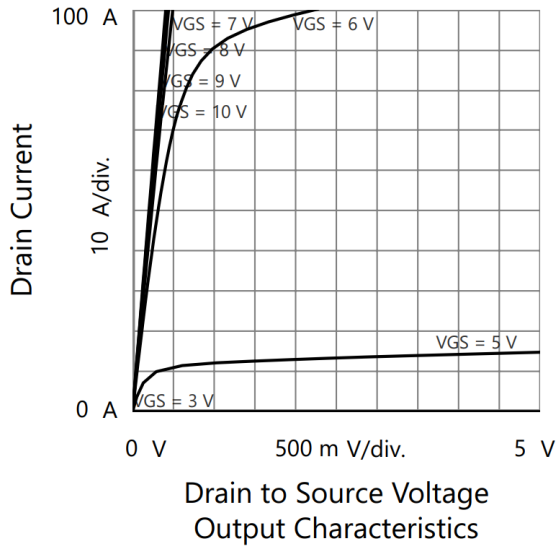
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2	-	4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	-	-	1	μA
		V _{DS} =80 V, V _{GS} = 0 V, T _J = 125 °C	-	-	100	
		V _{DS} =80 V, V _{GS} = 0 V, T _J = 175 °C	-	-	2	mA
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 30 A	-	3.2	3.8	mΩ
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 65 A	-	92	-	S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 50 V, f = 1 MHz	-	5920	-	pF
Output Capacitance	C _{oss}		-	650	-	
Reverse Transfer Capacitance	C _{rss}		-	43	-	
Total Gate Charge ^c	Q _g	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 30 A	-	83.4	-	nC
Gate-Source Charge ^c	Q _{gs}		-	36.5	-	
Gate-Drain Charge ^c	Q _{gd}		-	33.5	-	
Gate Resistance	R _g	f = 1 MHz	-	2.1	-	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 50 V, R _L = 1.67 Ω I _D ≅ 65 A, V _{GEN} = 10 V, R _g = 1 Ω	-	20	-	ns
Rise Time ^c	t _r		-	11.5	-	
Turn-Off Delay Time ^c	t _{d(off)}		-	48	-	
Fall Time ^c	t _f		-	9	-	
Drain-Source Body Diode Ratings and Characteristics ^b (T _C = 25 °C)						
Pulsed Current (t = 100 μs)	I _{SM}		-	-	702	A
Forward Voltage ^a	V _{SD}	I _F = 65 A, V _{GS} = 0 V	-	-	1.2	V
Reverse Recovery Time	t _{rr}	I _F = 65 A, di/dt = 100 A/μs	-	78	-	ns
Peak Reverse Recovery Charge	I _{RM(REC)}		-	5	-	A
Reverse Recovery Charge	Q _{rr}		-	150	-	nC

Notes

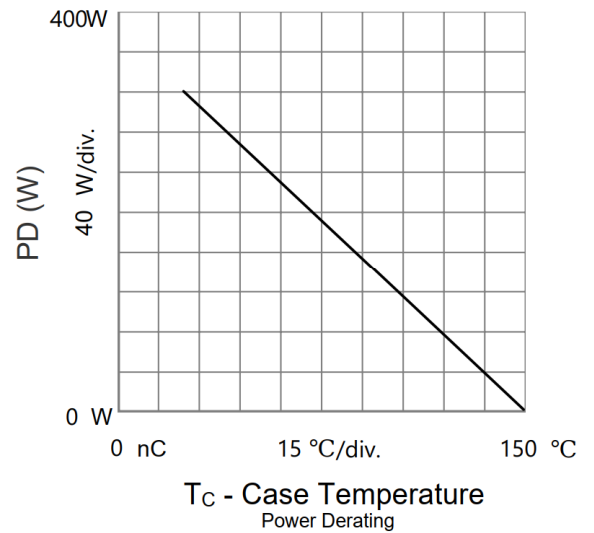
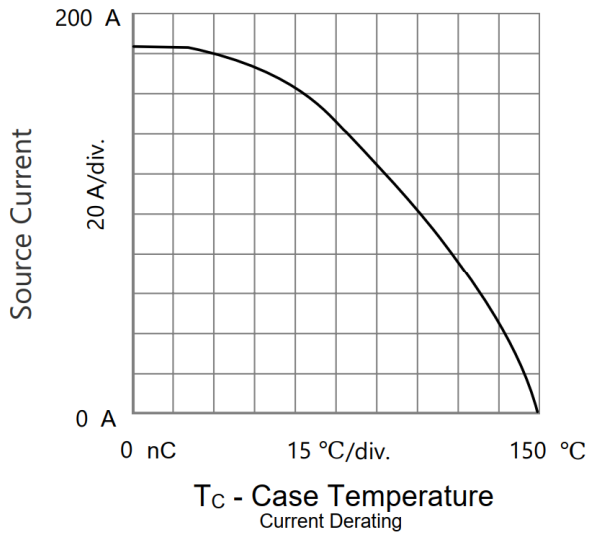
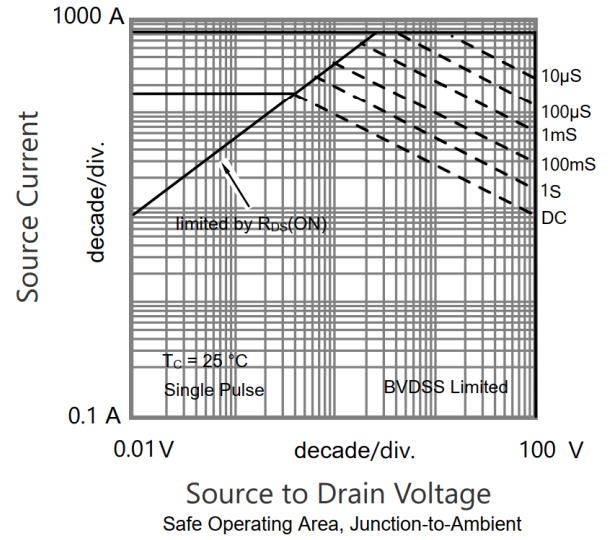
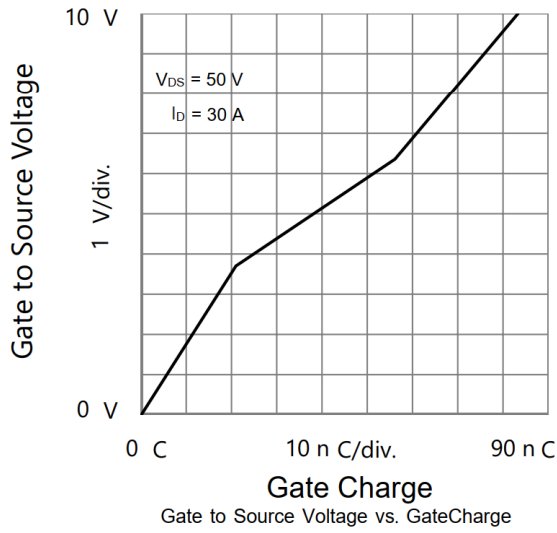
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

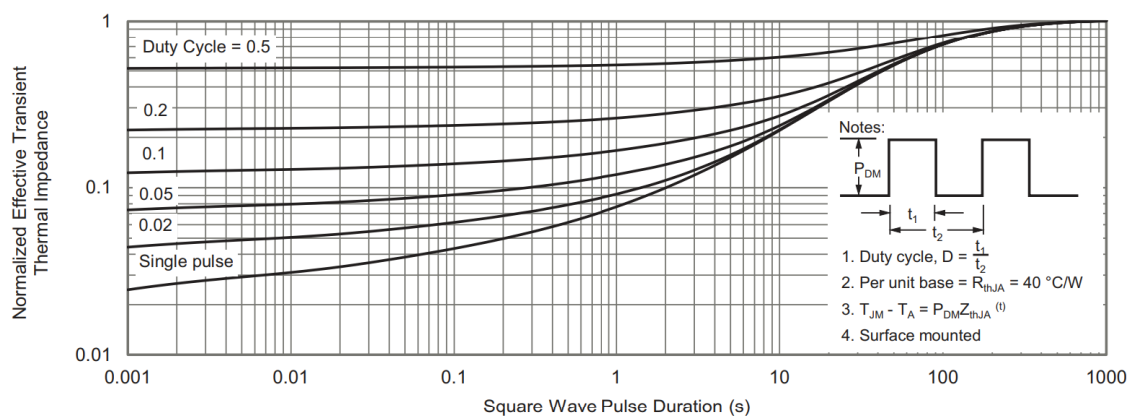
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)



TYPICAL CHARACTERISTICS ($T = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



THERMAL RATINGS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

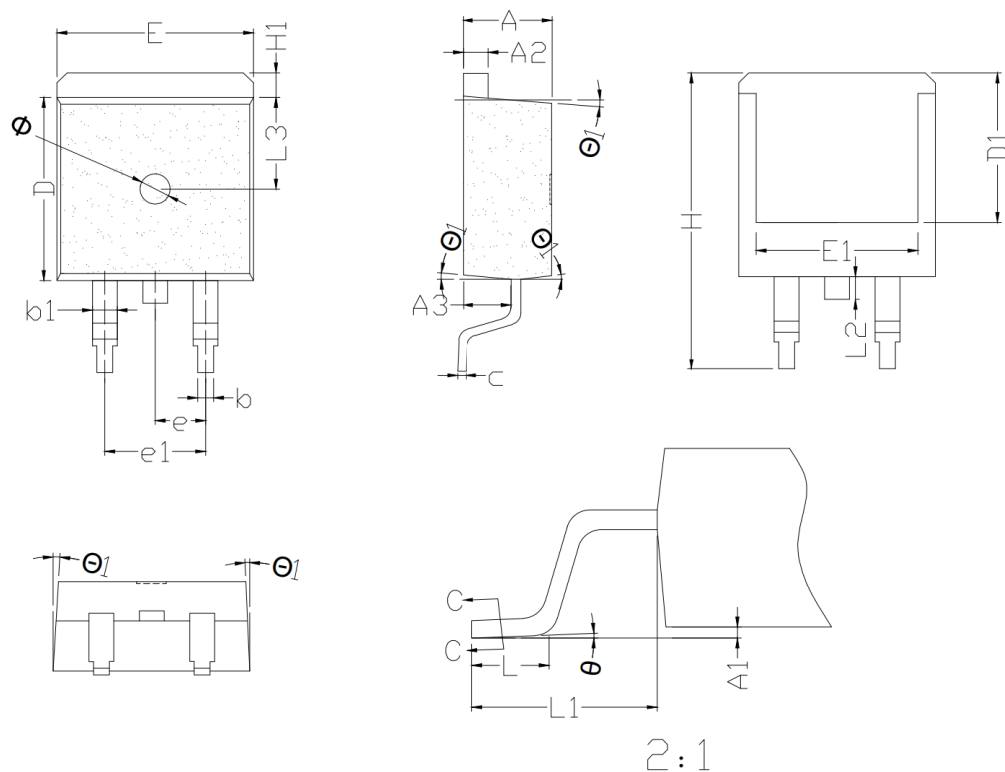


Normalized Thermal Transient Impedance, Junction-to-Ambient

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient ($25\text{ }^{\circ}\text{C}$)
 - Normalized Transient Thermal Impedance Junction to Case ($25\text{ }^{\circ}\text{C}$)
 are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

TO-263 PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX	SYMBOL	MIN	TYP	MAX
A	4.10	4.50	4.80	e	2.35	2.54	2.75
A1	0.00	0.10	0.30	e1	5.08REF		
A2	1.10	1.30	1.50	H	14.50	15.15	16.00
A3	2.15	2.50	3.10	H1	1.00	1.28	1.75
b	0.60	0.80	1.05	L	1.80	2.23	2.90
b1	1.05	1.33	1.50	L1	4.30	4.75	5.50
c	0.33	0.50	0.66	L2	1.00	1.30	1.85
D	8.40	9.20	9.60	L3	0.90	4.65	9.00
D1	7.50REF			ϕ	0°	2°	5°
E	9.60	10.02	10.80	$\phi 1$	2°	-	7°
E1	7.60	9.88	10.30	Φ	1.5BSC		

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