

## 1200V N-Channel Silicon Carbide Power MOSFET

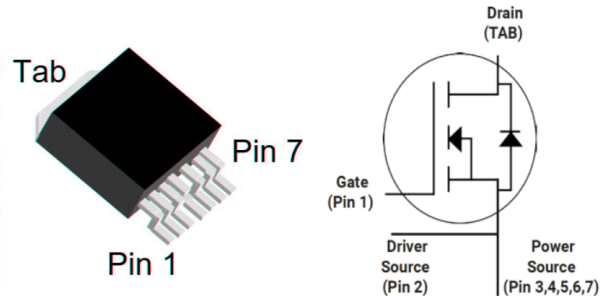
### Features:

- High blocking voltage with low  $R_{DS(ON)}$
- Fast switching speed with low capacitances
- Fast intrinsic diode with low reverse recovery ( $Q_{RR}$ )
- Halogen-free, RoHS compliant <sup>(Note 1)</sup>

### Applications:

- PFC
- EV battery charges
- High voltage DC/DC converters
- Booster converters
- Solar inverters

### Package:



Part Number	Package
DTK30N120SC7	TO-263-7L

### Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions
$V_{DS}$	Drain-Source voltage	1200	V	$V_{GS}=0\text{V}$ , $I_D=100\mu\text{A}$
$V_{GS}$	Gate-Source voltage	-6 to 18	V	static
$V_{GS}$	Gate-Source voltage	-10 to 22	V	dynamic
$I_D$	Drain current (continuous)	30	A	$T_c=25^\circ\text{C}$
		22	A	$T_c=100^\circ\text{C}$
$I_{DM}$	Drain current (pulsed)	80	A	
$P_{TOT}$	Total power dissipation	150	W	$T_c=25^\circ\text{C}$
$T_{stg}$	Storage temperature range	-55 to 175	$^\circ\text{C}$	
$T_J$	Operating junction temperature	-55 to 175	$^\circ\text{C}$	

### Thermal Data

Symbol	Parameter	Value	Unit
$R_{\theta(J-C)}$	Thermal Resistance from Junction to Case	1.0	$^\circ\text{C/W}$
$R_{\theta(J-A)}$	Thermal Resistance from Junction to Case	40	$^\circ\text{C/W}$

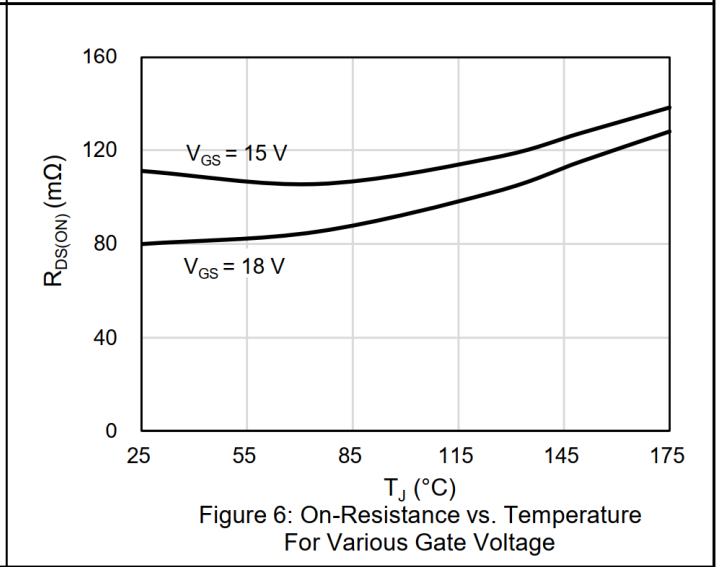
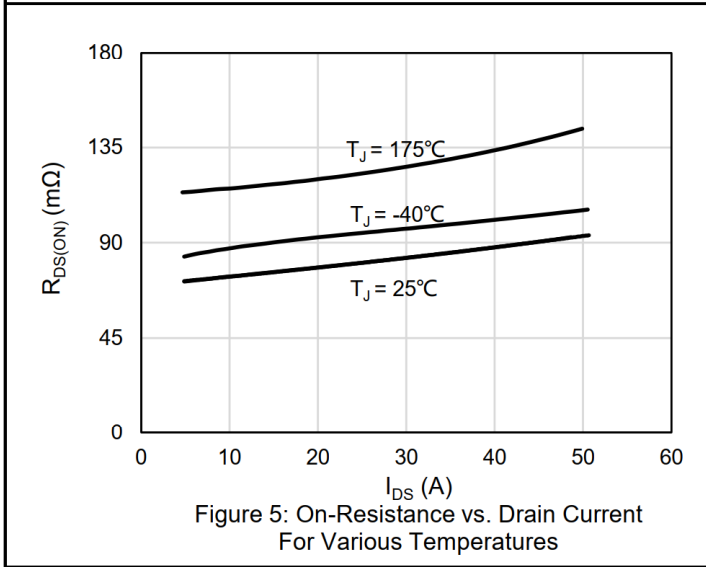
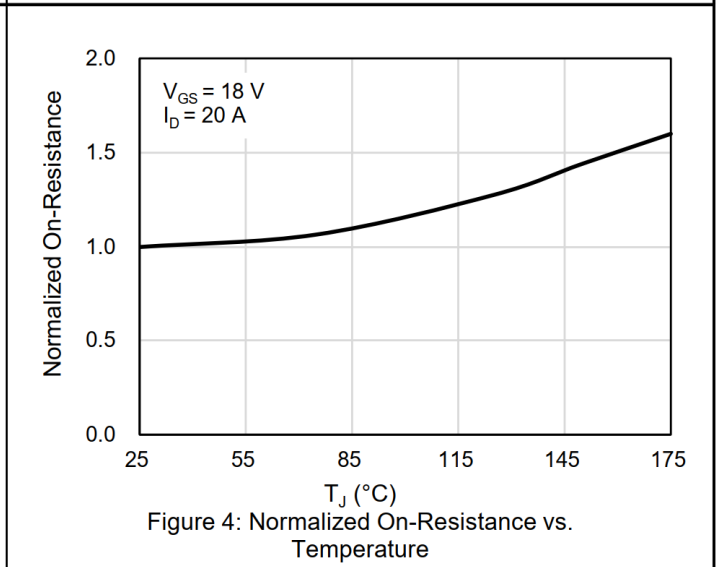
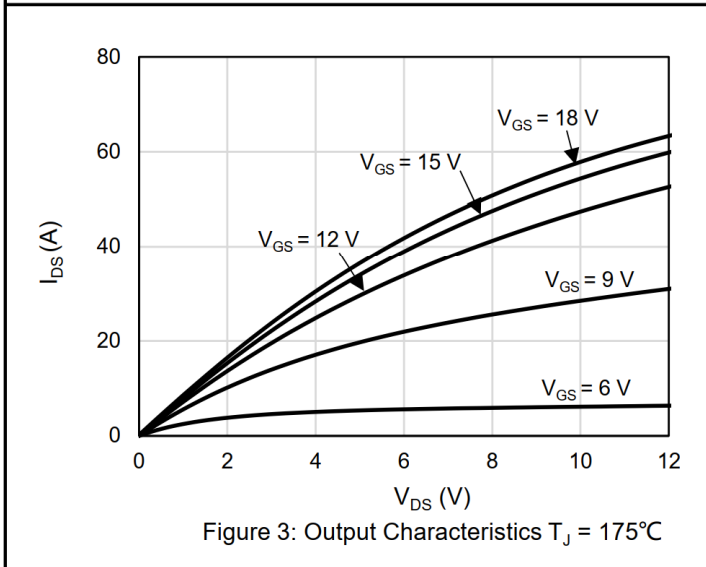
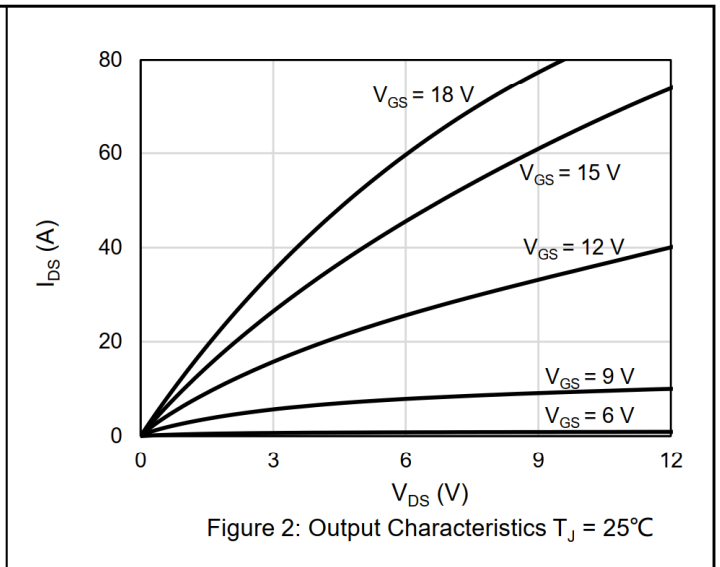
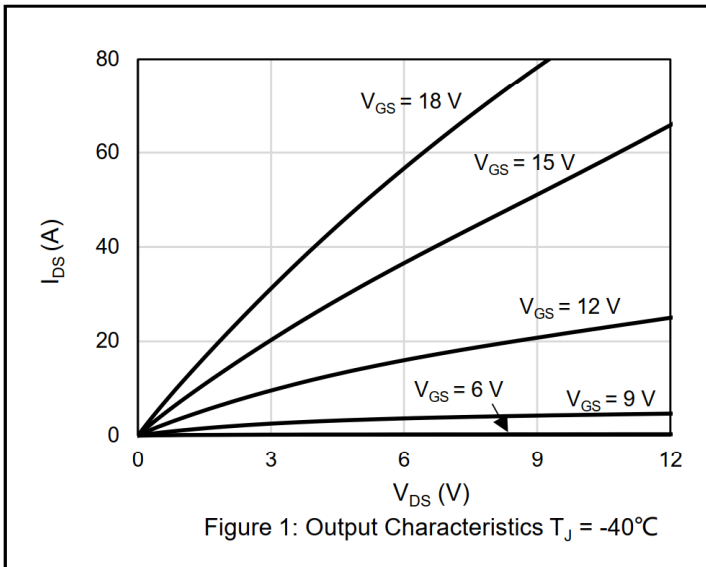
**Electrical Characteristics** ( $T_C=25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Value			Unit	Test Conditions
		Min.	Typ.	Max.		
$I_{DSS}$	Zero gate voltage drain current		5	50	$\mu\text{A}$	$V_{DS}=1200\text{V}, V_{GS}=0\text{V}$
$I_{GSS}$	Gate leakage current		1	$\pm 100$	nA	$V_{DS}=0\text{V}, V_{GS}=-6\sim 18\text{V}$
$V_{TH}$	Gate threshold voltage		3.2	4.5	V	$V_{GS}=V_{DS}, I_D=5\text{mA}$
			2.2		V	$V_{GS}=V_{DS}, I_D=5\text{mA}$ @ $T_C=175^\circ\text{C}$
$R_{ON}$	Static drain-source on-resistance		80	95	m $\Omega$	$V_{GS}=18\text{V}, I_D=20\text{A}$ @ $T_J=25^\circ\text{C}$
			128		m $\Omega$	$V_{GS}=18\text{V}, I_D=20\text{A}$ @ $T_J=175^\circ\text{C}$
$C_{iss}$	Input capacitance		1295		pF	$V_{DS}=800\text{V}, V_{GS}=0\text{V},$ $f=100\text{kHz}, V_{AC}=25\text{mV}$
$C_{oss}$	Output capacitance		65		pF	
$C_{rss}$	Reverse transfer capacitance		8		pF	
$E_{oss}$	$C_{oss}$ stored energy		26		$\mu\text{J}$	
$Q_g$	Total gate charge		67		nC	$V_{DS}=800\text{V}, I_D=20\text{A},$ $V_{GS}=-5\text{ to }18\text{V}$
$Q_{gs}$	Gate-source charge		15		nC	
$Q_{gd}$	Gate-drain charge		46		nC	
$R_g$	Gate input resistance		5.4		$\Omega$	$f=1\text{MHz}, V_{AC}=25\text{mV}$
$G_{FS}$	Forward Transconductance		11		S	$V_{DS} = 20\text{ V}, I_D = 20\text{ A}$
			9.5		S	$V_{DS} = 20\text{ V}, I_D = 20\text{ A},$ $T_J = 175^\circ\text{C}$

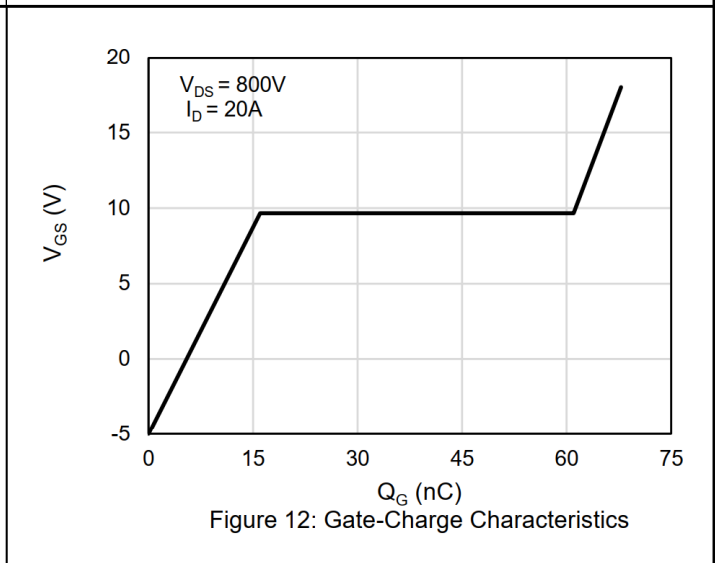
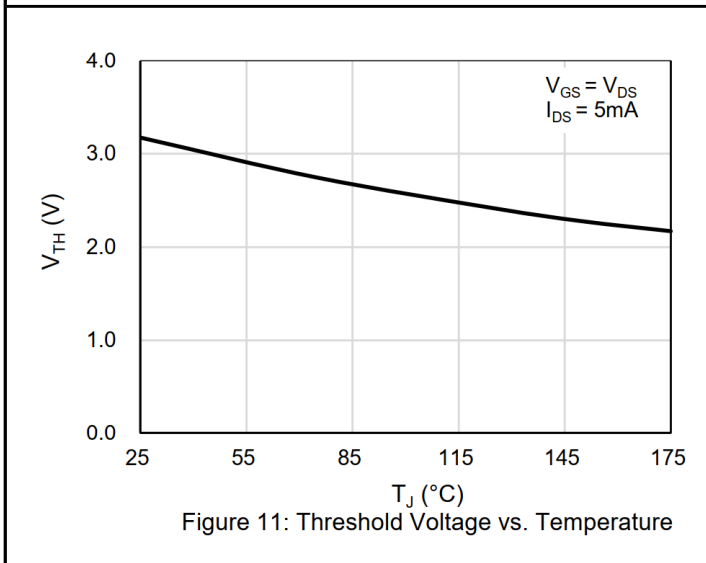
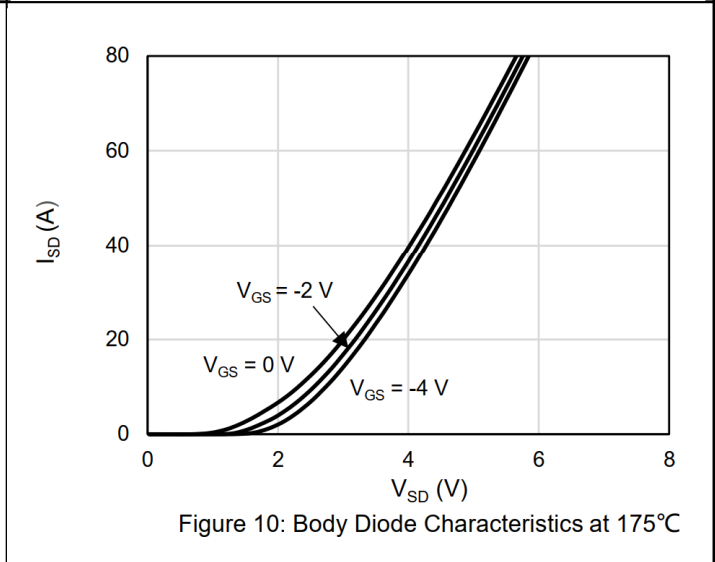
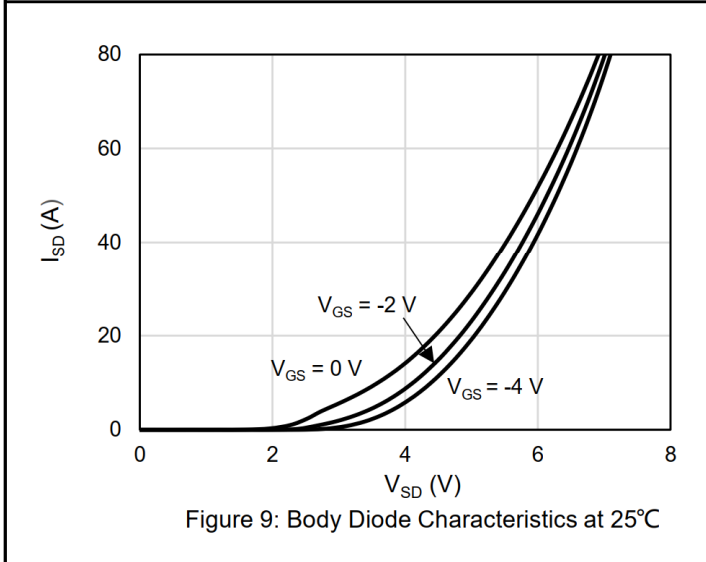
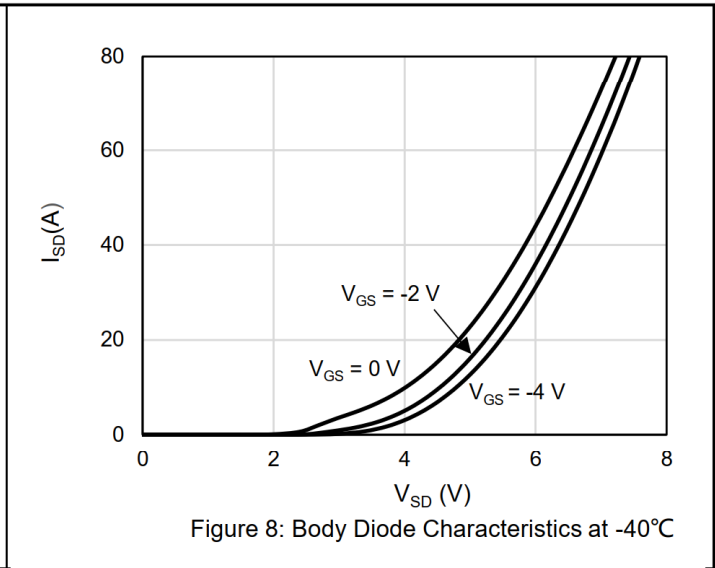
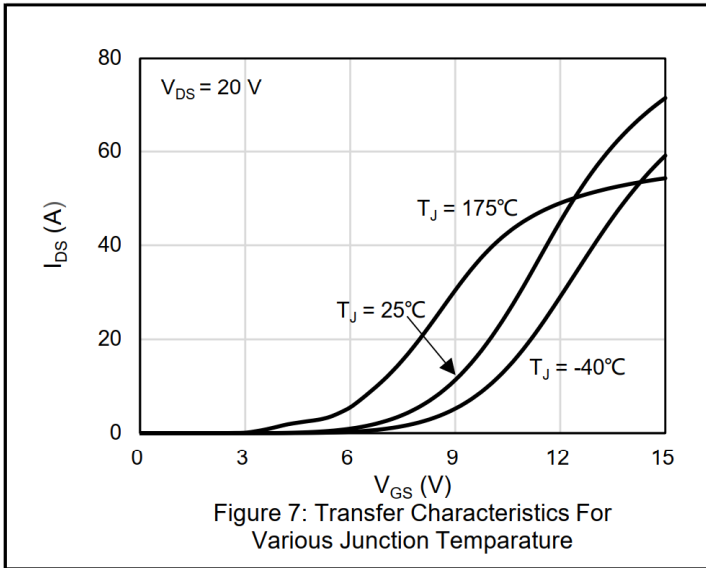
**Reverse Diode Characteristics** ( $T_c=25^\circ\text{C}$  unless otherwise specified)

<b>Switching Characteristics</b>						
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
$T_{D(ON)}$	Turn On Delay Time	$V_{DD} = 800\text{ V}$ , $I_D = 20\text{ A}$ , $V_{GS} = -5/+18\text{ V}$ , $R_{G,EXT} = 5\ \Omega$ $L = 99\ \mu\text{H}$  Diode: Body Diode at $V_{GS} = -5\text{V}$ $T_J = 25\ ^\circ\text{C}$		3		ns
$T_R$	Rise Time			5		ns
$T_{D(OFF)}$	Turn Off Delay Time			22		ns
$T_F$	Fall Time			17		ns
$E_{ON}$	Turn On Energy			464		$\mu\text{J}$
$E_{OFF}$	Turn Off Energy			37		$\mu\text{J}$
$T_{D(ON)}$	Turn On Delay Time	$V_{DD} = 800\text{ V}$ , $I_D = 20\text{ A}$ , $V_{GS} = -5/+18\text{ V}$ , $R_{G,EXT} = 5\ \Omega$ $L = 99\ \mu\text{H}$  Diode: Body Diode at $V_{GS} = -5\text{V}$ $T_J = 175\ ^\circ\text{C}$		3		ns
$T_R$	Rise Time			6		ns
$T_{D(OFF)}$	Turn Off Delay Time			24		ns
$T_F$	Fall Time			15		ns
$E_{ON}$	Turn On Energy			423		$\mu\text{J}$
$E_{OFF}$	Turn Off Energy			35		$\mu\text{J}$
<b>Drain-Source Diode Characteristics</b> ( $T_J = 25\ ^\circ\text{C}$ unless otherwise noted)						
$I_S$	Maximum Continuous Drain-Source Diode Forward Current				30	A
$I_{SM}$	Maximum Pulsed Drain-Source Diode Forward Current				80	A
$V_{SD}$	Diode Forward Voltage	$V_{GS} = -4\text{ V}$ , $I_{SD} = 20\text{ A}$		5		V
		$V_{GS} = -4\text{ V}$ , $I_{SD} = 20\text{ A}$ , $T_J = 175\ ^\circ\text{C}$		3.4		V
$I_{RM}$	Peak Reverse Recovery Current	$V_{GS} = -4\text{ V}$ , $I_{SD} = 20\text{ A}$ , $V_R = 800\text{ V}$ , $di/dt = 460\text{ A}/\mu\text{S}$		3.1		A
$T_{RR}$	Reverse Recovery Time			21		ns
$Q_{RR}$	Reverse Recovery Charge			32		nC

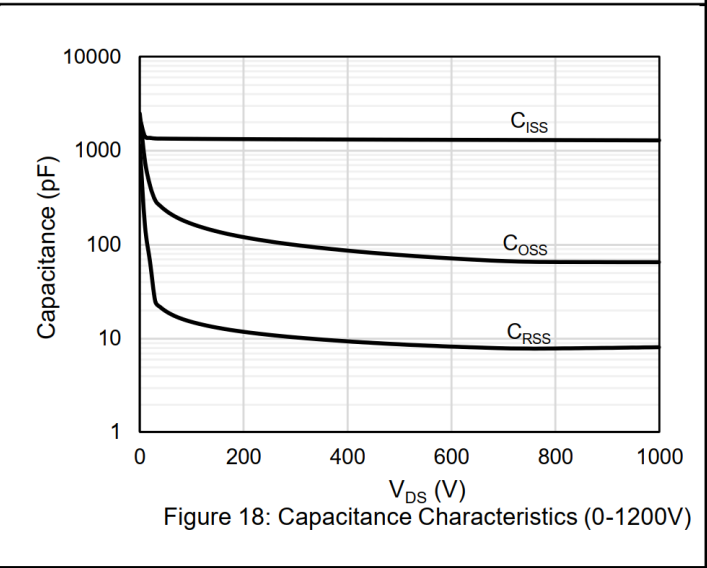
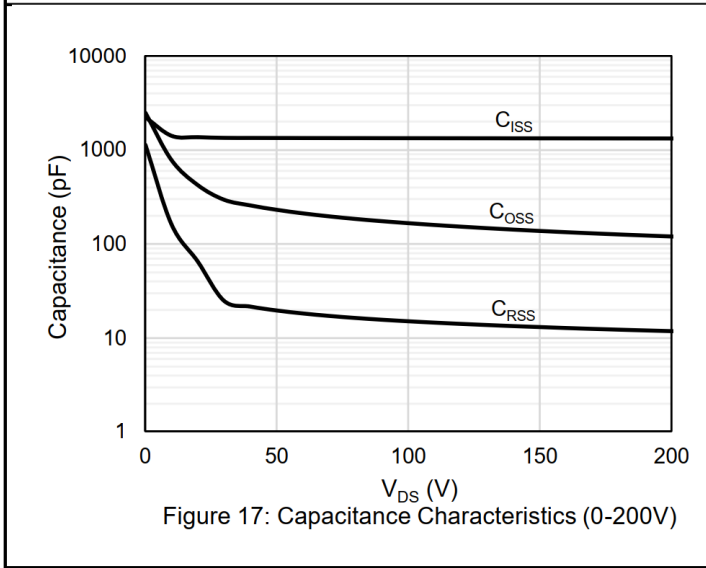
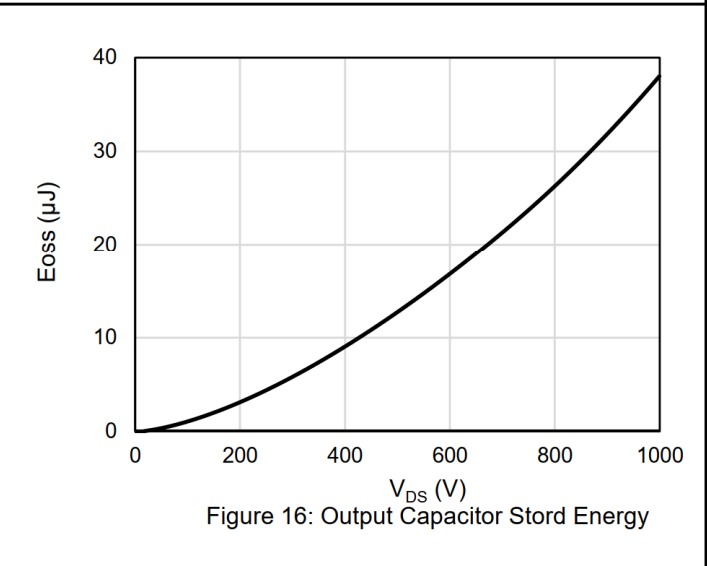
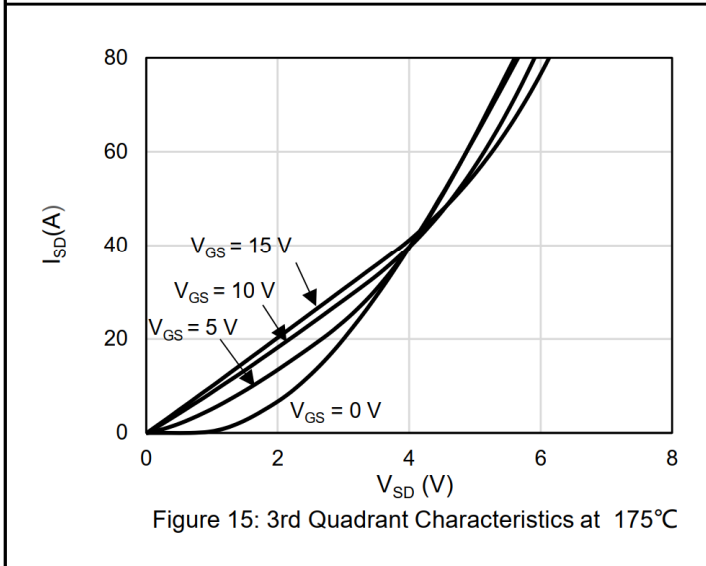
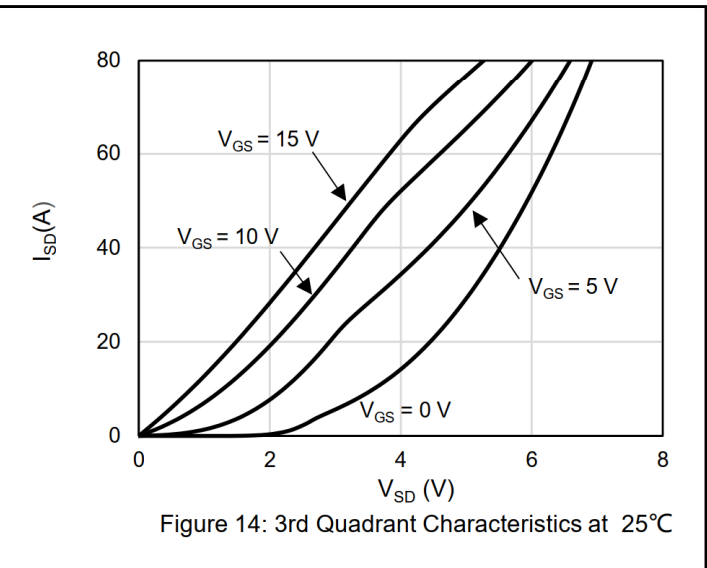
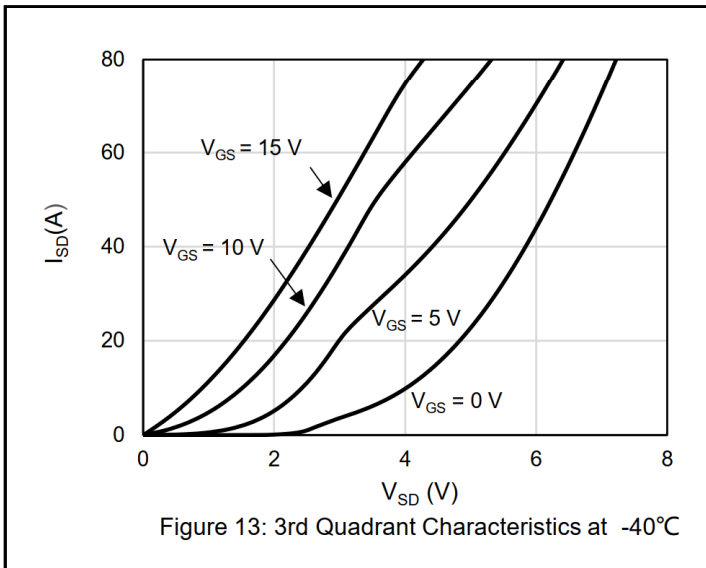
Typical Performance (curves)



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Typical Performance (curves)

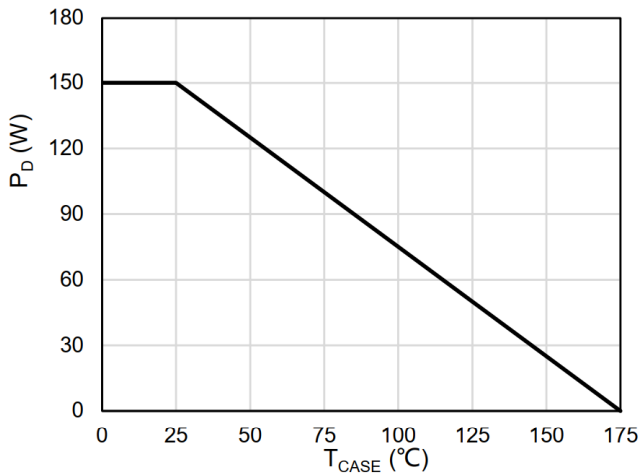


Figure 19: Maximum Power Dissipation Derating vs Case Temperature

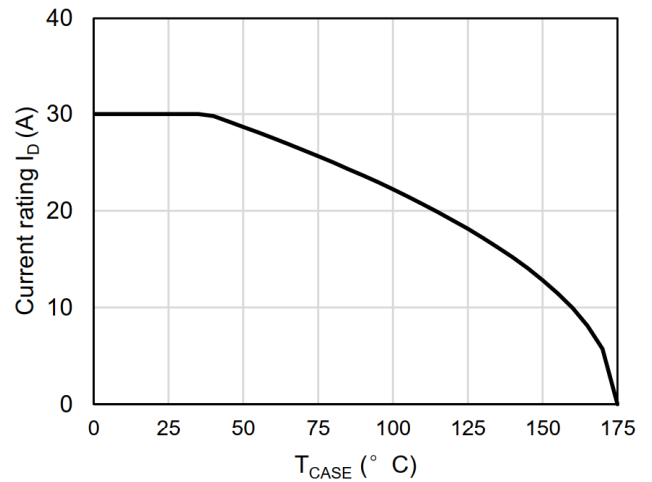


Figure 20: Current De-rating

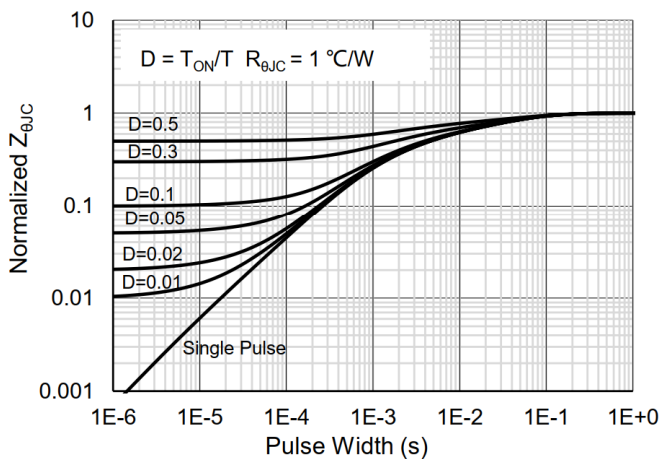


Figure 21: Normalized Maximum Transient Thermal Impedance

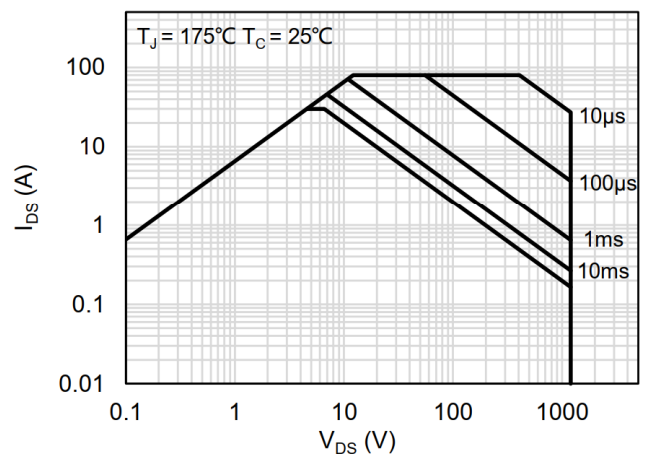


Figure 22: Maximum Forward Biased Safe Operating Area

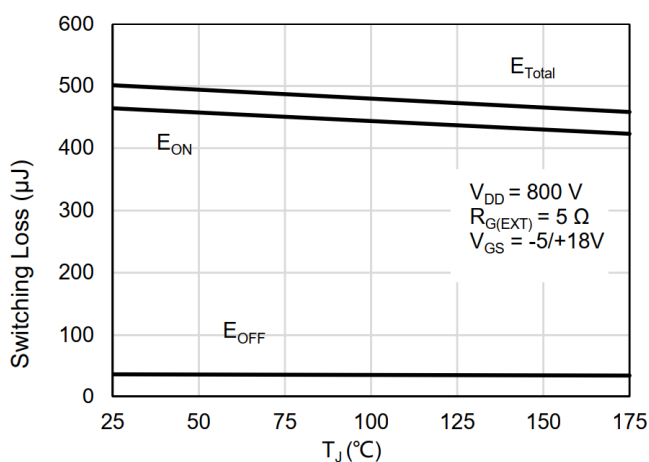


Figure 23: Clamped Inductive Switching Energy vs.  $T_J$

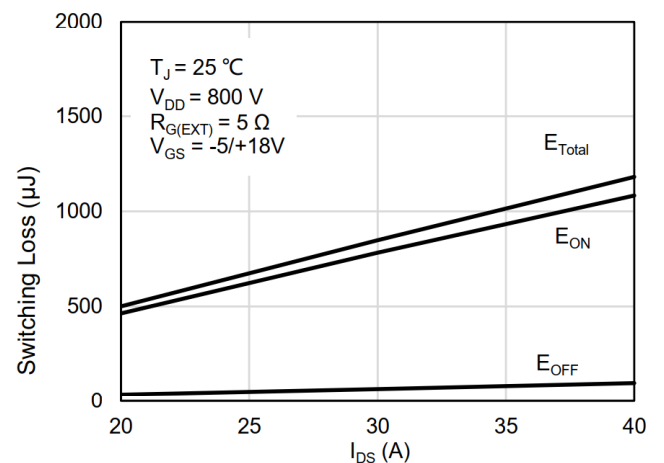
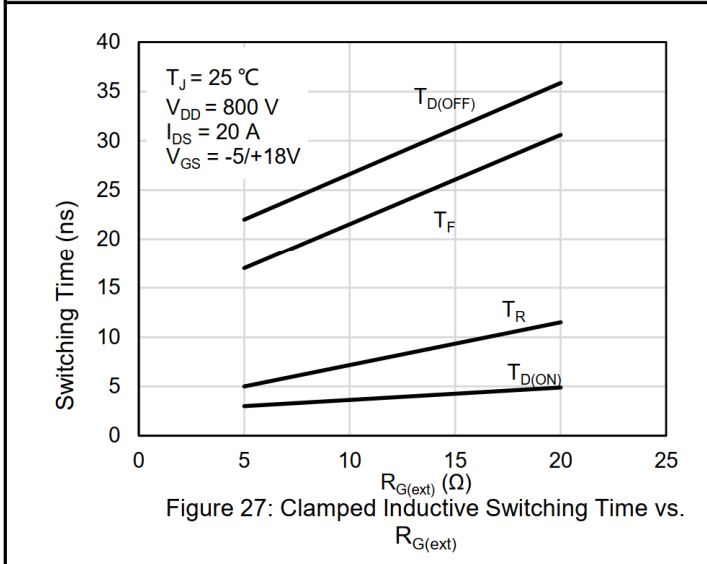
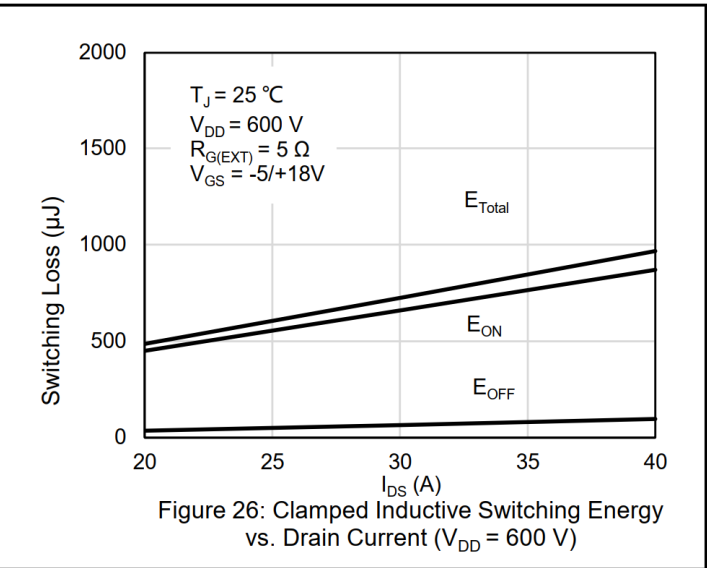
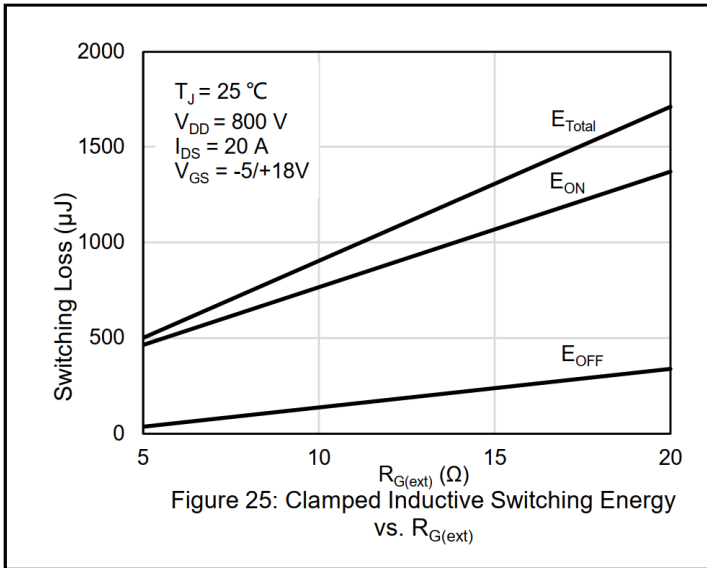


Figure 24: Clamped Inductive Switching Energy vs. Drain Current ( $V_{DD} = 600 V$ )

Typical Performance (curves)



Note4: All figures reference T0247-4L.



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