

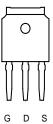
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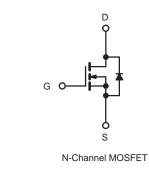
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N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^{a, g}	Q _g (Typ.)			
30	0.0135 at V _{GS} = 10 V	15	7.3 nC			
	0.0165 at V _{GS} = 4.5 V	15	7.5110			







Top View

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g Tested • 100 % UIS Tested

APPLICATIONS

 DC/DC Conversion - System Power



Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	30	V	
Gate-Source Voltage		V _{GS}	± 20		
	T _C = 25 °C		15		
Continuous Drain Current ($T_1 = 150 \ ^{\circ}C$)	T _C = 70 °C		13		
Continuous Drain Current $(T_J = 150 \text{ C})$	T _A = 25 °C	I _D	12 ^{b, c}	Α	
	T _A = 70 °C		9.5 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	45		
Avalanche Current	L = 0.1 mH	I _{AS}	15		
Avalanche Energy		E _{AS}	11.25	mJ	
Continuous Source-Drain Diode Current	T _C = 25 °C		15	Α	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	2.9 ^{b, c}	A	
	T _C = 25 °C		27.7		
Maximum Power Dissipation	T _C = 70 °C	P _D	17.7	w	
	T _A = 25 °C	ГD	3.5 ^{b, c}		
	T _A = 70 °C		2.2 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	<u></u>	
Soldering Recommendations (Peak Tempera		260			

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient	t ≤ 10 s	R _{thJA}	29	36	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	3.6	4.5	- C/W	

Notes:

a. Based on $T_C = 25 \text{ °C}$. b. Surface Mounted on 1" x 1" FR4 board.

c. t = 10 s.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	JΙ _D = 250 μΑ		33		m\//°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1.2		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zerre Octo Maltana Davis Octores ($V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	15			Α	
	D	V _{GS} = 10 V, I _D = 10 A		0.0105	0.0135	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 7 \text{ A}$		0.0135	0.0165		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 10 A		24		S	
Dynamic ^b							
Input Capacitance	C _{iss}			846		pF	
Output Capacitance	C _{oss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		187			
Reverse Transfer Capacitance	C _{rss}			72			
T + I O + OI	Qg	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 10 A		15.4	23		
Total Gate Charge				7.3	11		
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 10 A		2.3			
Gate-Drain Charge	Q _{gd}			2.2			
Gate Resistance	R _g	f = 1 MHz	0.2	0.8	1.6	Ω	
Turn-On Delay Time	t _{d(on)}			15	30	ns	
Rise Time	t _r	V_{DD} = 15 V, R_L = 1.5 Ω		12	24		
Turn-Off Delay Time	t _{d(off)}	$\rm I_D \cong 10$ A, $\rm V_{GEN}$ = 4.5 V, $\rm R_g$ = 1 Ω		13	26		
Fall Time	t _f			10	20		
Turn-On Delay Time	t _{d(on)}			9	18		
Rise Time	t _r	V_{DD} = 15 V, R_L = 1.5 Ω		9	18		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 10 A, V_{GEN} = 10 V, R_g = 1 Ω		14	28		
Fall Time	t _f			8	16		
Drain-Source Body Diode Characteristi	cs						
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			16	А	
Pulse Diode Forward Current	I _{SM}				45	~	
Body Diode Voltage	V _{SD}	I _S = 3 A, V _{GS} = 0 V		0.78	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			17	34	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = 10 A, dl/dt = 100 A/μs, Τ _{.1} = 25 °C		9.5	19	nC	
Reverse Recovery Fall Time	t _a	F = 10 A, and = 100 A/ µs, J = 25 C		10			
Reverse Recovery Rise Time	t _b	-		7		ns	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

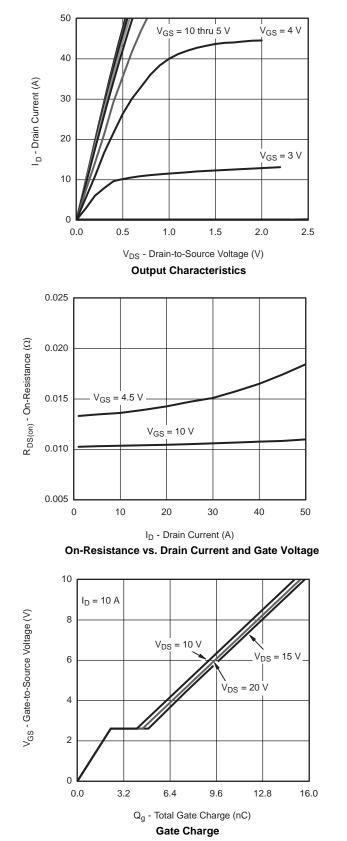
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

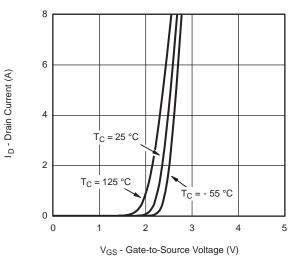


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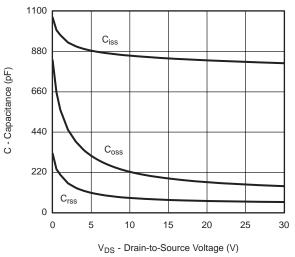
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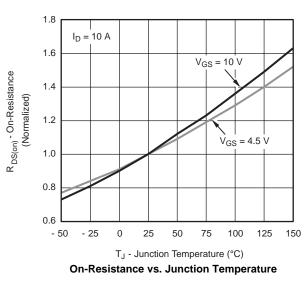




Transfer Characteristics



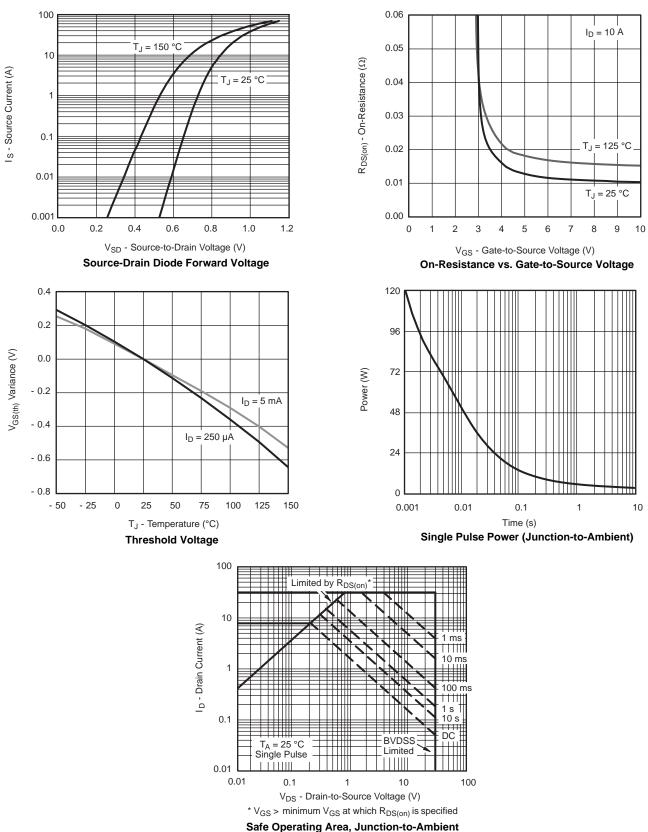




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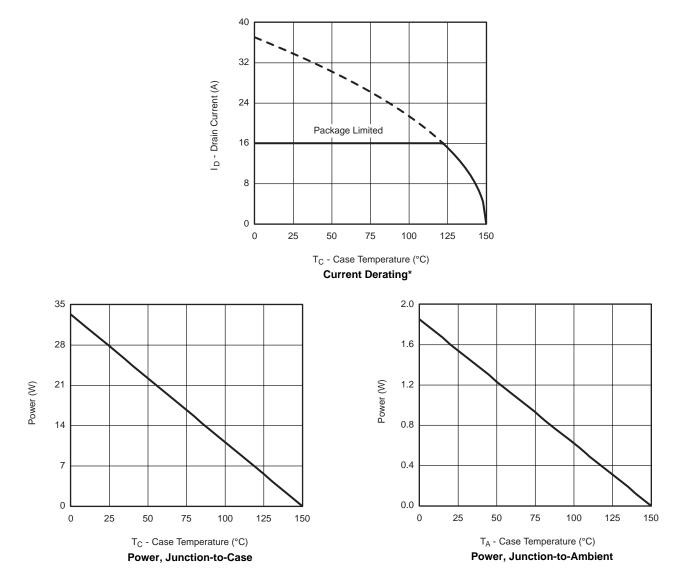
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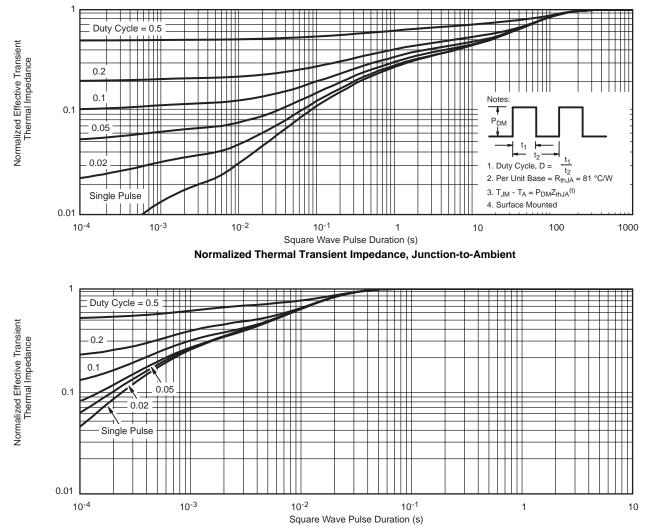
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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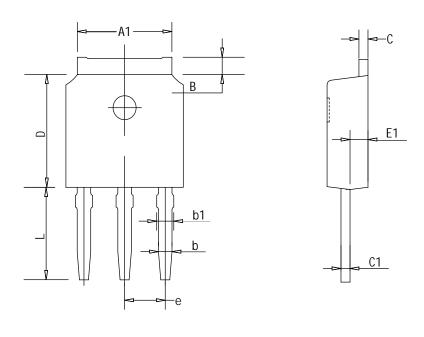
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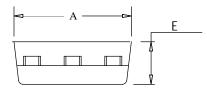




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TO-251 PACKAGE OUTLINE





COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
А	6.30	6.60	6.90
A1	5.00	5.30	5.60
В	0.80	1.00	1.20
C	0.40	0.50	0.60
C1	0.40	0.50	0.60
D	5.80	6.10	6.40
E	2.10	2.30	2.50
E1	0.80	1.00	1.20
L	4.50	5.00	5.50
e	2.10	2.30	2.50
b	0.66	0.76	0.86
b1	0.66	0.86	1.06



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