

## P-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)
- 100	0.202 at V <sub>GS</sub> = - 10 V	- 15	23.2 nC
	0.210 at V <sub>GS</sub> = - 6 V	- 15	

### FEATURES

- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

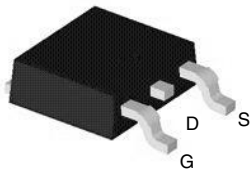


**RoHS**  
COMPLIANT

### APPLICATIONS

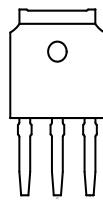
- Active Clamp in Intermediate DC/DC Power Supplies
- H-Bridge High Side Switch for Lighting Application

TO-252 Pin Configuration

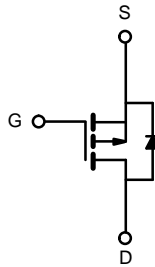


Top View

TO-251



Top View



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 100	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 25 °C	I <sub>D</sub>	- 15	A
	T <sub>C</sub> = 70 °C		- 9.1	
	T <sub>A</sub> = 25 °C		- 2.3 <sup>a, b</sup>	
	T <sub>A</sub> = 70 °C		- 1.9 <sup>a, b</sup>	
Pulsed Drain Current		I <sub>DM</sub>	- 45	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I <sub>S</sub>	- 15	
	T <sub>A</sub> = 25 °C		- 3 <sup>a, b</sup>	
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	15	
Single-Pulse Avalanche Energy		E <sub>AS</sub>	11.25	mJ
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	P <sub>D</sub>	52	W
	T <sub>C</sub> = 70 °C		33	
	T <sub>A</sub> = 25 °C		3.7 <sup>a, b</sup>	
	T <sub>A</sub> = 70 °C		2.4 <sup>a, b</sup>	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 50 to 150	°C
Soldering Recommendations (Peak Temperature)			260	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. t = 10 s.

**THERMAL RESISTANCE RATINGS**

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a, b</sup>	t ≤ 10 s	R <sub>thJA</sub>	26	33	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	1.9	2.4	

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. Maximum under steady state conditions is 81 °C/W.

**SPECIFICATIONS (T<sub>J</sub> = 25 °C, unless otherwise noted)**

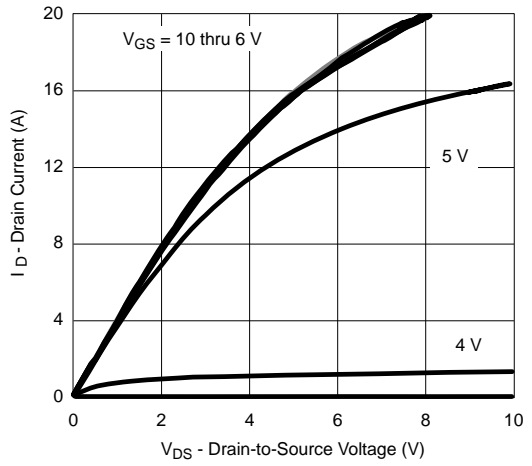
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 100			V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = - 250 μA		- 165		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>			- 6.6		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 2		- 4	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 0 V			- 1	μA
		V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ - 5 V, V <sub>GS</sub> = - 10 V	- 15			A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 4 A		0.202	0.223	Ω
		V <sub>GS</sub> = - 6 V, I <sub>D</sub> = - 3 A		0.210	0.231	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = 4 A		12		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = - 50 V, V <sub>GS</sub> = 0 V, f = 1 MHz		1190		pF
Output Capacitance	C <sub>oss</sub>			61		
Reverse Transfer Capacitance	C <sub>rss</sub>			42		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = - 75 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 3 A		27.5	42	nC
		V <sub>DS</sub> = - 75 V, V <sub>GS</sub> = - 6 V, I <sub>D</sub> = - 3 A		23.2	35	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 75 V, V <sub>GS</sub> = - 6 V, I <sub>D</sub> = - 3 A		5.4		
Gate-Drain Charge	Q <sub>gd</sub>			8.4		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	1.3	6.1	9.2	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = - 75 V, R <sub>L</sub> = 25 Ω I <sub>D</sub> ≅ - 3 A, V <sub>GEN</sub> = - 6 V, R <sub>g</sub> = 1 Ω		20	30	ns
Rise Time	t <sub>r</sub>			95	145	
Turn-Off DelayTime	t <sub>d(off)</sub>			38	60	
Fall Time	t <sub>f</sub>			34	51	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = - 75 V, R <sub>L</sub> = 25 Ω I <sub>D</sub> ≅ - 3 A, V <sub>GEN</sub> = - 10 V, R <sub>g</sub> = 1 Ω		11	18	
Rise Time	t <sub>r</sub>			28	42	
Turn-Off DelayTime	t <sub>d(off)</sub>			52	78	
Fall Time	t <sub>f</sub>			35	53	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 13	A
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 45	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 3 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 4 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		65	90	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			180	270	nC
Reverse Recovery Fall Time	t <sub>a</sub>			45		ns
Reverse Recovery Rise Time	t <sub>b</sub>			20		

Notes:

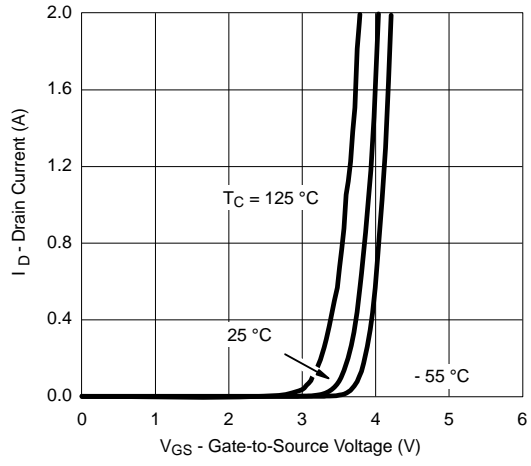
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

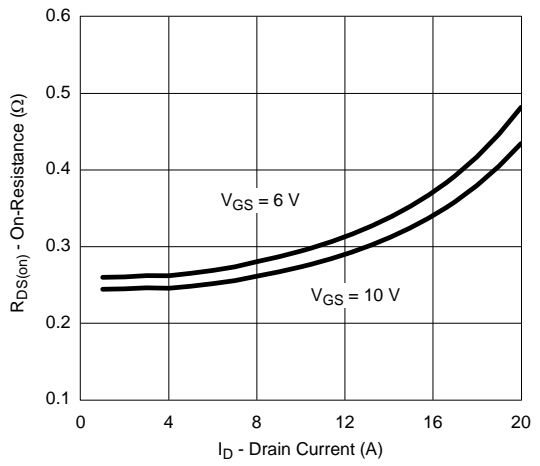
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



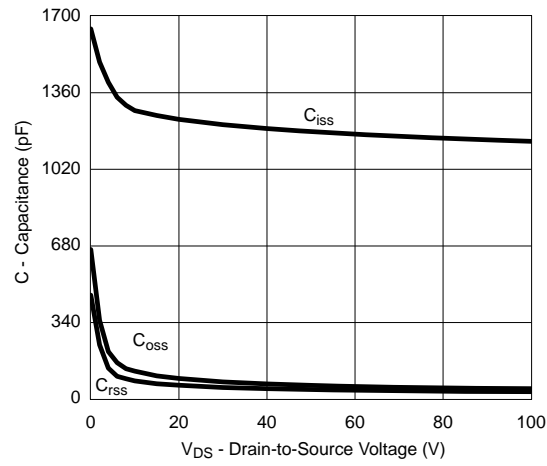
**Output Characteristics**



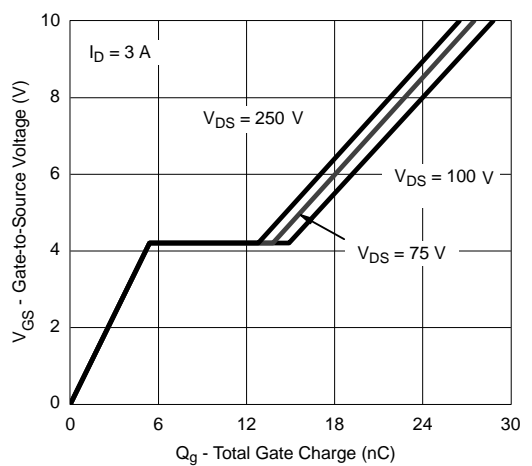
**Transfer Characteristics**



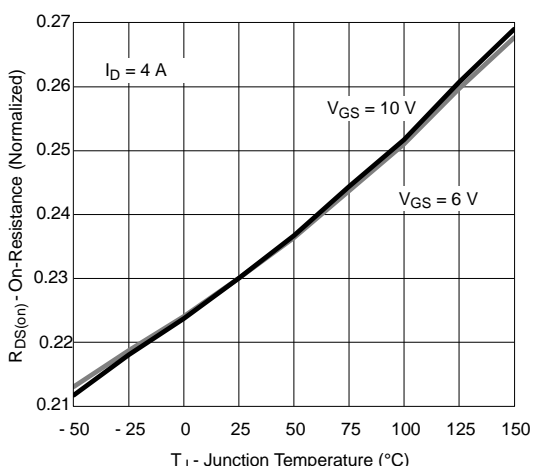
**On-Resistance vs. Drain Current and Gate Voltage**



**Capacitance**

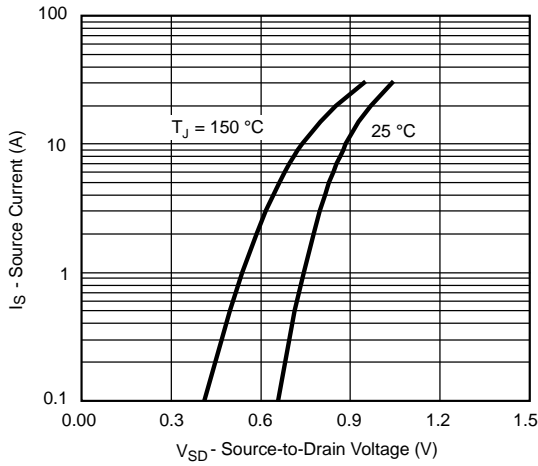


**Gate Charge**

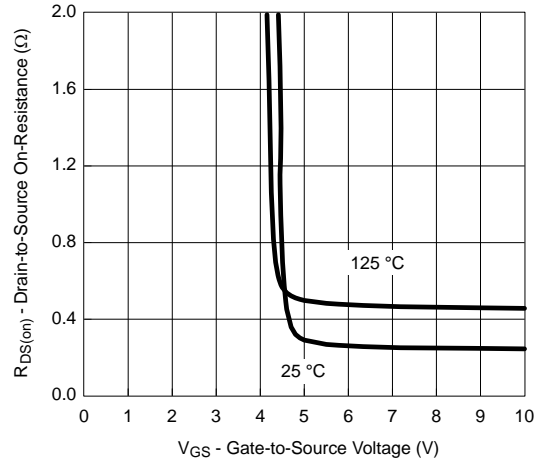


**On-Resistance vs. Junction Temperature**

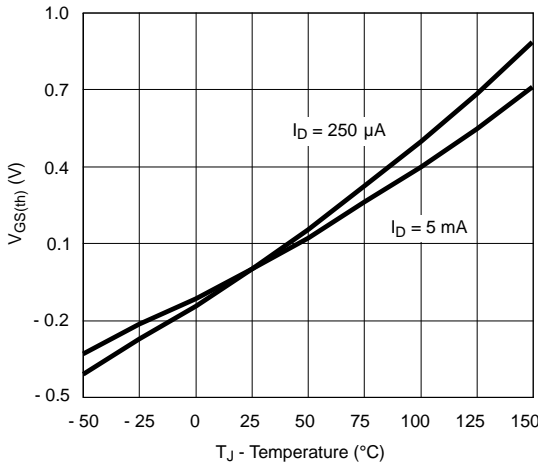
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



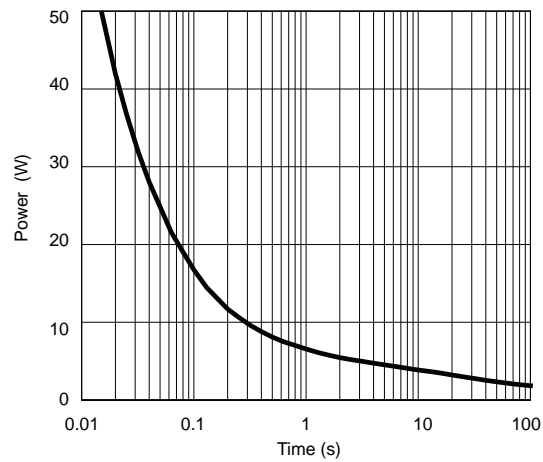
**Source-Drain Diode Forward Voltage**



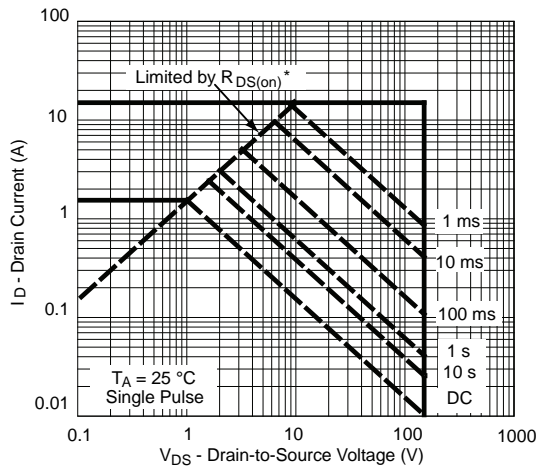
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**

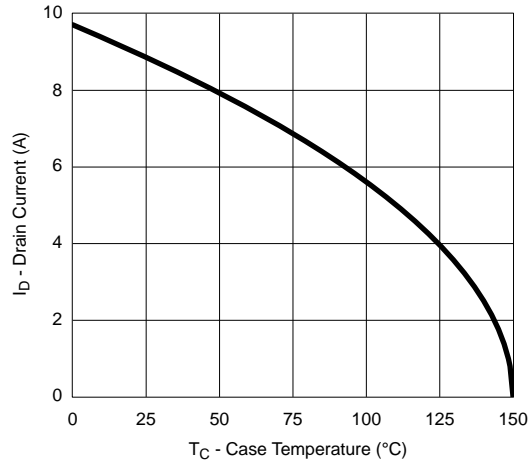


**Single Pulse Power, Junction-to-Ambient**

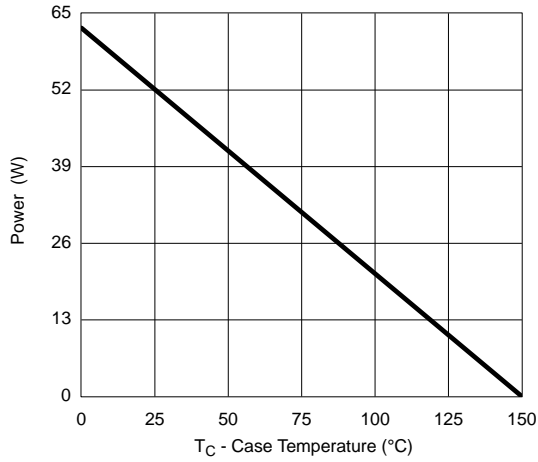


**Safe Operating Area, Junction-to-Ambient**

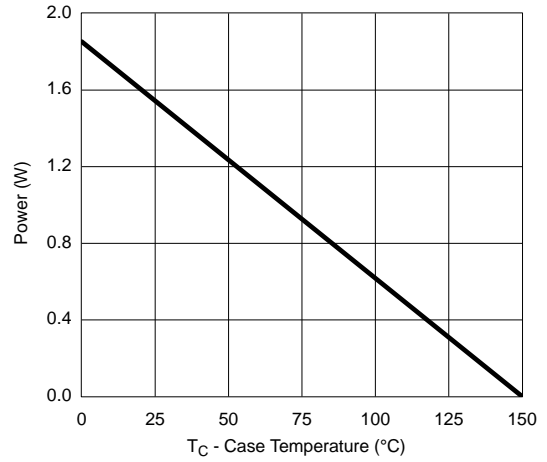
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



**Current Derating\***



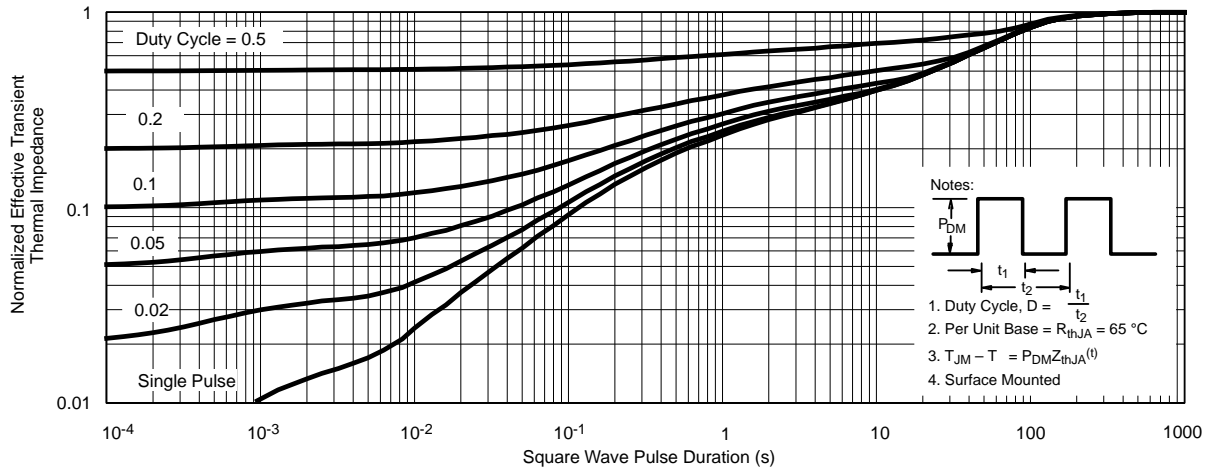
**Power, Junction-to-Case**



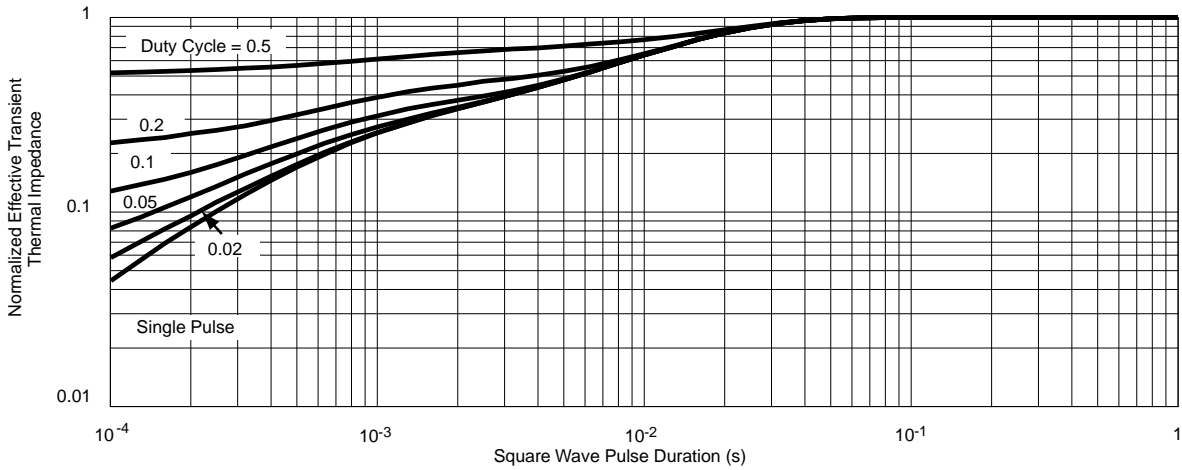
**Power, Junction-to-Ambient**

\* The power dissipation PD is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

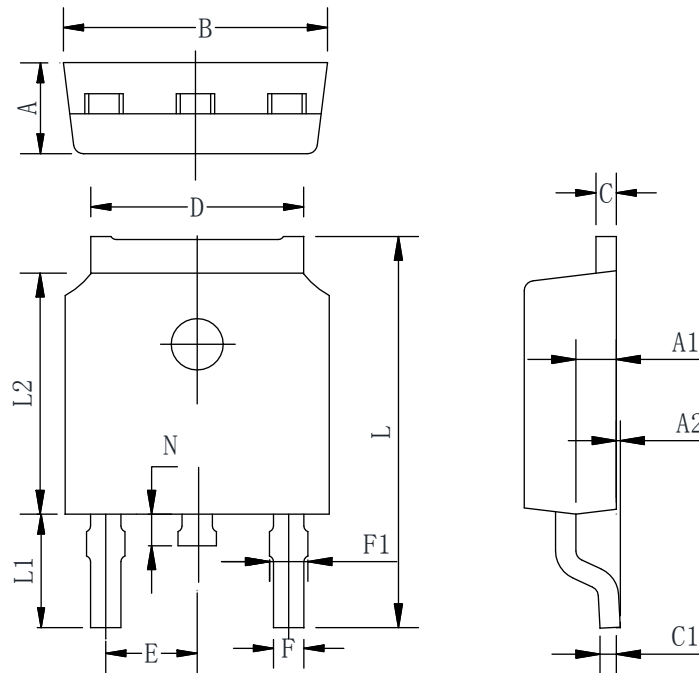


**Normalized Thermal Transient Impedance, Junction-to-Ambient**



**Normalized Thermal Transient Impedance, Junction-to-Foot**

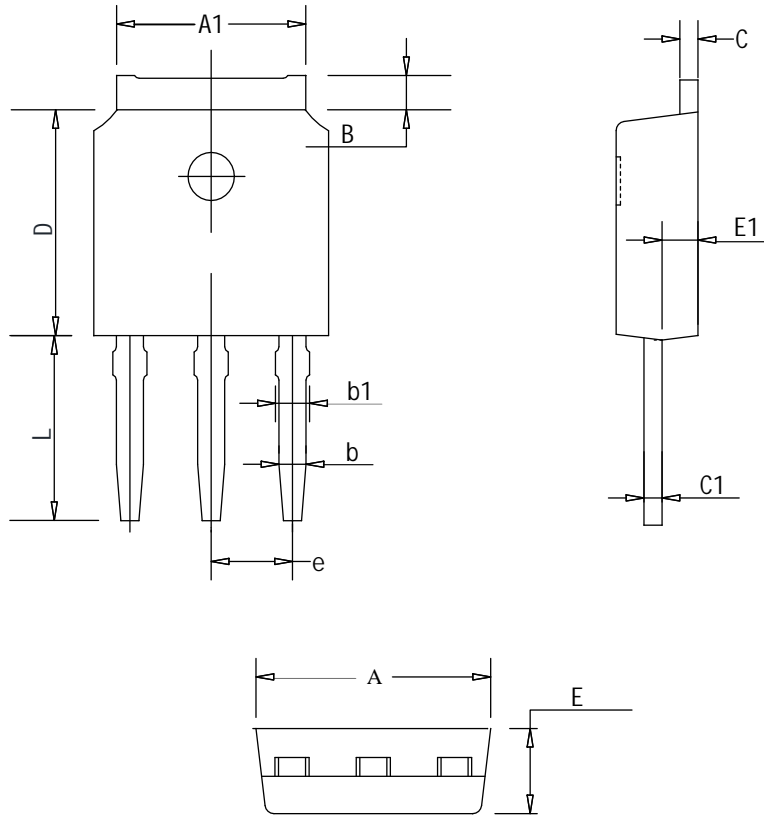
## TO-252-2L PACKAGE OUTLINE



COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Typ	Max
A	2.10	2.30	2.50
A1	0.88	1.01	1.16
A2	0.00	0.15	0.28
B	6.40	6.60	6.80
C	0.42	0.50	0.63
C1	0.42	0.50	0.63
D	5.08	5.32	5.65
E	2.286 TYP		
F	0.63	0.76	0.89
F1	0.64	0.86	1.08
L	9.30	9.90	10.80
L1	2.4	2.8	3.6
L2	5.90	6.10	6.55
N	0.57	0.80	1.05

## TO-251 PACKAGE OUTLINE



COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	6.30	6.60	6.90
A1	5.00	5.30	5.60
B	0.80	1.00	1.20
C	0.40	0.50	0.60
C1	0.40	0.50	0.60
D	5.80	6.10	6.40
E	2.10	2.30	2.50
E1	0.80	1.00	1.20
L	4.50	5.00	5.50
e	2.10	2.30	2.50
b	0.66	0.76	0.86
b1	0.66	0.86	1.06



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