# N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>			
60	0.0093 at V <sub>GS</sub> = 10 V	55			
	0.0122 at V <sub>GS</sub> = 4.5 V	50			

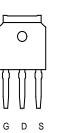
TO-251

#### **FEATURES**

- 175 °C Junction Temperature
- DT-Trench Power MOSFET

D

• Material categorization:





Parameter		Symbol	Limit	Unit	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
Continuous Ducin Current (T. 175 °C)b	T <sub>C</sub> = 25 °C	1	55	_	
Continuous Drain Current $(T_J = 175 \ ^{\circ}C)^b$	T <sub>C</sub> = 100 °C	I <sub>D</sub>	50 <sup>a</sup>		
Pulsed Drain Current	I <sub>DM</sub>	165	А		
Continuous Source Current (Diode Conduction)	۱ <sub>S</sub>	50 <sup>a</sup>			
Avalanche Current	I <sub>AS</sub>	50			
Single Avalanche Energy (Duty Cycle $\leq$ 1 %)	L = 0.1 mH	E <sub>AS</sub>	125	mJ	
Maximum Dawar Dissinction	T <sub>C</sub> = 25 °C	P	136	w	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	• P <sub>D</sub> —	3 <sup>b</sup> , 8.3 <sup>b, c</sup>	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stq</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum lungtion to Amelianta	$t \le 10 \text{ sec}$	R <sub>thJA</sub>	15	18		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		40	50	°C/W	
Maximum Junction-to-Case		R <sub>thJC</sub>	0.85	1.1		

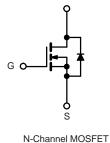
Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

c. t  $\leq$  10 s.







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Parameter	Symbol Test Conditions		Min.	Typ. <sup>a</sup>	Max.	Unit	
Static	<u> </u>				<u> </u>		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	60			V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		2	3	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
		$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50		
		$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	50 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C		250		
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 V, V_{GS} = 10 V$	50			А	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A	0.0074 0.0093		0.0093	_	
- ·	Р	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.0160			
Drain-Source On-State Resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 175 °C			0.0200		
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 15 A			0.0122		
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		60		S	
Dynamic							
Input Capacitance	C <sub>iss</sub>			2650		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS}$ = 0 V, $V_{DS}$ = 25 V, f = 1 MHz		470			
Reverse Transfer Capacitance	C <sub>rss</sub>			225		1	
Total Gate Charge <sup>c</sup>	Qg			47	70	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{\rm DS}$ = 30 V, $V_{\rm GS}$ = 10 V, $I_{\rm D}$ = 50 A		10			
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			12			
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			10	20	ns	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = 30 V, $R_L$ = 0.6 $\Omega$		15	25		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$\text{I}_\text{D} \cong$ 50 A, $\text{V}_\text{GEN}$ = 10 V, $\text{R}_\text{g}$ = 2.5 $\Omega$		35	50		
Fall Time <sup>c</sup>	t <sub>f</sub>			20	30		
Source-Drain Diode Ratings and Cha	aracteristics (	T <sub>C</sub> = 25 °C)		<u> </u>			
Pulsed Current	I <sub>SM</sub>				165	А	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> = 20 A, V <sub>GS</sub> = 0 V		1	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs		45	100	ns	

Notes:

a. For design aid only; not subject to production testing.

b. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

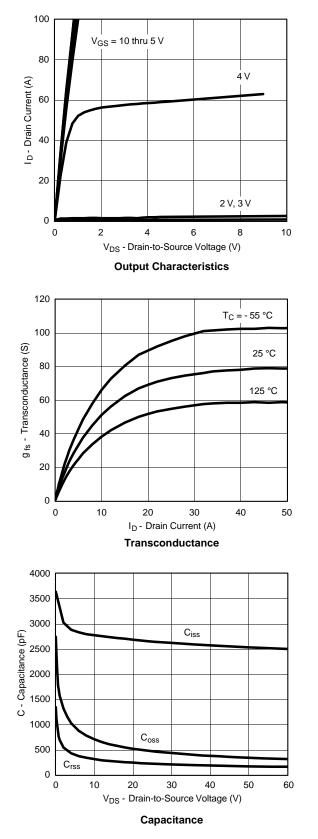
c. Independent of operating temperature.

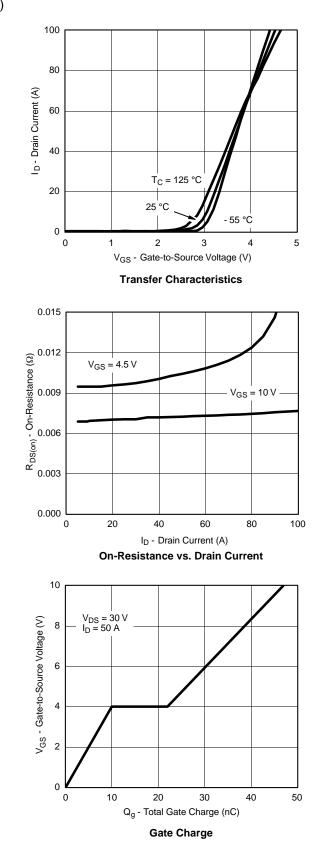
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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#### TYPICAL CHARACTERISTICS (25 °C unless noted)

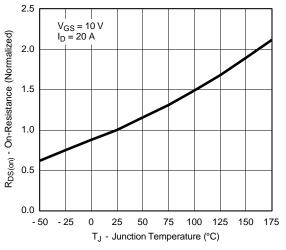




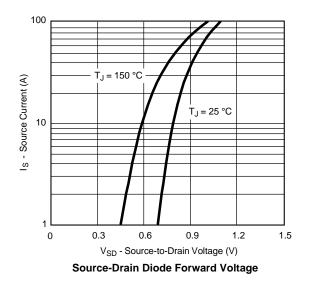


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### TYPICAL CHARACTERISTICS (25 °C unless noted)



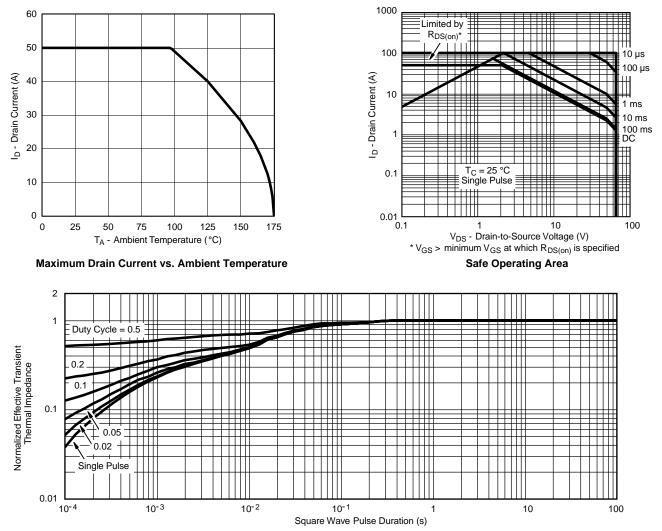
**On-Resistance vs. Junction Temperature** 





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#### **THERMAL RATINGS**

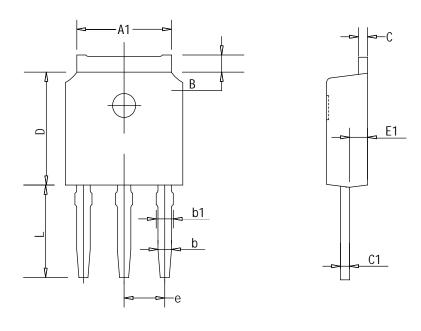


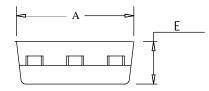
Normalized Thermal Transient Impedance, Junction-to-Case

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# TO-251 PACKAGE OUTLINE





#### COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
А	6.30	6.60	6.90
A1	5.00	5.30	5.60
В	0.80	1.00	1.20
C	0.40	0.50	0.60
C1	0.40	0.50	0.60
D	5.80	6.10	6.40
E	2.10	2.30	2.50
E1	0.80	1.00	1.20
L	4.50	5.00	5.50
е	2.10	2.30	2.50
b	0.66	0.76	0.86
b1	0.66	0.86	1.06

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