

## P-Channel 100 V (D-S) MOSFET

### PRODUCT SUMMARY

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) Max.	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)
- 100	0.075 at V <sub>GS</sub> = - 10 V	- 9	22
	0.08 at V <sub>GS</sub> = - 4.5 V	- 8	

### FEATURES

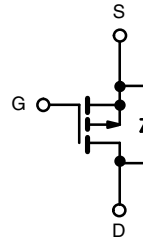
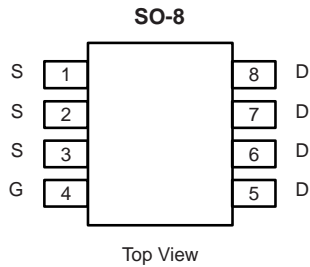
- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested

### APPLICATIONS

- Networking
- LED Lighting Application
- Portable Equipment
- DC-DC Power Management



**RoHS**  
COMPLIANT



P-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25 °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	- 100	V
Gate-Source Voltage	V <sub>GS</sub>	± 20	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	I <sub>D</sub>	T <sub>C</sub> = 25 °C	- 9
		T <sub>C</sub> = 70 °C	- 7.5
Pulsed Drain Current	I <sub>DM</sub>	- 36	A
Avalanche Current	I <sub>AS</sub>	- 8.8	
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	T <sub>C</sub> = 25 °C	5.5 <sup>b</sup>
		T <sub>C</sub> = 70 °C	3.5
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	45	°C/W
Junction-to-Case (Drain)	R <sub>thJC</sub>	3.8	

Notes:

- Duty cycle ≤ 1 %.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR-4 material).

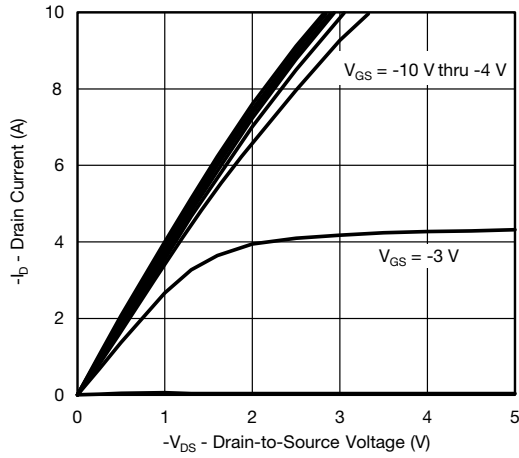
<b>SPECIFICATIONS</b> ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{DS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	- 100			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	- 1		- 3	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			$\pm 250$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -100\text{ V}, V_{GS} = 0\text{ V}$			- 1	$\mu\text{A}$
		$V_{DS} = -80\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$			- 50	
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -6\text{ A}$		0.075	0.095	$\Omega$
		$V_{GS} = -4.5\text{ V}, I_D = -5\text{ A}$		0.08	0.110	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = -20\text{ V}, I_D = -6\text{ A}$		15.5		S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = -50\text{ V}, f = 1\text{ MHz}$		3720		pF
Output Capacitance	$C_{oss}$			104		
Reverse Transfer Capacitance	$C_{rss}$			43		
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{DS} = -50\text{ V}, V_{GS} = -10\text{ V}, I_D = -6\text{ A}$		53.5		nC
				22		
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = -50\text{ V}, V_{GS} = -4.5\text{ V}, I_D = -5\text{ A}$		14		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			8.8		
Gate Resistance	$R_g$	$f = 1\text{ MHz}$		7.5		$\Omega$
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = -50\text{ V}, R_L = 17.2\text{ }\Omega$ $I_D \equiv -6\text{ A}, V_{GEN} = -10\text{ V}, R_g = 1\text{ }\Omega$		10		ns
Rise Time <sup>c</sup>	$t_r$			22		
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$			85		
Fall Time <sup>c</sup>	$t_f$			50		
<b>Drain-Source Body Diode Ratings and Characteristics</b> $T_C = 25\text{ }^\circ\text{C}^b$						
Continuous Current	$I_S$				- 9	A
Pulsed Current	$I_{SM}$				- 36	
Forward Voltage <sup>a</sup>	$V_{SD}$	$I_F = -2.9\text{ A}, V_{GS} = 0\text{ V}$		- 0.7	- 1.3	V
Reverse Recovery Time	$t_{rr}$	$I_F = -2.9\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$		26		ns
Reverse Recovery Charge	$Q_{rr}$				38	

**Notes:**

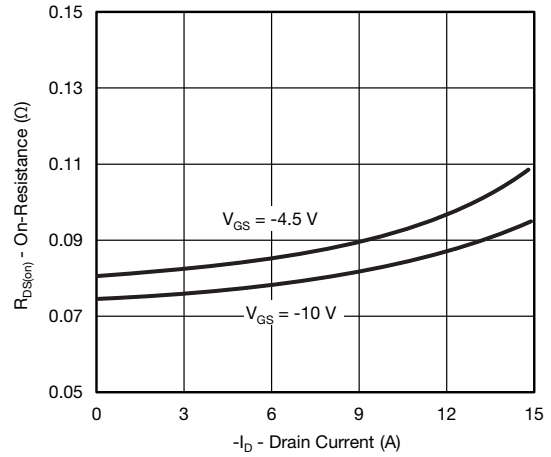
- Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

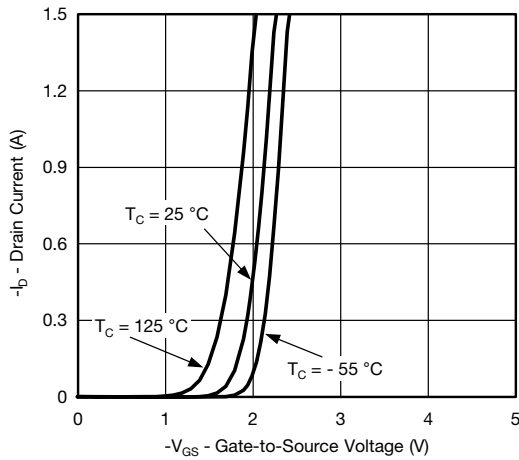
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



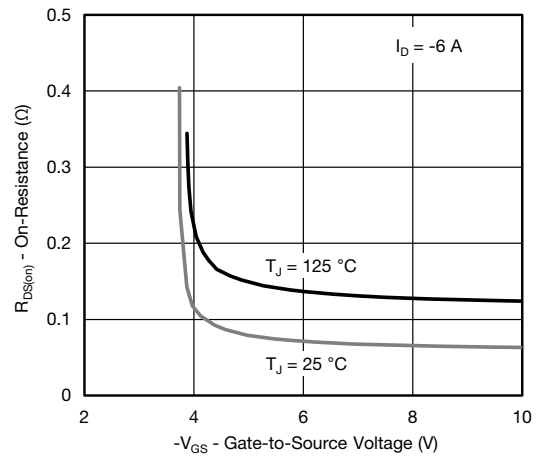
**Output Characteristics**



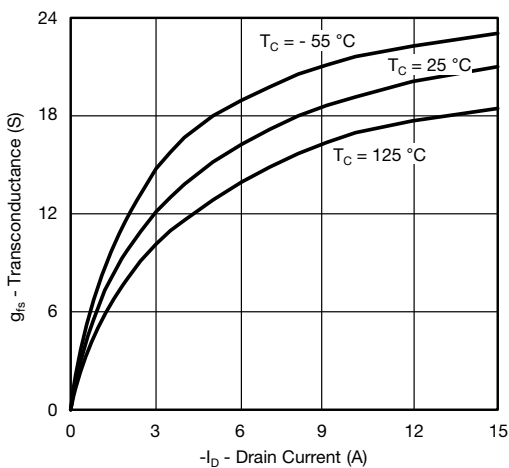
**On-Resistance vs. Drain Current**



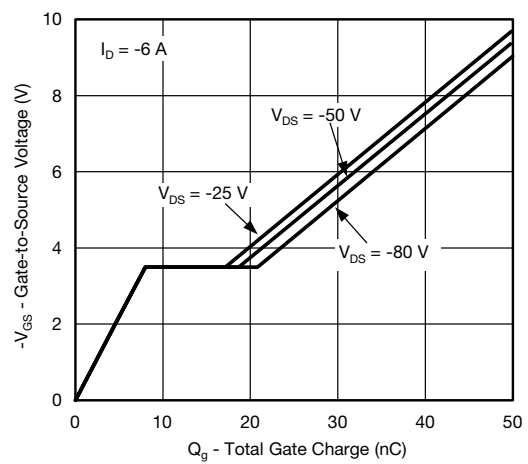
**Transfer Characteristics**



**On-Resistance vs. Gate-to-Source Voltage**

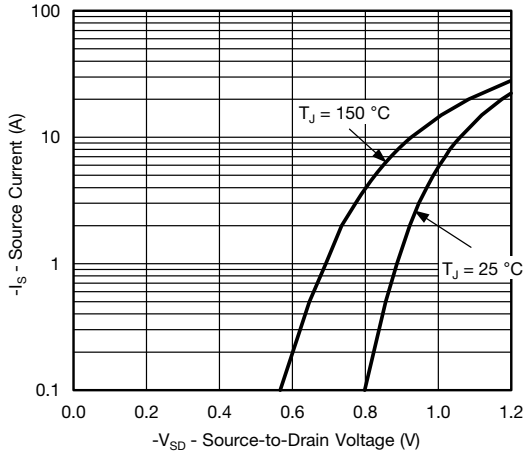


**Transconductance**

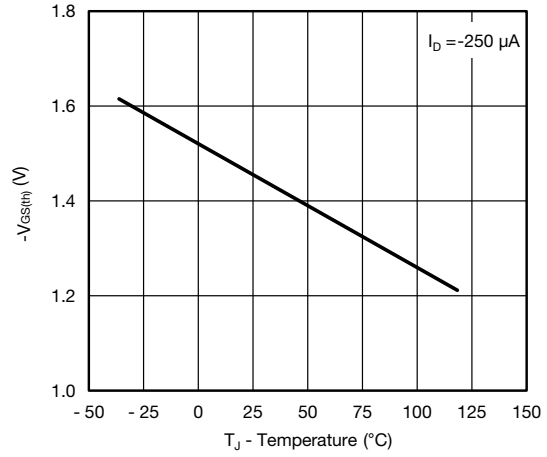


**Gate Charge**

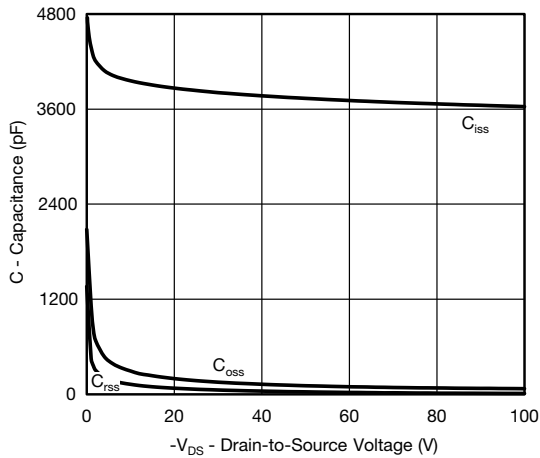
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



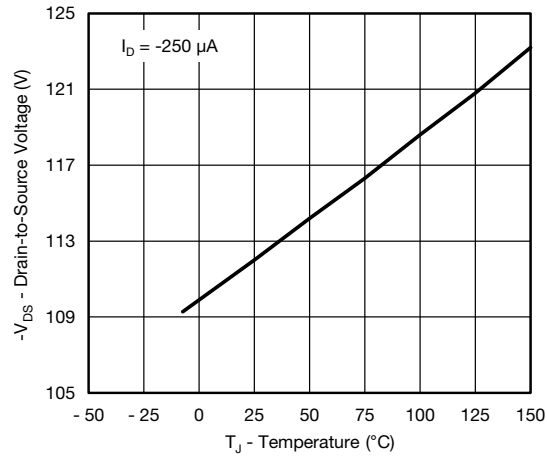
**Source-Drain Diode Forward Voltage**



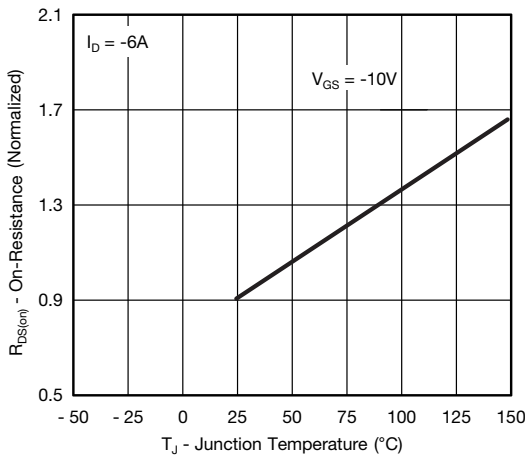
**Threshold Voltage**



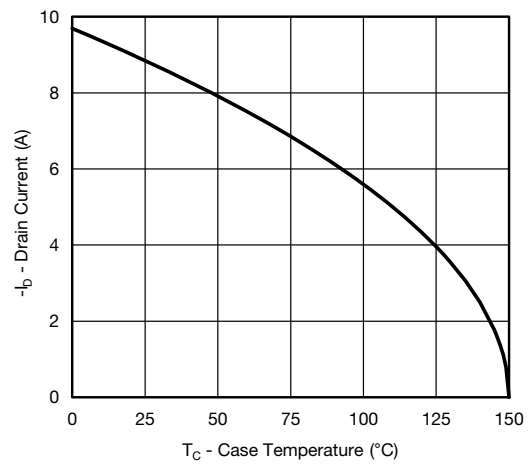
**Capacitance**



**Drain Source Breakdown vs. Junction Temperature**

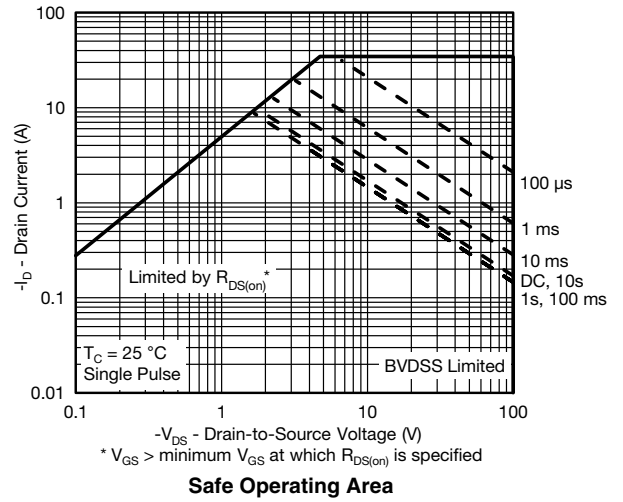
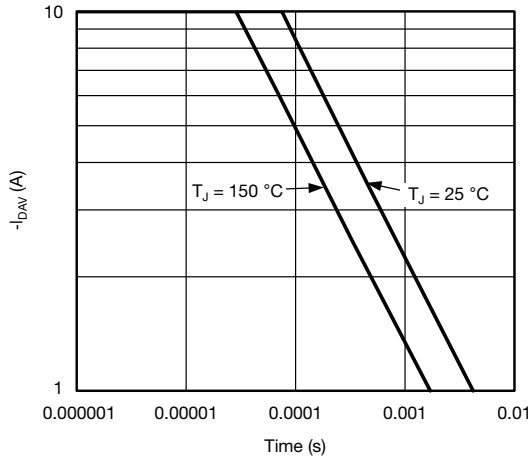


**On-Resistance vs. Junction Temperature**



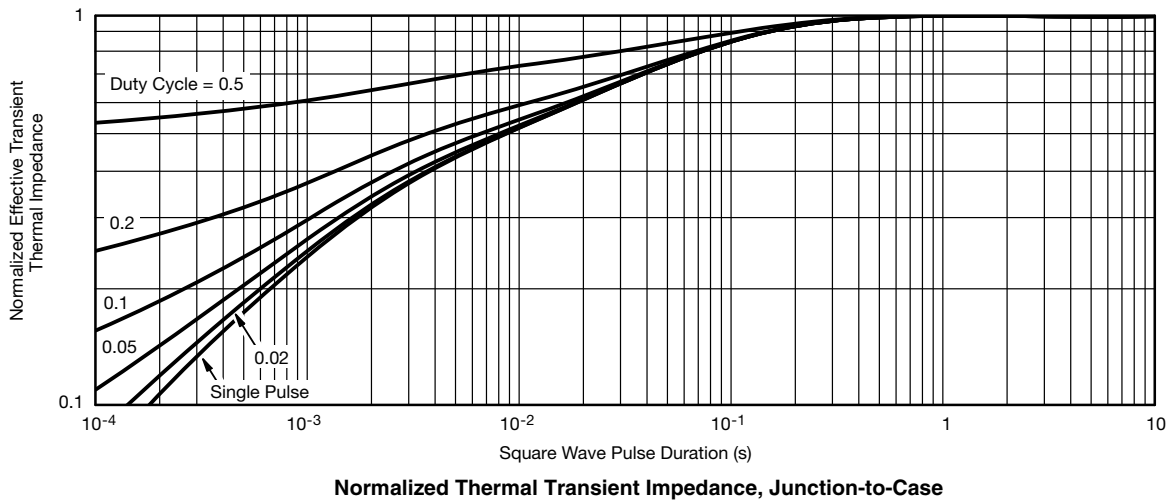
**Current Derating**

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



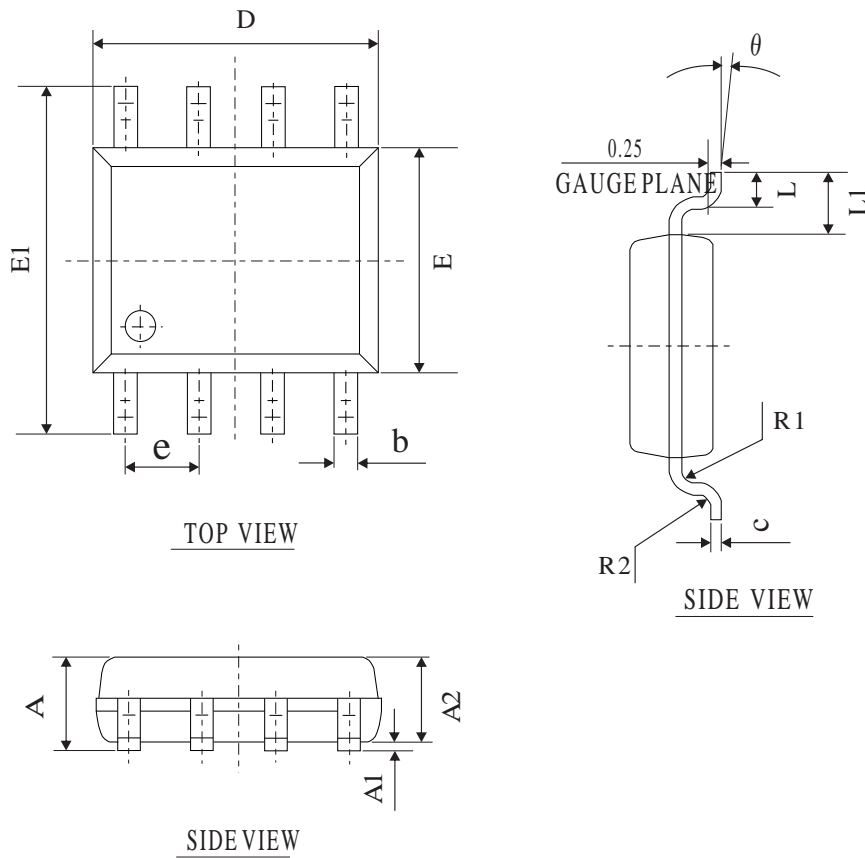
**Single Pulse Avalanche Current Capability vs. Time**

**Safe Operating Area**



**Normalized Thermal Transient Impedance, Junction-to-Case**

## SOP-8 PACKAGE OUTLINE



COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	1.30	1.60	1.85
A1	0.03	0.15	0.28
A2	1.20	1.45	1.70
b	0.26	0.40	0.54
C	0.132	0.203	0.273
D	4.50	4.90	5.30
E	3.50	3.00	4.30
E1	5.50	6.00	6.50
L	0.30	0.70	1.10
$\theta$	2°	4°	6°
L1	1.04REF		
e	1.27BSC		
R1	0.07TYP		
R2	0.07TYP		

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