Dual P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (mΩ)(Typ.)	I_D (A)	Q_g (Typ.)
- 20	16 at V_{GS} = - 4.5 V	- 10[a]	19.5 nC
	19 at V_{GS} = - 2.5 V		

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

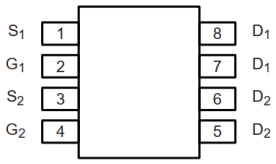
APPLICATIONS

- MB / VGA / Vcore
- POL Applications
- Networking

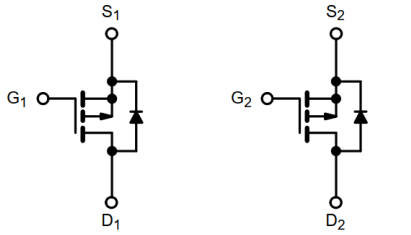
Pb-free

RoHS COMPLIANT

SOP-8



Top View



P-Channel MOSFET P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	- 20	V
Gate-Source Voltage		V_{GS}	± 12	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	- 10[a]	A
	T_C = 70 °C		- 7.2[a]	
	T_A = 25 °C		- 3[b, c]	
	T_A = 70 °C		- 1.2[b, c]	
Pulsed Drain Current (t = 300 µs)		I_{DM}	- 40	
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	- 9[a]	
	T_A = 25 °C		- 3[b, c]	
Maximum Power Dissipation	T_C = 25 °C	P_D	12	W
	T_C = 70 °C		7.7	
	T_A = 25 °C		2.8[b, c]	
	T_A = 70 °C		1.7[b, c]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150	°C
Soldering Recommendations (Peak Temperature)[e]			265	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient[b, e]	t ≤ 5 s	R_{thJA}	45	63	°C/W
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	4.0	5.5	

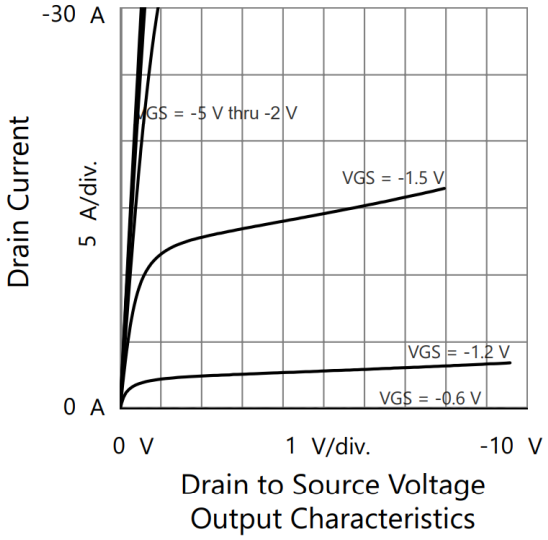
Notes:
a. Package limited.
b. Surface mounted on 1" x 1" FR4 board.
c. t = 5 s.
d. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
e. Maximum under steady state conditions is 80 °C/W.

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = - 250 µA	- 20			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = - 250 µA		- 10		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			2.8		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = - 250 µA	- 0.3		- 1.0	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 12 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = - 20 V, V_{GS} = 0 V			- 1	µA
		V_{DS} = - 16 V, V_{GS} = 0 V, T_J = 55 °C			- 10	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} = - 5 V, V_{GS} = - 4.5 V	- 10			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = - 4.5 V, I_D = - 4 A		16	20	mΩ
		V_{GS} = - 2.5 V, I_D = - 3 A		19	25	
Forward Transconductance[a]	g_{fs}	V_{DS} = - 10 V, I_D = - 4 A		10		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = - 10 V, V_{GS} = 0 V, f = 1 MHz		1820		pF
Output Capacitance	C_{oss}			210		
Reverse Transfer Capacitance	C_{rss}			204		
Total Gate Charge	Q_g	V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_D = - 4 A		19.5		nC
Gate-Source Charge	Q_{gs}			3		
Gate-Drain Charge	Q_{gd}			5		
Gate Resistance	R_g	f = 1 MHz		6		Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = - 10 V, R_L = 0.75 Ω I_D = - 4 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		11		ns
Rise Time	t_r			12		
Turn-Off Delay Time	$t_{d(off)}$			20		
Fall Time	t_f			28		
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = - 10 V, R_L = 0.75 Ω I_D = - 3 A, V_{GEN} = - 2.5 V, R_g = 1 Ω		20		
Rise Time	t_r			31		
Turn-Off Delay Time	$t_{d(off)}$			42		
Fall Time	t_f			30		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C			- 10	A
Pulse Diode Forward Current	I_{SM}				- 40	
Body Diode Voltage	V_{SD}	I_S = - 1 A, V_{GS} = 0 V			- 1	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = - 4 A, di/dt = 100 A/µs, T_J = 25 °C		38		ns
Body Diode Reverse Recovery Charge	Q_{rr}			20		nC
Reverse Recovery Fall Time	t_a			15		ns
Reverse Recovery Rise Time	t_b			17		

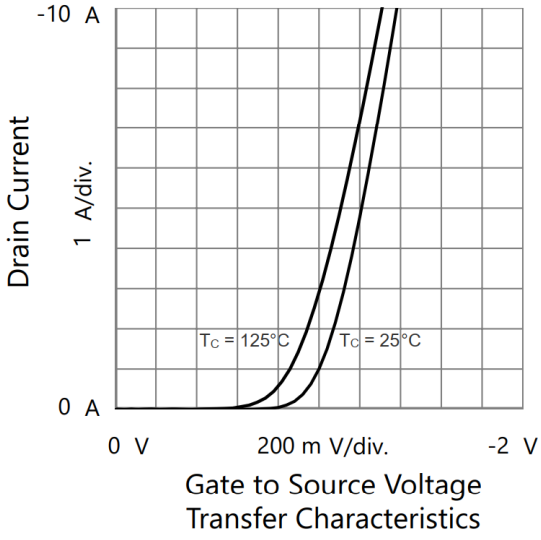
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

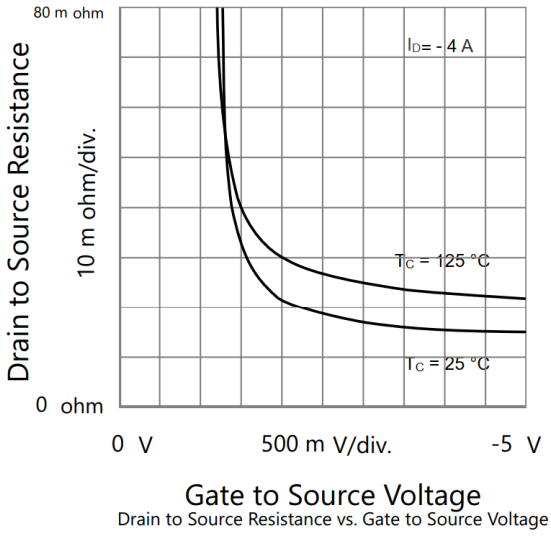
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



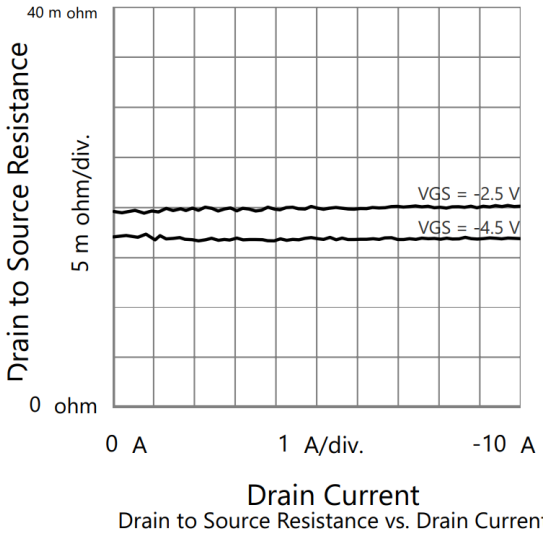
Drain to Source Voltage
Output Characteristics



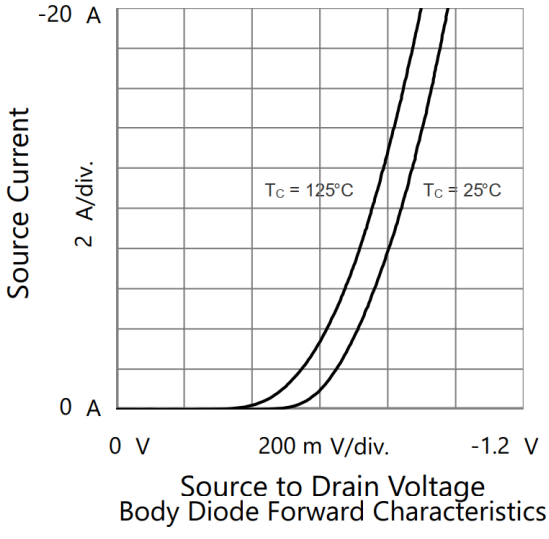
Gate to Source Voltage
Transfer Characteristics



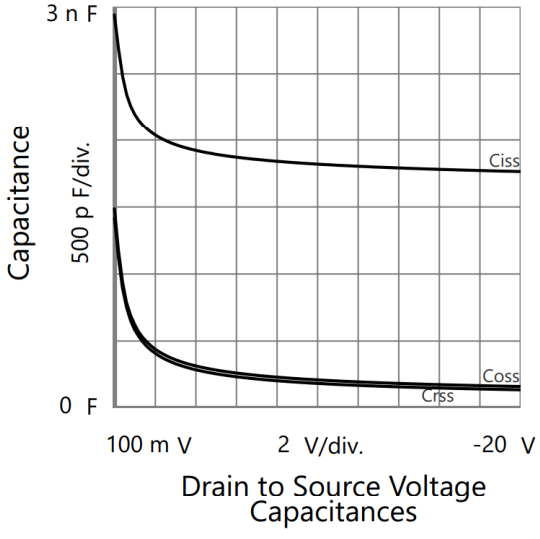
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



Drain Current
Drain to Source Resistance vs. Drain Current

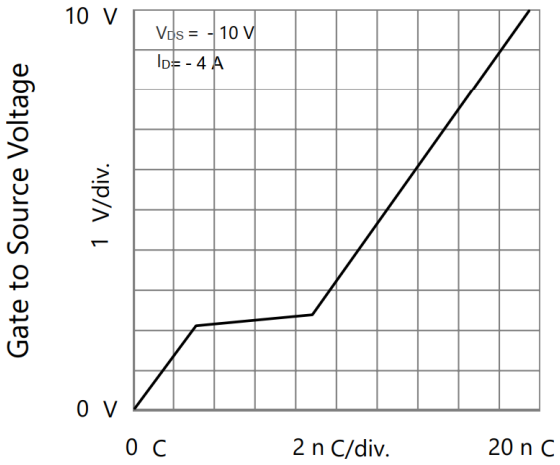


Source to Drain Voltage
Body Diode Forward Characteristics

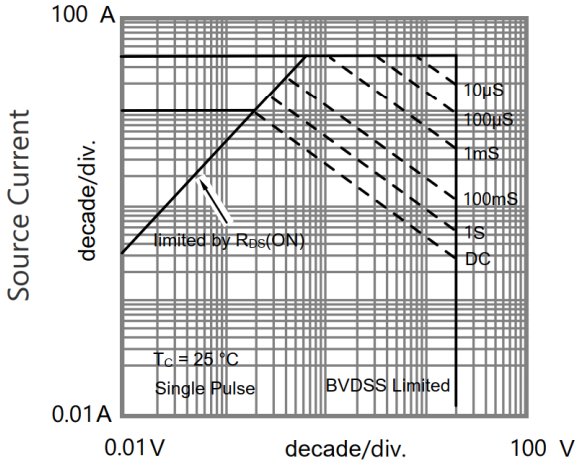


Drain to Source Voltage
Capacitances

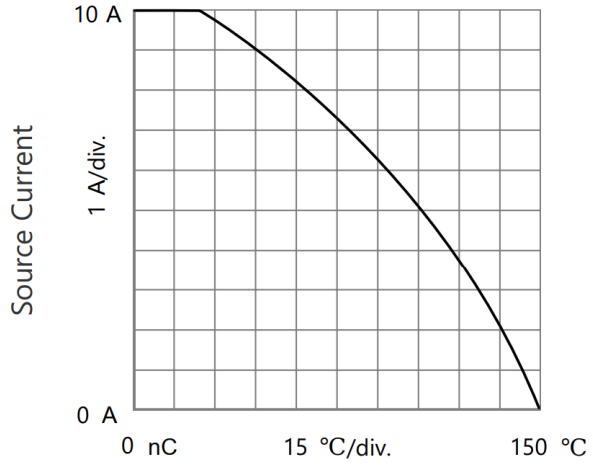
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



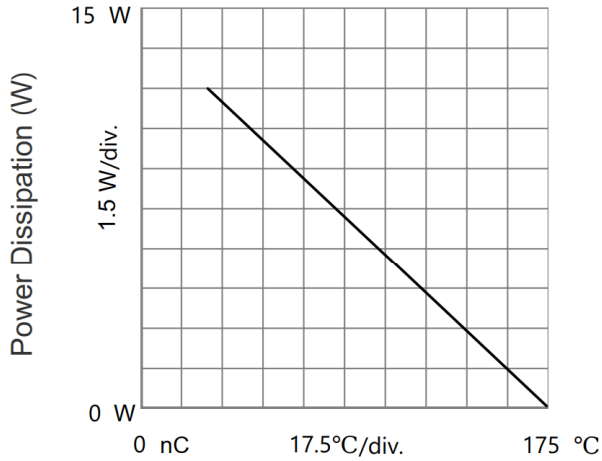
Gate to Source Voltage vs. GateCharge



Safe Operating Area, Junction-to-Ambient

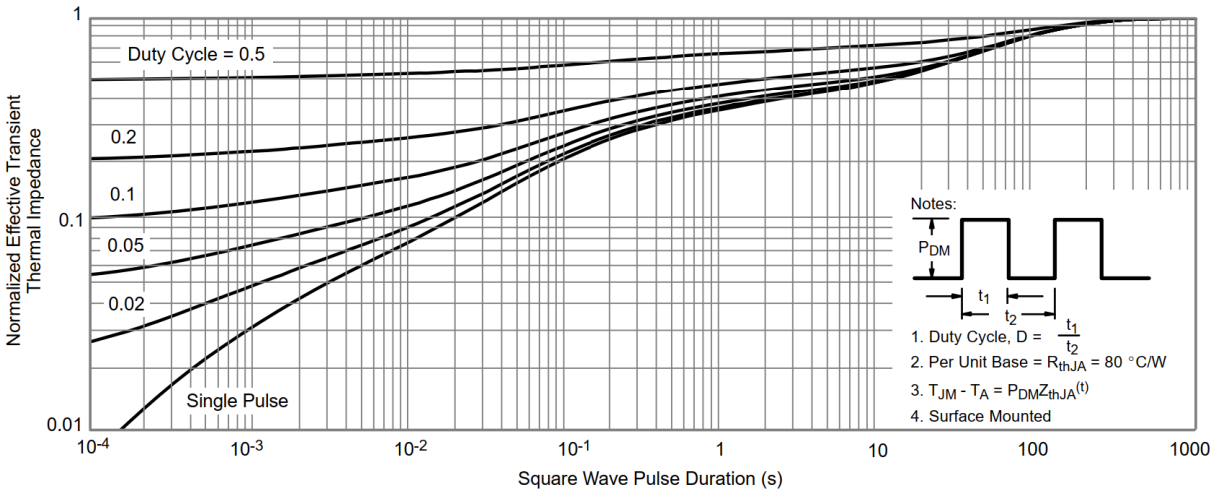


Current Derating

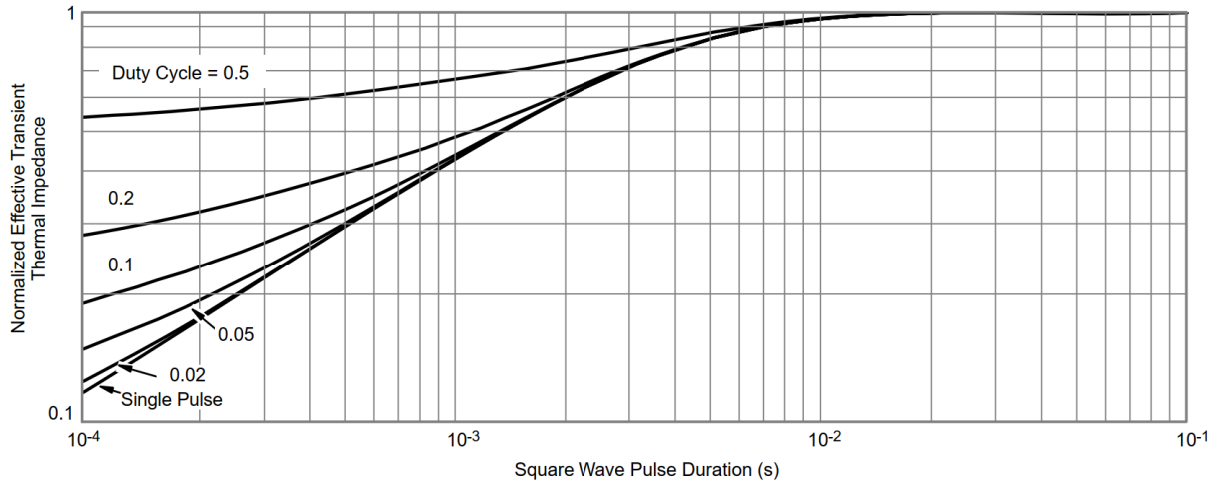


Current Derating

TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

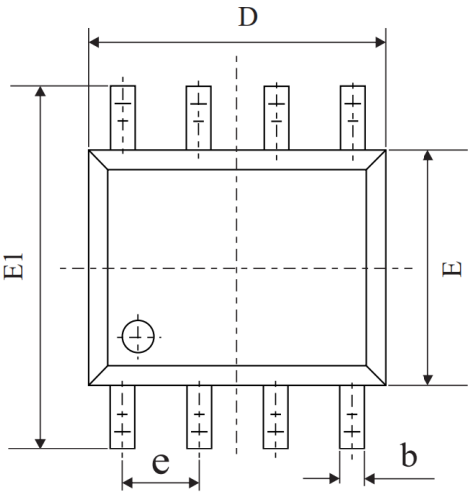


Normalized Thermal Transient Impedance, Junction-to-Ambient

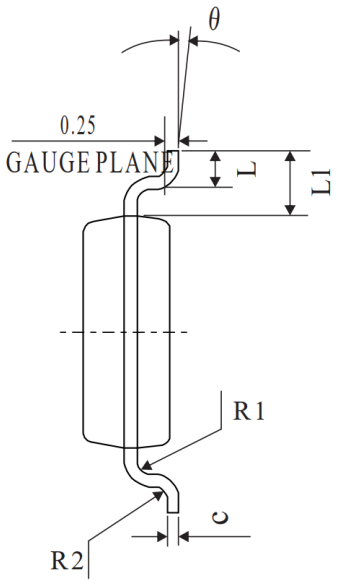


Normalized Thermal Transient Impedance, Junction-to-Case

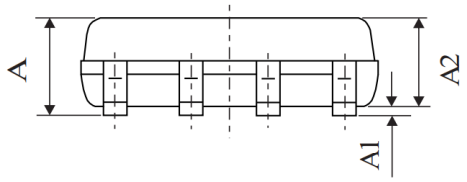
SOP-8 PACKAGE OUTLINE



TOP VIEW



SIDE VIEW



SIDE VIEW

COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	1.30	1.60	1.85
A1	0.03	0.15	0.28
A2	1.20	1.45	1.70
b	0.26	0.40	0.54
C	0.132	0.203	0.273
D	4.50	4.90	5.30
E	3.50	3.00	4.30
E1	5.50	6.00	6.50
L	0.30	0.70	1.10
θ	2°	4°	6°
L1	1.04REF		
e	1.27BSC		
R1	0.07TYP		
R2	0.07TYP		

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