

## N-Channel 20-V (D-S) Fast Switching MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ.)
20	0.005 at V <sub>GS</sub> = 4.5 V	19.5	17.5 nC
	0.007 at V <sub>GS</sub> = 2.5 V	15.5	

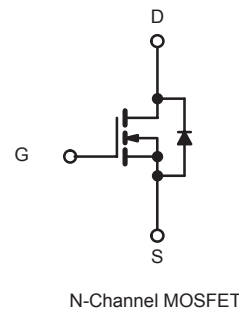
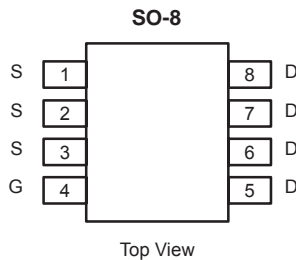
### FEATURES

- Halogen-free Option Available  
TrenchFET® Power MOSFET
- PWM Optimized
- 100 % R<sub>g</sub> Tested



### APPLICATIONS

- Synchronous Rectification



ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted					
Parameter	Symbol	10 s	Steady State	Unit	
Drain-Source Voltage	V <sub>DS</sub>	20		V	
Gate-Source Voltage	V <sub>GS</sub>	± 12			
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	I <sub>D</sub>	T <sub>A</sub> = 25 °C	19.5	12.5	A
		T <sub>A</sub> = 70 °C	15.6	10.0	
Pulsed Drain Current	I <sub>DM</sub>	60			
Continuous Source Current (Diode Conduction) <sup>a</sup>	I <sub>S</sub>	3.2	1.3		
Single Avalanche Current	I <sub>AS</sub> E <sub>AS</sub>	L = 0.1 mH	30		mJ
Single Avalanche Energy			45		
Maximum Power Dissipation <sup>a</sup>	P <sub>D</sub>	T <sub>A</sub> = 25 °C	3.8	1.5	W
		T <sub>A</sub> = 70 °C	2.0	0.8	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	
Soldering Recommendations (Peak Temperature) <sup>b, c</sup>		260			

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	R <sub>thJA</sub>	24	33	°C/W
	Steady State		65	81	
Maximum Junction-to-Case (Drain)	Steady State	R <sub>thJC</sub>	1.9	2.4	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.

c. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

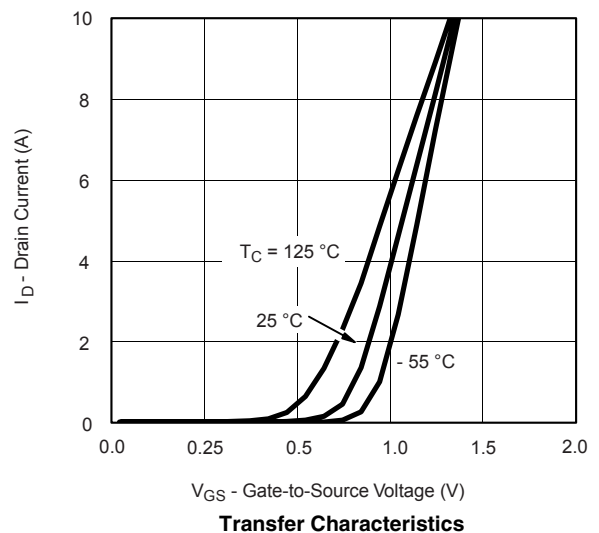
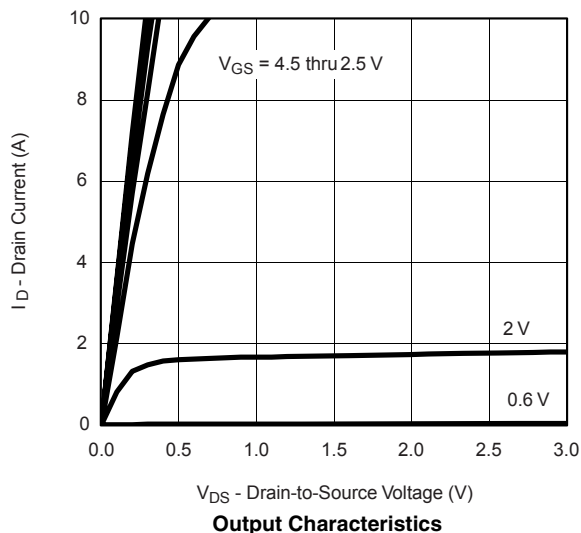
<b>MOSFET SPECIFICATIONS</b> $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	0.5		1.0	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\ \text{V}, V_{GS} = \pm 12\ \text{V}$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 20\ \text{V}, V_{GS} = 0\ \text{V}$			1	$\mu\text{A}$
		$V_{DS} = 20\ \text{V}, V_{GS} = 0\ \text{V}, T_J = 55\text{ }^\circ\text{C}$			5	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\ \text{V}, V_{GS} = 4.5\ \text{V}$	40			A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 4.5\ \text{V}, I_D = 6.5\ \text{A}$		0.005	0.007	$\Omega$
		$V_{GS} = 2.5\ \text{V}, I_D = 5.5\ \text{A}$		0.007	0.009	
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 15\ \text{V}, I_D = 19.5\ \text{A}$		105		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	$I_S = 3.2\ \text{A}, V_{GS} = 0\ \text{V}$		0.8	1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	$Q_g$	$V_{DS} = 10\ \text{V}, V_{GS} = 4.5\ \text{V}, I_D = 19.5\ \text{A}$		17.5	27	nC
Gate-Source Charge	$Q_{gs}$			6.6		
Gate-Drain Charge	$Q_{gd}$			2.8		
Gate Resistance	$R_g$	$f = 1\ \text{MHz}$	0.7	1.4	2.1	$\Omega$
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\ \text{V}, R_L = 10\ \Omega$ $I_D = 1\ \text{A}, V_{GEN} = 10\ \text{V}, R_g = 6\ \Omega$		25	40	ns
Rise Time	$t_r$			15	25	
Turn-Off Delay Time	$t_{d(off)}$			50	75	
Fall Time	$t_f$			12	20	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = 3.2\ \text{A}, di/dt = 100\ \text{A}/\mu\text{s}$		30	60	

Notes:

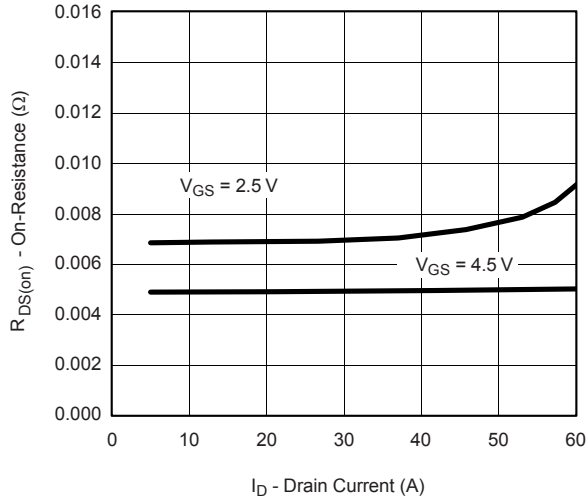
- a. Pulse test; pulse width  $\leq 300\ \mu\text{s}$ , duty cycle  $\leq 2\%$ .
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

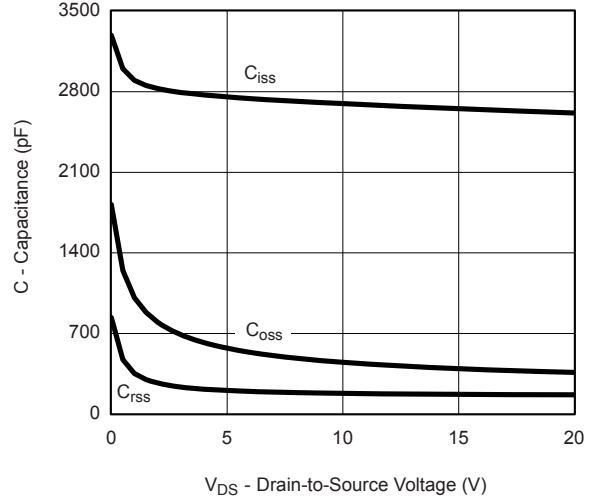
**TYPICAL CHARACTERISTICS**  $25\text{ }^\circ\text{C}$ , unless otherwise noted



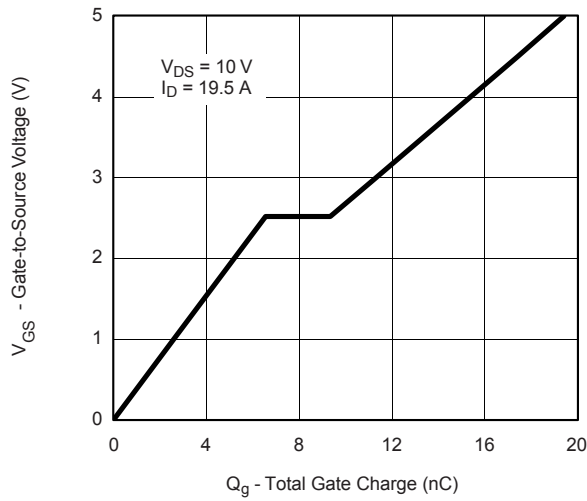
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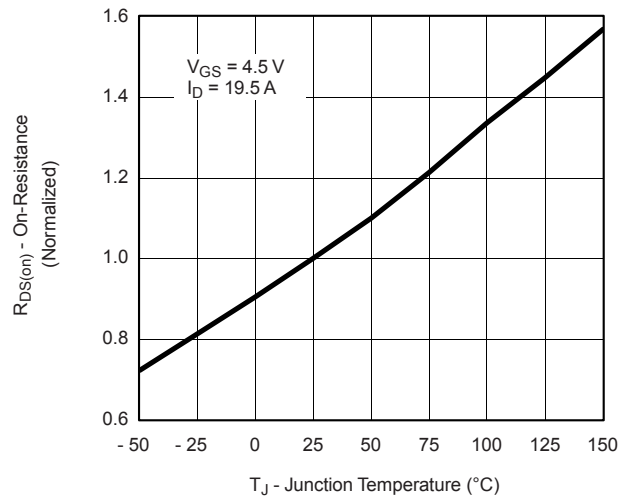
**On-Resistance vs. Drain Current**



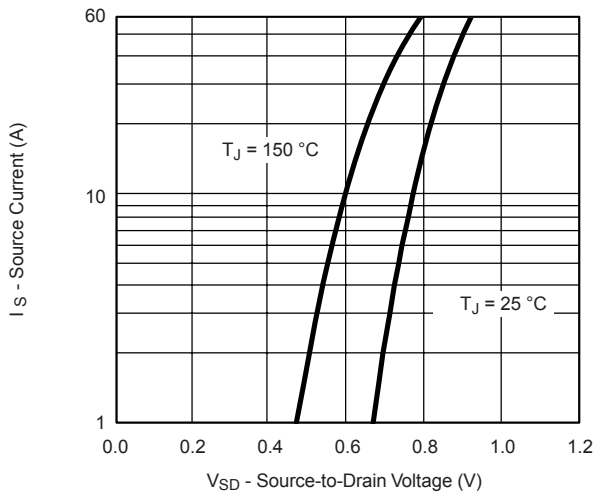
**Capacitance**



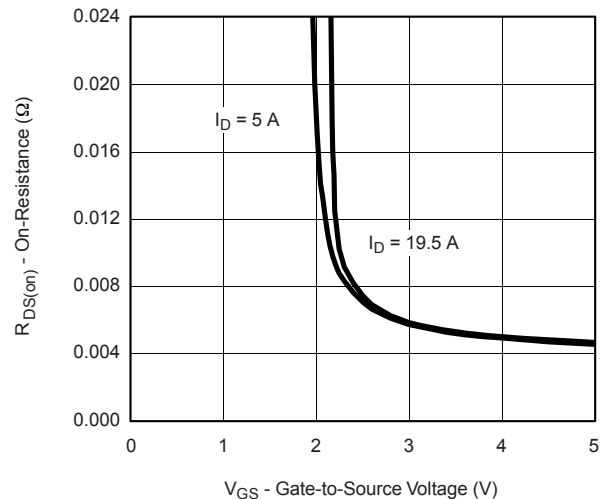
**Gate Charge**



**On-Resistance vs. Junction Temperature**

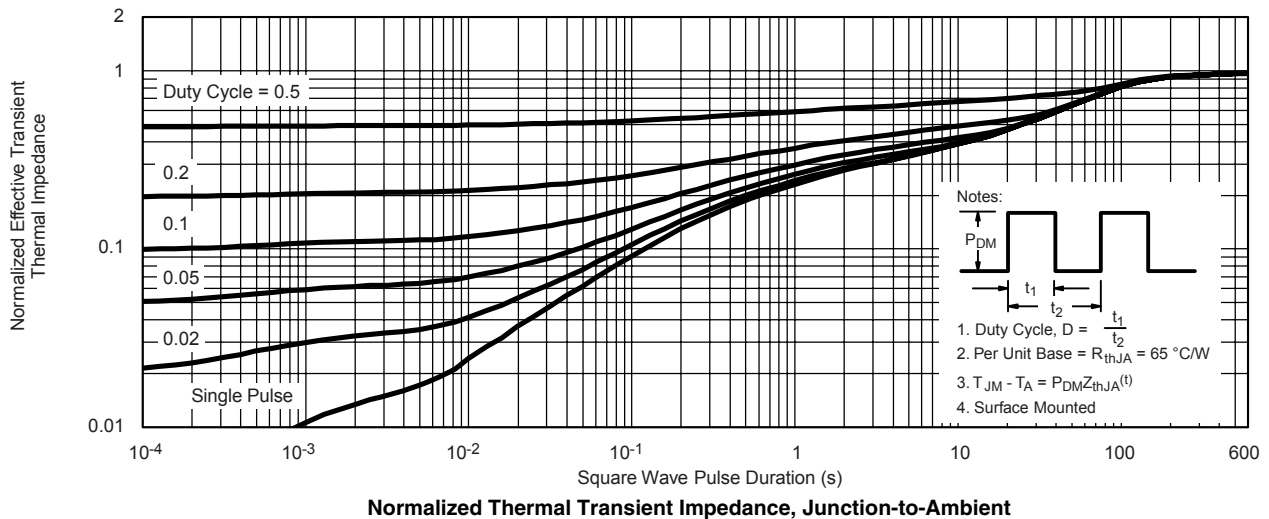
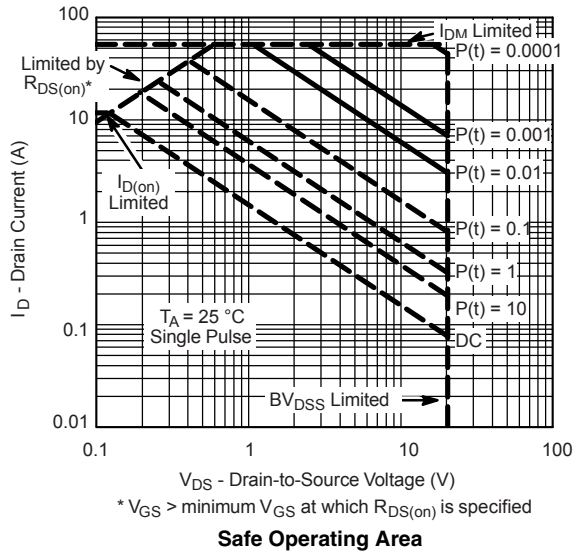
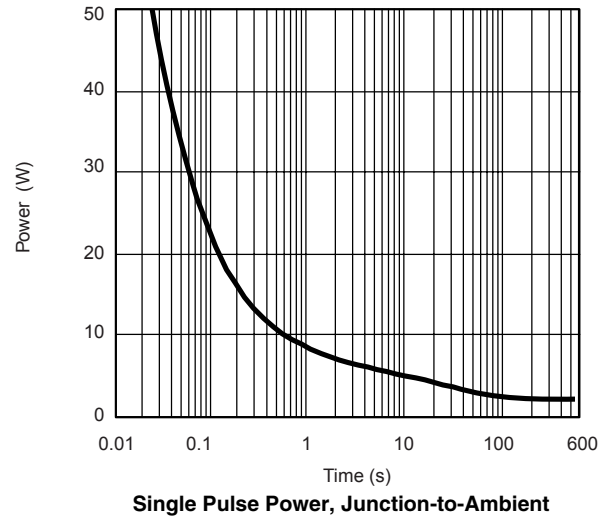
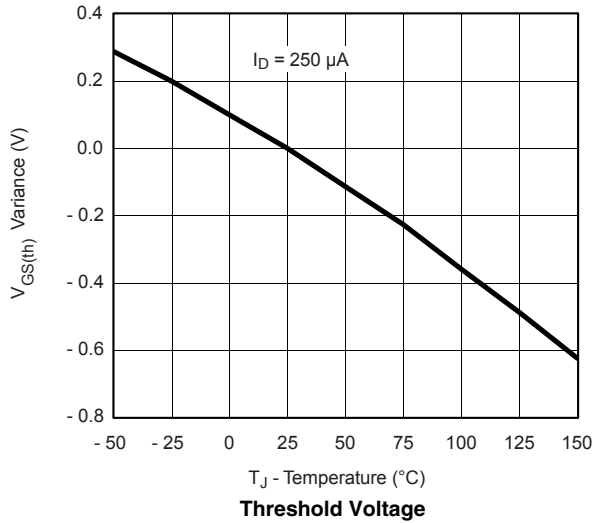


**Source-Drain Diode Forward Voltage**

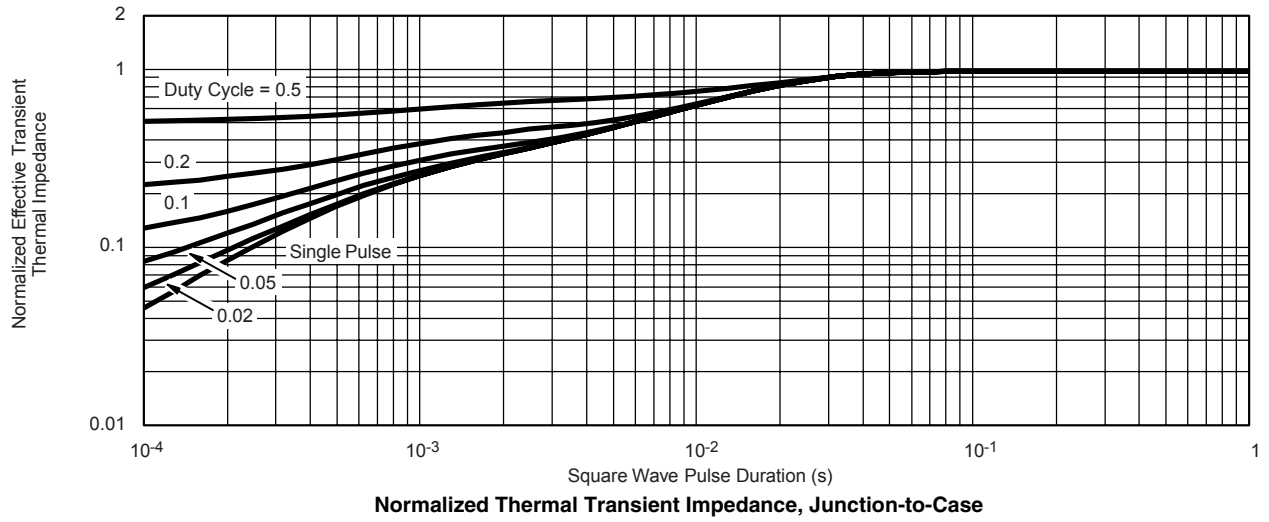


**On-Resistance vs. Gate-to-Source Voltage**

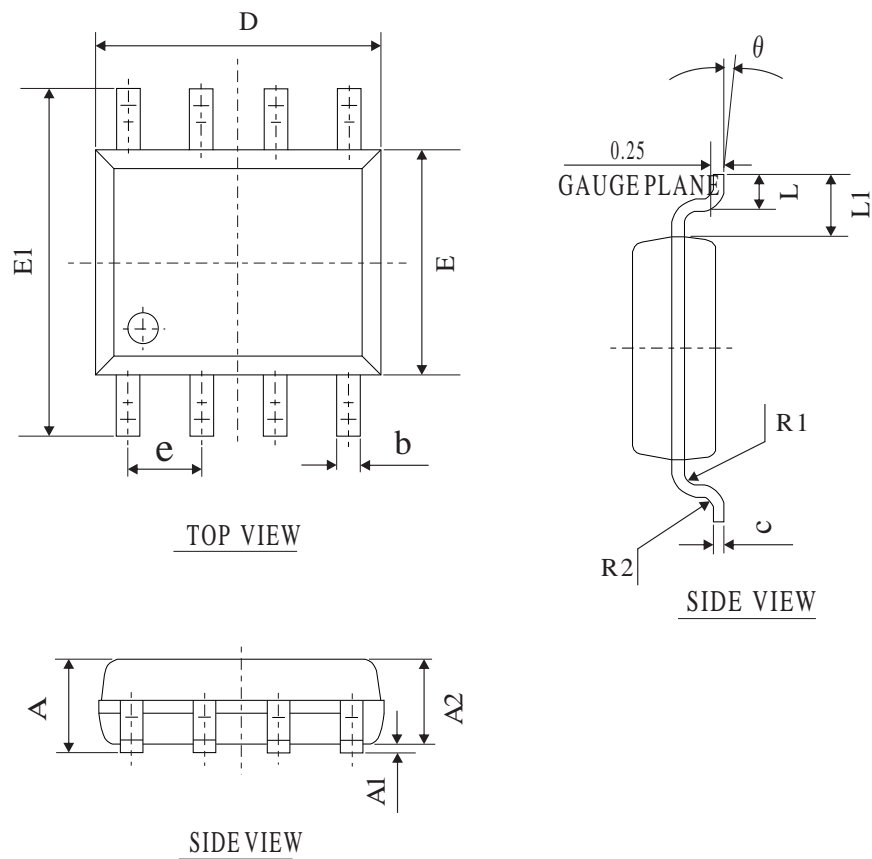
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



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## SOP-8 PACKAGE OUTLINE



COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	1.30	1.60	1.85
A1	0.03	0.15	0.28
A2	1.20	1.45	1.70
b	0.26	0.40	0.54
C	0.132	0.203	0.273
D	4.50	4.90	5.30
E	3.50	3.00	4.30
E1	5.50	6.00	6.50
L	0.30	0.70	1.10
$\theta$	2°	4°	6°
L1	1.04REF		
e	1.27BSC		
R1	0.07TYP		
R2	0.07TYP		

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