

P-Channel 30-V (D-S) MOSFET



RoHS
COMPLIANT

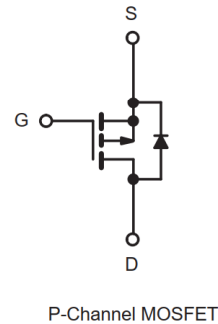
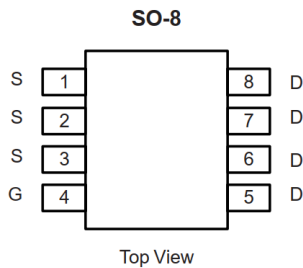
PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)
-30	9 at V _{GS} = -10 V	-13.5	57 nC
	12 at V _{GS} = -4.5 V		

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested

APPLICATIONS

- Load Switches
 - Notebook PCs
 - Desktop PCs



ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	- 30	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	- 13.5
		T _C = 70 °C	- 11.5
		T _A = 25 °C	-10.9 ^{a, b}
		T _A = 70 °C	-8.6 ^{a, b}
Pulsed Drain Current	I _{DM}	- 45	A
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	
		T _A = 25 °C	- 2.2 ^{a, b}
Avalanche Current	I _{AS}	- 20	mJ
Single-Pulse Avalanche Energy	E _{AS}	20	
Maximum Power Dissipation	P _D	T _C = 25 °C	5.0
		T _C = 70 °C	3.2
		T _A = 25 °C	2.7 ^{a, b}
		T _A = 70 °C	1.7 ^{a, b}
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R _{thJA}	38	46	°C/W
Maximum Junction-to-Foot	Steady State	R _{thJF}	20	25	

Notes:

- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under Steady State conditions is 85 °C/W.
- Based on T_C = 25 °C.

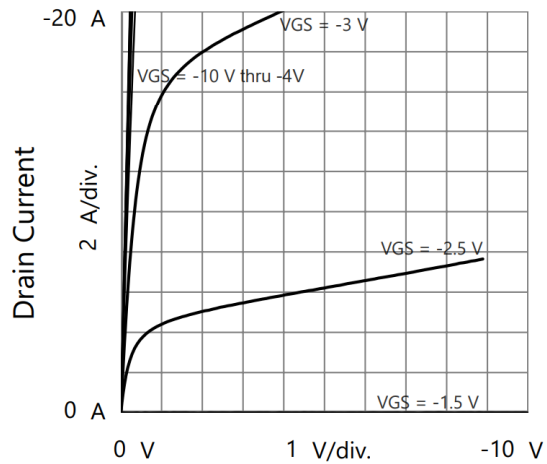
SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = -250\text{ }\mu\text{A}$	-30			V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-1		-3	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30\text{ V}, V_{GS} = 0\text{ V}$			-1	μA
		$V_{DS} = -30\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			-5	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq -10\text{ V}, V_{GS} = -10\text{ V}$	-15			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -10\text{ V}, I_D = -10\text{ A}$		9	12	m Ω
		$V_{GS} = -4.5\text{ V}, I_D = -8\text{ A}$		12	14	
Forward Transconductance ^a	g_{fs}	$V_{DS} = -10\text{ V}, I_D = -10\text{ A}$		28		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = -15\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		2970		pF
Output Capacitance	C_{oss}			320		
Reverse Transfer Capacitance	C_{rss}			315		
Total Gate Charge	Q_g	$V_{DS} = -15\text{ V}, V_{GS} = -10\text{ V}, I_D = -10\text{ A}$		57		nC
Gate-Source Charge	Q_{gs}			8		
Gate-Drain Charge	Q_{gd}			22		
Gate Resistance	R_g	$f = 1\text{ MHz}$		7		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -15\text{ V}, R_L = 1.5\text{ }\Omega$ $I_D \cong -10\text{ A}, V_{GEN} = -10\text{ V}, R_g = 1\text{ }\Omega$		14		ns
Rise Time	t_r			12		
Turn-Off Delay Time	$t_{d(off)}$			41		
Fall Time	t_f			10		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -15\text{ V}, R_L = 1.5\text{ }\Omega$ $I_D \cong -10\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 1\text{ }\Omega$		45		
Rise Time	t_r			90		
Turn-Off Delay Time	$t_{d(off)}$			35		
Fall Time	t_f			20		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			-13.5	A
Pulse Diode Forward Current	I_{SM}				-45	
Body Diode Voltage	V_{SD}	$I_S = -1\text{ A}, V_{GS} = 0\text{ V}$		-0.6	-1	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = -10\text{ A}, dI/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		28	45	ns
Body Diode Reverse Recovery Charge	Q_{rr}			15		nC
Reverse Recovery Fall Time	t_a			13		ns
Reverse Recovery Rise Time	t_b			15		

Notes:

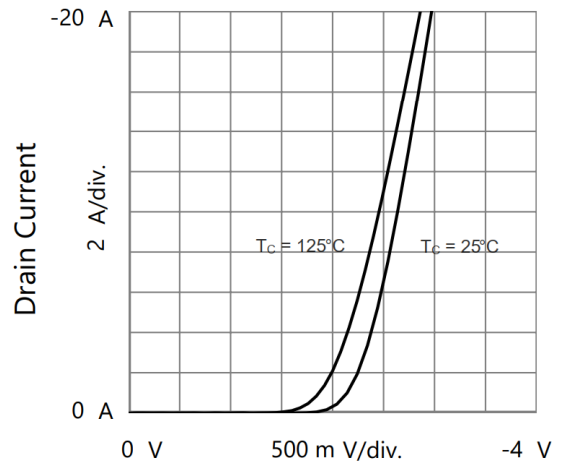
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

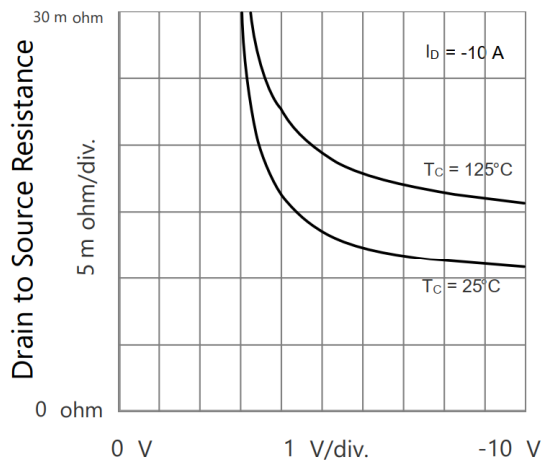
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



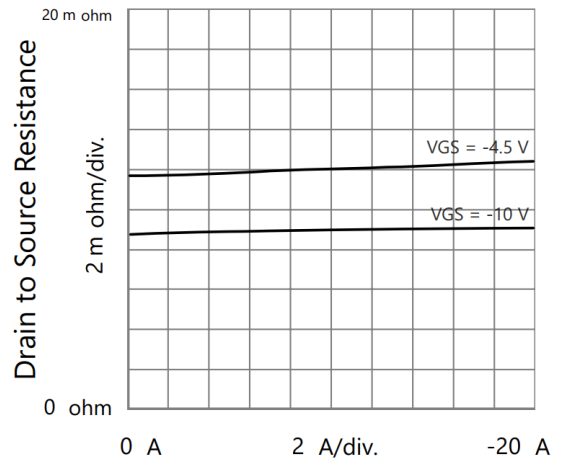
Drain to Source Voltage Output Characteristics



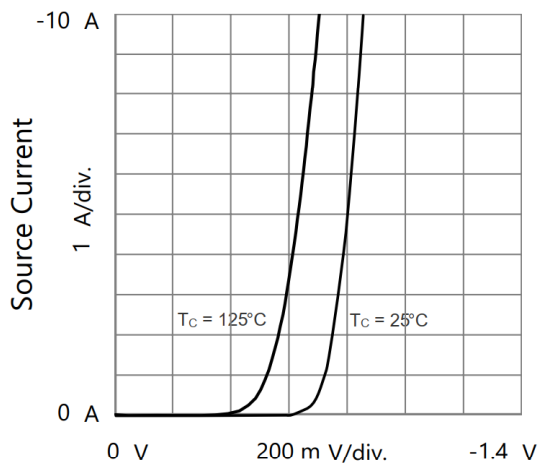
Gate to Source Voltage Transfer Characteristics



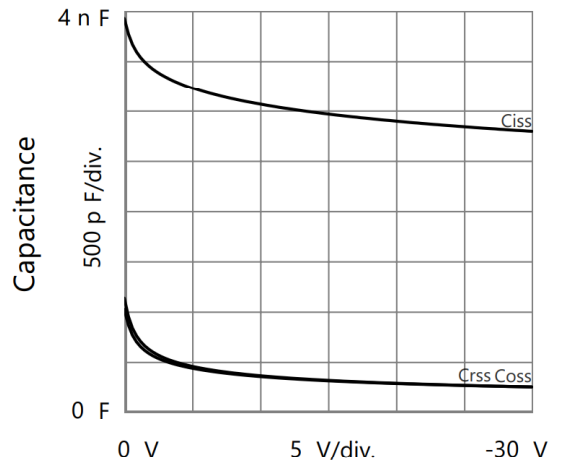
Gate to Source Voltage Drain to Source Resistance vs. Gate to Source Voltage



Drain Current Drain to Source Resistance vs. Drain Current

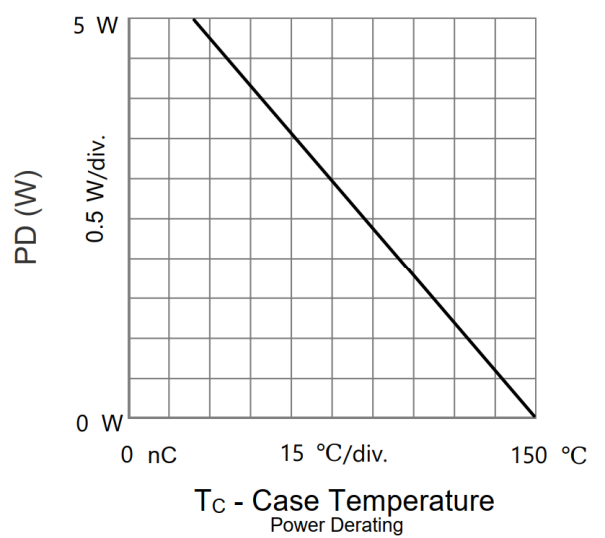
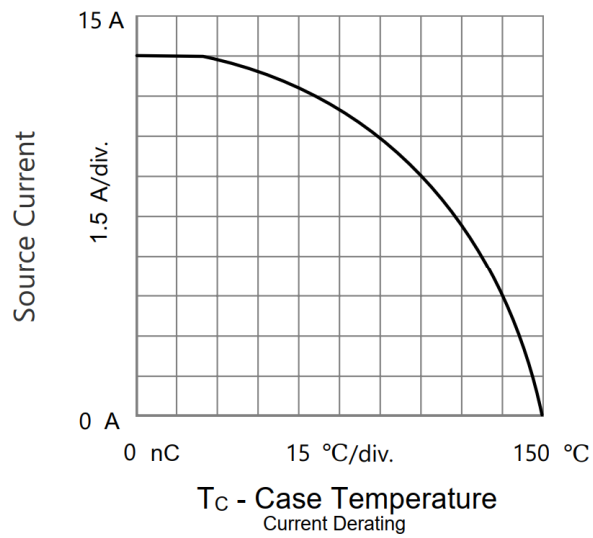
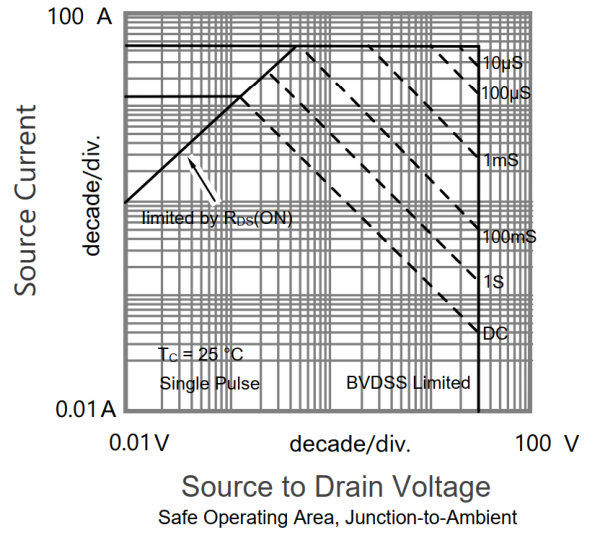
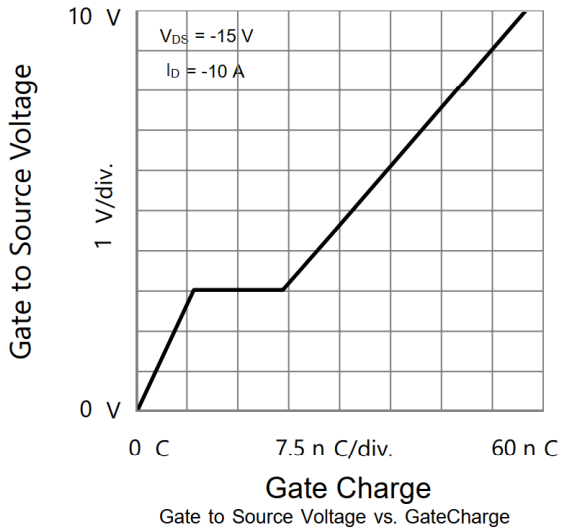


Source to Drain Voltage Body Diode Forward Characteristics

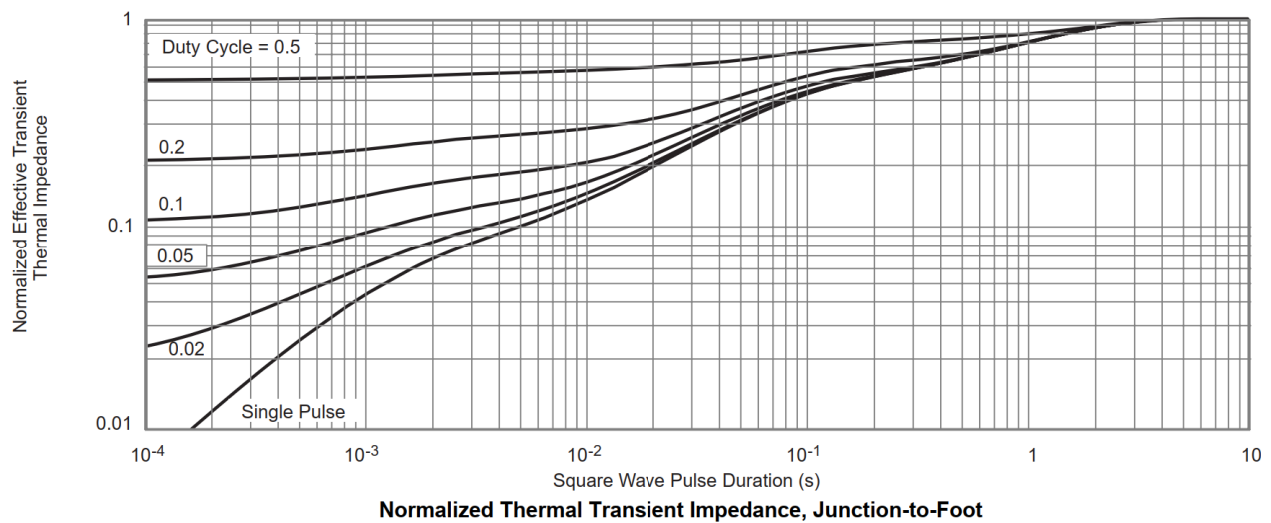
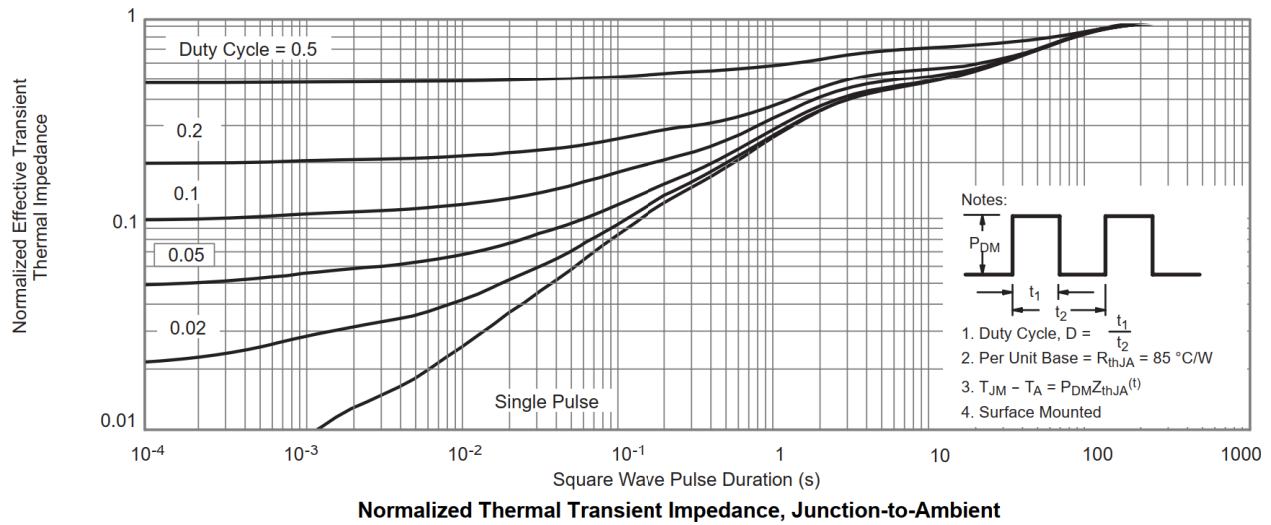


Drain to Source Voltage Capacitances

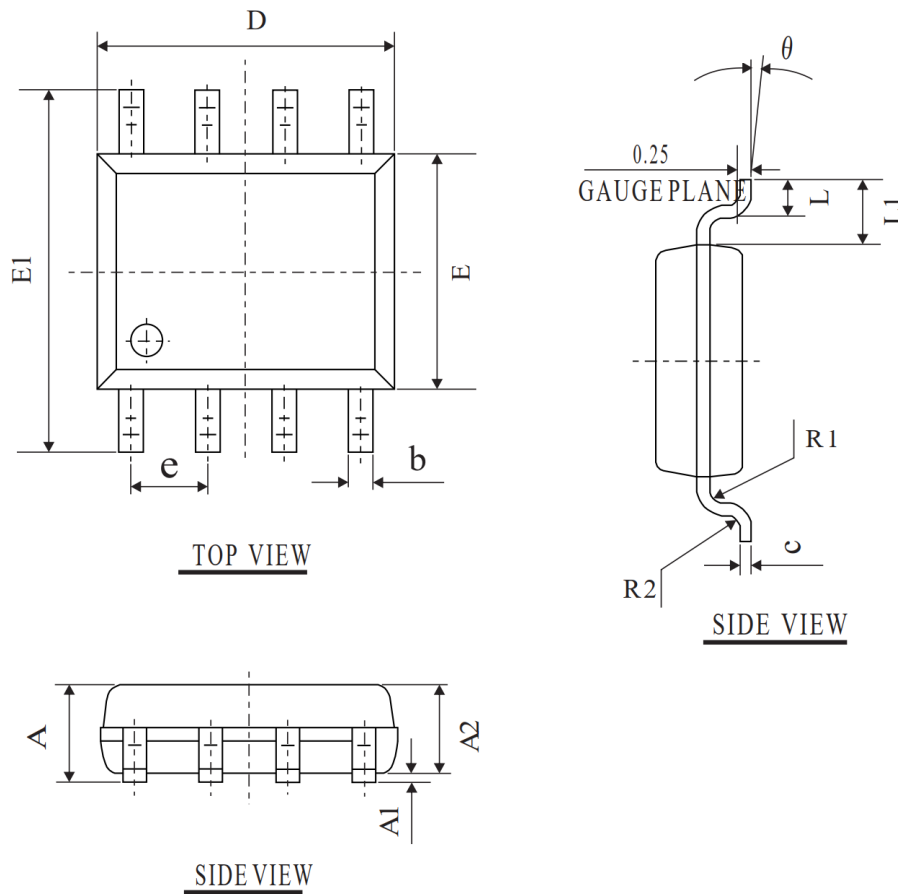
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



SOP-8 PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	1.30	1.60	1.85
A1	0.03	0.15	0.28
A2	1.20	1.45	1.70
b	0.26	0.40	0.54
C	0.132	0.203	0.273
D	4.50	4.90	5.30
E	3.50	3.00	4.30
E1	5.50	6.00	6.50
L	0.30	0.70	1.10
θ	2°	4°	6°
L1	1.04REF		
e	1.27BSC		
R1	0.07TYP		
R2	0.07TYP		

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