

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}$ (Ω)	I _D (A) ^a	Q _g (Typ.)			
- 30	0.0039 at V _{GS} = 10 V	- 21	80 nC			
- 30	0.006 at V _{GS} = 4.5 V	-18	60 HC			

\$0-8 S 1 8 D S 2 7 D S 3 6 D G 4 5 D

Top View

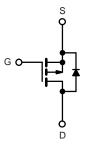
FEATURES

- DT-Trench Power MOSFET
- 100 % R_g Tested

RoHS

APPLICATIONS

- · Load Switches
 - Notebook PCs
 - Desktop PCs



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	- 30	V		
Gate-Source Voltage		V_{GS}	± 20]		
	T _C = 25 °C		- 21			
Continuous Prain Current (T. – 150 °C)	T _C = 70 °C	I _D	- 19.2			
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C		- 18 ^{b, c}			
	T _A = 70 °C		- 14.7 ^{b, c}	Α		
Pulsed Drain Current		I _{DM}	- 80			
Continous Source-Drain Diode Current	T _C = 25 °C	1-	- 22.5			
Continuos Source-Diain Diode Current	T _A = 25 °C	- I _S	- 17 ^{b, c}			
	T _C = 25 °C		6.1			
Maximum Power Dissipation	T _C = 70 °C	P _D	4] w		
	T _A = 25 °C		3.6 ^{b, c}] vv		
	T _A = 70 °C		2.2 ^{b, c}	1		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R_{thJA}	28	35	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	15	20		

Notes

- a. Based on $T_C = 25$ °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. Maximum under Steady State conditions is 85 °C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 30			V	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	- 1.2		- 2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Cata Valta na Duais Comunit	,	V _{DS} = - 24 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 24V, V _{GS} = 0 V, T _J = 55 °C			- 5	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 21			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = -15 A		0.0039	0.0049	Ω	
Diain-Source On-State Resistance		V _{GS} = - 4.5 V, I _D = - 10 A		0.006	0.0072		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 15 A		70		S	
Dynamic ^b							
Input Capacitance	C _{iss}			6129		pF	
Output Capacitance	C _{oss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1080			
Reverse Transfer Capacitance	C _{rss}]		701			
Total Cata Charge	Q_g	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 15 A		80	120		
Total Gate Charge				37		nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -10 \text{ A}$		13			
Gate-Drain Charge	Q_{gd}			19			
Gate Resistance	R_g	f = 1 MHz		90		Ω	
Turn-On Delay Time	t _{d(on)}			105		ns	
Rise Time	t _r	V_{DD} = - 15 V, R_L = 1.5 Ω		152			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -10 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		96			
Fall Time	t _f			55			
Turn-On Delay Time	$t_{d(on)}$			88			
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 1.5 \Omega$		106			
Turn-Off Delay Time	$t_{d(off)}$	$I_D \cong$ - 15 A, V_{GEN} = - 10 V, R_g = 1 Ω		72			
Fall Time	t _f			40			
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 21	Α	
Pulse Diode Forward Current	I _{SM}				- 80		
Body Diode Voltage	V_{SD}	I _S = - 10 A, V _{GS} = 0 V		- 0.7	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			19		ns	
Body Diode Reverse Recovery Charge	Q _{rr}	Q_{rr} $I_F = -15 \text{ A, dl/dt} = 100 \text{ A/µs, T}_J = 25 °C$		11		nC	
Reverse Recovery Fall Time	t _a	- 1, - 10 Λ, αι/αι - 100 Λ/μο, 1, - 20 0		10		ns	
Reverse Recovery Rise Time	t _b]		9			

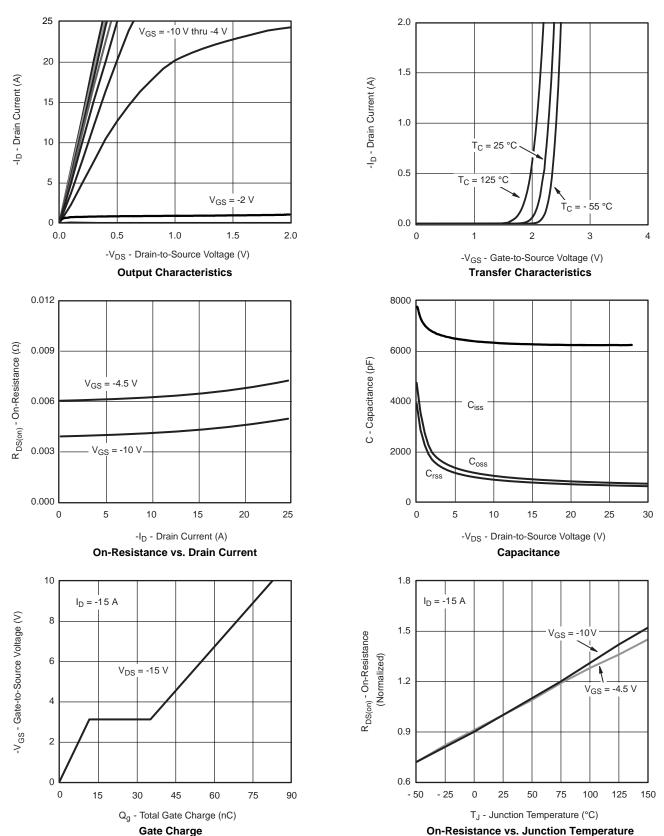
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

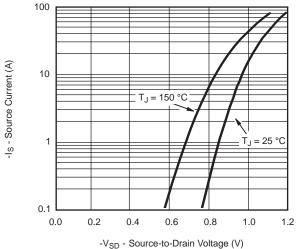


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

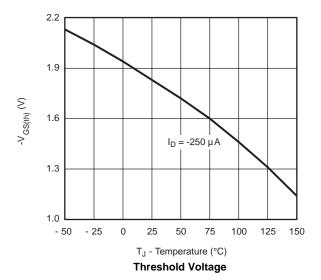




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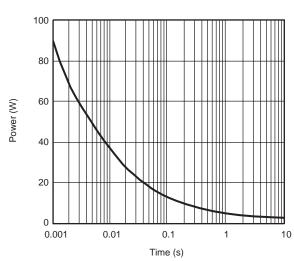


Source-Drain Diode Forward Voltage

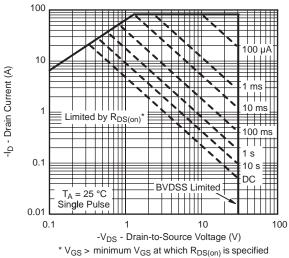


 $C_{\text{SO}} = 0.04$ $C_{\text{SO}} = 0.03$ $C_{\text{SO}} = 0.02$ $C_{\text{SO}} = 0.02$ $C_{\text{SO}} = 0.01$ $C_{\text{SO}} =$

On-Resistance vs. Gate-to-Source Voltage



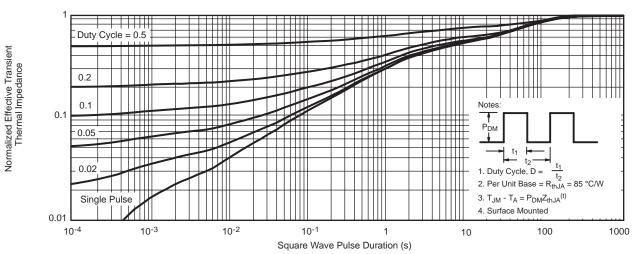
Single Pulse Power (Junction-to-Ambient)



Safe Operating Area, Junction-to-Ambient



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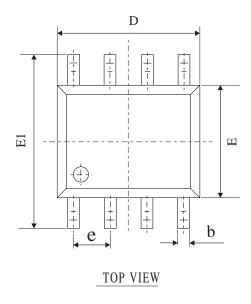


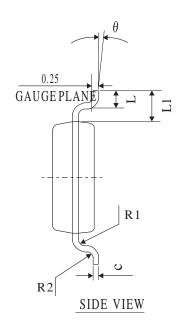
Normalized Thermal Transient Impedance, Junction-to-Ambient

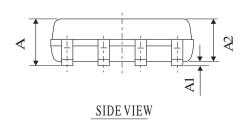




SOP-8 PACKAGE OUTLINE







COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX		
A	1.30	1.60	1.85		
A1	0.03	0.15	0.28		
A2	1.20	1.45	1.70		
b	0.26	0.40	0.54		
С	0.132	0.203	0.273		
D	4.50	4.90	5.30		
Е	3.50	3.00	4.30		
E1	5.50	6.00	6.50		
L	0.30	0.70	1.10		
θ	2°	4°	6°		
L1	1.04REF				
e	1.27BSC				
R1	0.07TYP				
R2	0.07TYP				





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