# **Dual N-Channel 60 V (D-S) MOSFET**

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>d</sup>	Q <sub>g</sub> (Typ.)			
60	0.0125 at V <sub>GS</sub> = 10 V	16	16.5 nC			
	0.018 at V <sub>GS</sub> = 4.5 V	12	16.5110			

#### **FEATURES**

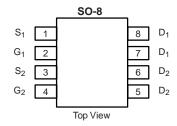
- DT-Trench Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested

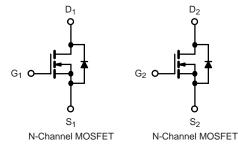


RoHS COMPLIANT

#### **APPLICATIONS**

• System power DC/DC





PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	60	V	
Gate-source voltage		V <sub>GS</sub>	± 20		
	T <sub>C</sub> = 25 °C		16ª		
Continuous drain augrent (T. – 150 °C)	T <sub>C</sub> = 70 °C	] , [	11 <sup>a</sup>	А	
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	8 <sup>a</sup>		
	T <sub>A</sub> = 70 °C	1 [	4.5 <sup>a</sup>		
Pulsed drain current		I <sub>DM</sub>	112	1	
On the desired world distance and	T <sub>C</sub> = 25 °C		16		
Source-drain current diode current	T <sub>A</sub> = 25 °C	l <sub>S</sub>	3 b, c		
	T <sub>C</sub> = 25 °C		25		
Maximum naviar dissination	T <sub>C</sub> = 70 °C	]	16	W	
Maximum power dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2.6 b, c		
	T <sub>A</sub> = 70 °C	1 [	1.5 <sup>b, c</sup>	1	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	- °C	
Soldering recommendations (peak temperature)			260	] '	

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYP.	MAX.	UNIT		
Maximum junction-to-ambient b, d	t ≤ 10 s	R <sub>thJA</sub>	20	35	°C/W	
Maximum junction-to-case (drain)	Steady state	R <sub>thJC</sub>	3.5	5	- C/VV	

#### Notes

- a. Package limited
- b. Surface mounted on 1" x 1" FR4 board
- c. t = 10 s
- d. Maximum under steady state conditions is 80 °C/W



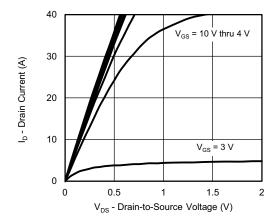
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static			•				
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60	-	-	V	
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA	-	38	-		
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA	-	-4.9	-	mV/°C	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0	-	3.0	V	
Gate-body leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	100	nA	
Zana mata walka sa alusia awumant	I <sub>DSS</sub>	V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V	-	-	1	μА	
Zero gate voltage drain current		V <sub>DS</sub> = 48 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C	-	-	10		
On-state drain current <sup>b</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	16	-	-	Α	
Duning and the second of the s	Б	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8 A	-	0.0125	0.016	Ω	
Drain-source on-state resistance b	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 6 \text{ A}$	-	0.018	0.025		
Forward transconductance b	9 <sub>fs</sub>	$V_{DS} = 30 \text{ V}, I_{D} = 8 \text{ A}$	-	38	-	S	
Dynamic <sup>a</sup>							
Input capacitance	C <sub>iss</sub>		-	1050	-	pF	
Output capacitance	C <sub>oss</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	235	-		
Reverse transfer capacitance	C <sub>rss</sub>		-	20	-		
Total gate charge	Q <sub>g</sub> -	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 8 A	-	16.5	-	nC	
			-	7.8	-		
Gate-source charge	Q <sub>gs</sub>	$V_{DS} = 30 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 6 \text{ A}$	-	3.4	-		
Gate-drain charge	Q <sub>gd</sub>		-	1.3	-		
Gate resistance	R <sub>g</sub>	f = 1 MHz	-	5.0	-	Ω	
Turn-on delay time	t <sub>d(on)</sub>		-	15	-		
Rise time	t <sub>r</sub>	$V_{DD}$ = 30 V, $R_L$ = 3.45 $\Omega$	-	40	-		
Turn-off delay time	t <sub>d(off)</sub>	$I_D\cong 6.0$ A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 $\Omega$	-	17	-		
Fall time	t <sub>f</sub>		-	35	-	1	
Turn-on delay time	t <sub>d(on)</sub>		-	8	-	ns -	
Rise time	t <sub>r</sub>	$V_{DD} = 30 \text{ V}, \text{ R}_{L} = 3.45 \Omega$	-	20	-		
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong 8.0 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	10	-		
Fall time	t <sub>f</sub>		-	19	-		
<b>Drain-Source Body Diode Characteristics</b>	•		_				
Continuous source-drain diode Current	Is	T <sub>C</sub> = 25 °C	-	-	16	_	
Pulse diode forward current <sup>a</sup>	I <sub>SM</sub>		-	-	112	A	
Body diode voltage	V <sub>SD</sub>	I <sub>S</sub> = 8.0 A	-	0.7	1.2	V	
Body diode reverse recovery time	t <sub>rr</sub>		-	35	53	ns	
Body diode reverse recovery charge	Q <sub>rr</sub>	$I_F = 8.7 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s},$	-	30	40	nC	
Reverse recovery fall time	t <sub>a</sub>	T <sub>J</sub> = 25 °C		11	-		
Reverse recovery rise time	t <sub>b</sub>		_	18	_	ns	

#### Notes

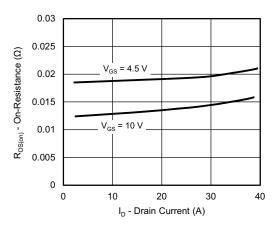
- a. Guaranteed by design, not subject to production testing
- b. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

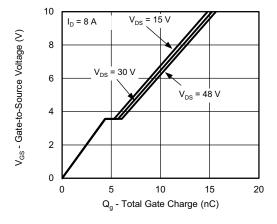




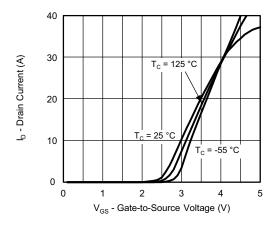
#### **Output Characteristics**



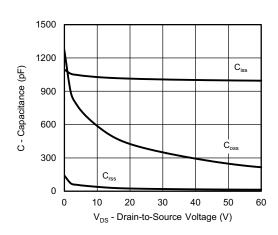
On-Resistance vs. Drain Current



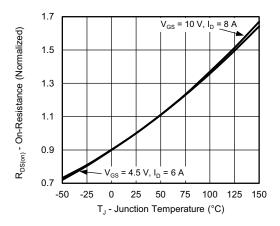
**Gate Charge** 



**Transfer Characteristics** 

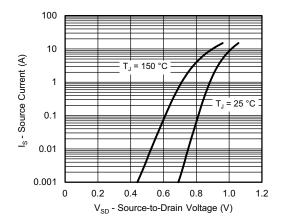


Capacitance

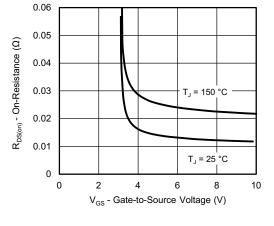


On-Resistance vs. Junction Temperature

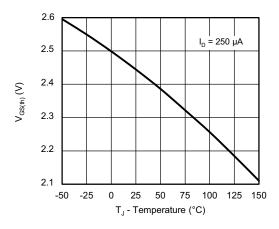




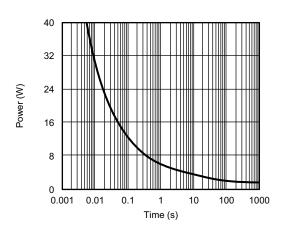
Source-Drain Diode Forward Voltage



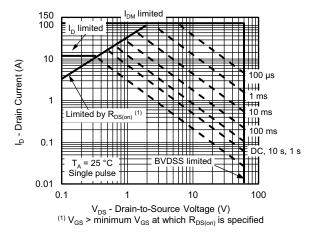
On-Resi.0stance vs. Gate-to-Source Voltage



**Threshold Voltage** 

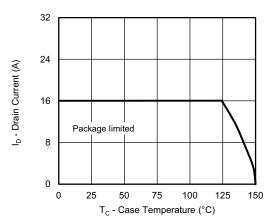


Single Pulse Power

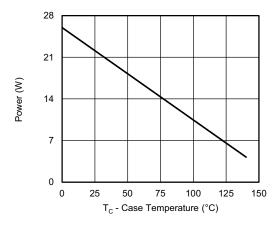


Safe Operating Area, Junction-to-Ambient

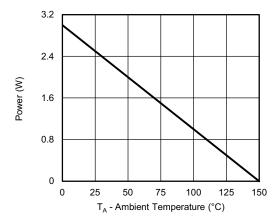




#### Current Derating a



Power, Junction-to-Case

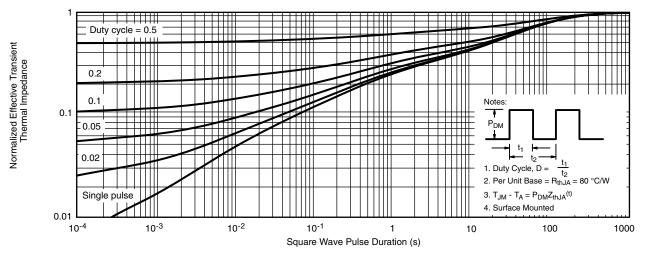


Power, Junction-to-Ambient

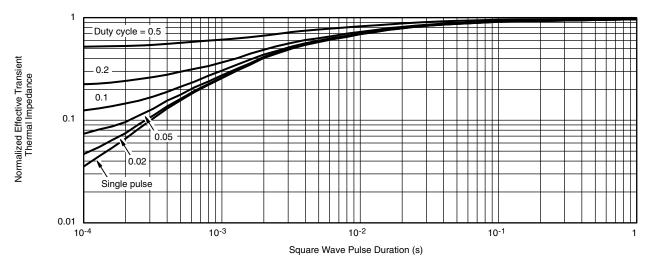
#### Note

a. The power dissipation  $P_D$  is based on  $T_J$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



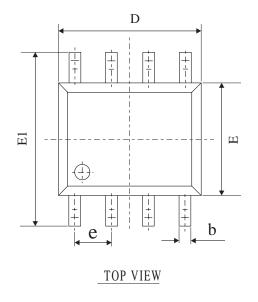


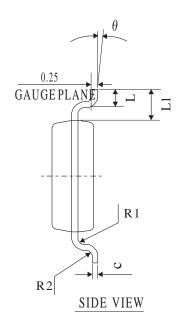
Normalized Thermal Transient Impedance, Junction-to-Ambient

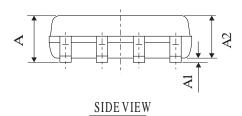


Normalized Thermal Transient Impedance, Junction-to-Case

# **SOP-8 PACKAGE OUTLINE**







COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX		
A	1.30	1.60	1.85		
A1	0.03	0.15	0.28		
A2	1.20	1.45	1.70		
ь	0.26	0.40	0.54		
С	0.132	0.203	0.273		
D	4.50	4.90	5.30		
Е	3.50	3.00	4.30		
E1	5.50	6.00	6.50		
L	0.30	0.70 1.10			
θ	2°	2° 4° 6°			
L1	1.04REF				
e	1.27BSC				
R1	0.07TYP				
R2	0.07TYP				





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