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N-Channel 60 V (D-S) MOSFET

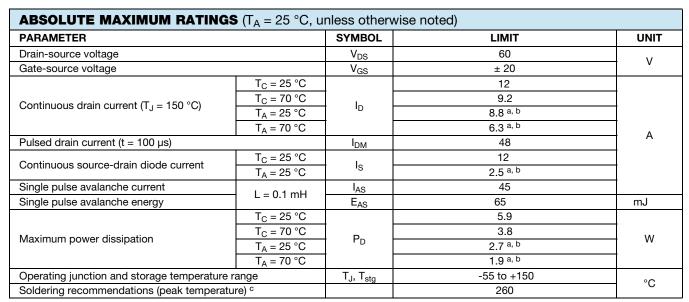
PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^d	Q _g (Тур.)	
60	0.010 at V _{GS} = 10 V	12	10.5 nC	
60	0.015 at V _{GS} = 4.5 V	12	10.5 HC	

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS tested

APPLICATIONS

- DC/DC converters
- Power supplies
- Motor drive control
- Battery and load switch



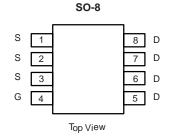
THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum junction-to-ambient ^a	t ≤ 10 s	R _{thJA}	32	50	°C/W	
Maximum junction-to-foot (drain)	Steady state	R _{thJF}	20	28	0/10	

Notes

a. Surface mounted on 1" x 1" FR4 board

b. t = 10 s

c. Maximum under steady state conditions is 85 °C/W



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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static			•	•			
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$	60	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	$\Lambda V_{DS}/T_{I}$		33	-	m)//°C	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA	-	-4.8	-	mV/°C	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1	-	3	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	100	nA	
	I _{DSS}	$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	1	μΑ	
Zero gate voltage drain current		$V_{DS} = 48 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$	-	-	10		
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	12	-	-		
		V _{GS} =10 V, I _D = 10 A	-	0.0100	0.0130		
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 5 \text{ A}$	-	0.0150	0.0190	Ω	
Forward transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 10 A	-	39	-	S	
Dynamic ^b			•	•			
Input capacitance	C _{iss}		-	1090	-	pF	
Output capacitance	C _{oss}	$V_{DS} = 48 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	-	530	-		
Reverse transfer capacitance	C _{rss}		-	25	-		
Total gata abayaa	Q _g	$V_{DS} = 48 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$	-	10.5	-		
Total gate charge			-	5.2	8	nC	
Gate-source charge	Q _{gs}	V_{DS} = 48 V, V_{GS} = 4.5 V, I_D = 5 A	-	2.2	-		
Gate-drain charge	Q _{gd}		-	1.1	-		
Gate resistance	R _g	f = 1 MHz	-	3	-	Ω	
Turn-on delay time	t _{d(on)}		-	7	15	-	
Rise time	t _r	$V_{DD} = 48 \text{ V}, \text{ R}_{L} = 6 \Omega, \text{ I}_{D} \cong 5 \text{ A},$	-	21	40		
Turn-off delay time	t _{d(off)}	$V_{GEN} = 10 \text{ V}, \text{ R}_{g} = 1 \Omega$	-	10	20		
Fall time	t _f		-	10	20		
Turn-on delay time	t _{d(on)}		-	13	25	ns	
Rise time	t _r	$V_{DD} = 48 \text{ V}, \text{ R}_{\text{I}} = 6 \Omega, \text{ I}_{\text{D}} \cong 5 \text{ A},$	-	25	50	-	
Turn-off delay time	t _{d(off)}	$V_{\text{GEN}} = 4.5 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$	-	10	20		
Fall time	t _f		-	22	45		
Drain-Source Body Diode Characteristi	cs		•	•		1	
Continuous source-drain diode current	Is	T _C = 25 °C	-	-	12	A	
Pulse diode forward current	I _{SM}		-	-	48		
Body diode voltage	V _{SD}	$I_{\rm S} = 5$ A, $V_{\rm GS} = 0$ V	-	0.70	1.2	V	
Body diode reverse recovery time	t _{rr}		-	30	60	ns	
Body diode reverse recovery charge	Q _{rr}		-	60	120	nC	
Reverse recovery fall time	t _a	$I_F = 5 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, \text{T}_J = 25 \text{ °C}$		15	-		
Reverse recovery rise time	t _b		-	15	-	ns	

Notes

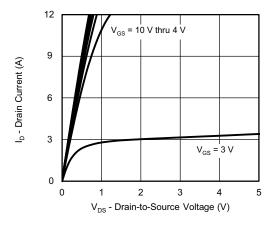
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

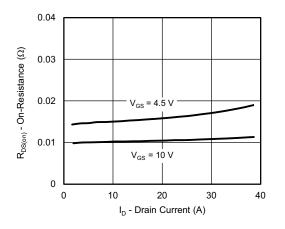
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



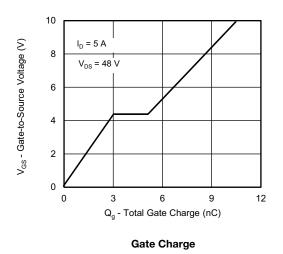
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

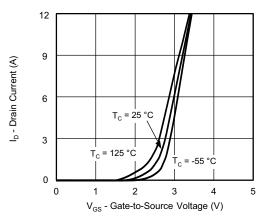


Output Characteristics

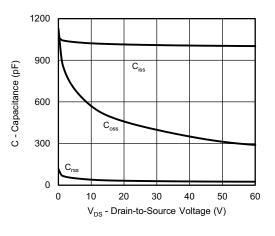


On-Resistance vs. Drain Current and Gate Voltage

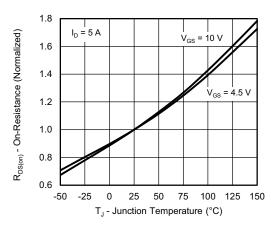




Transfer Characteristics



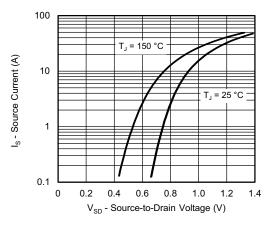
Capacitance



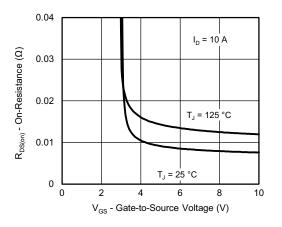
On-Resistance vs. Junction Temperature



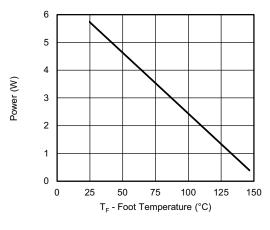
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



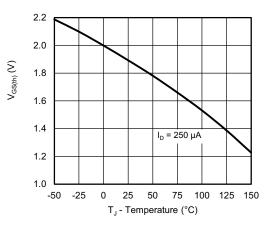
Source-Drain Diode Forward Voltage



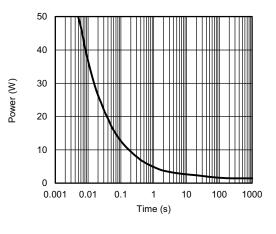
On-Resistance vs. Gate-to-Source Voltage



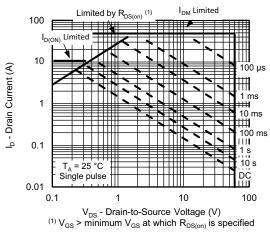
Power, Junction-to-Foot



Threshold Voltage



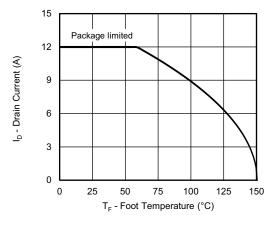
Single Pulse Power, Junction-to-Ambient



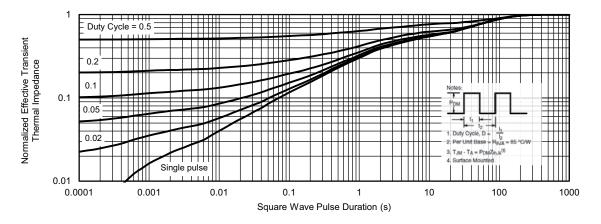
Safe Operating Area, Junction-to-Ambient



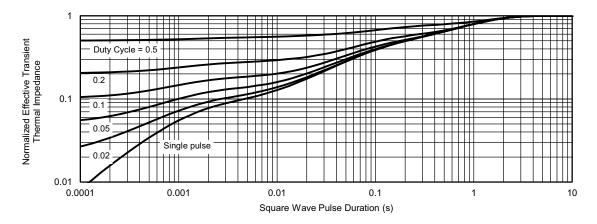
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating ^a

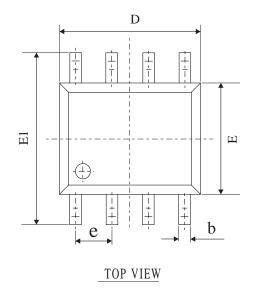


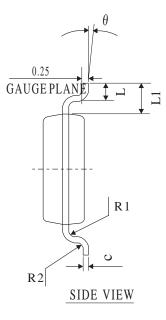
Normalized Thermal Transient Impedance, Junction-to-Ambient

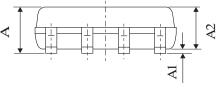


Normalized Thermal Transient Impedance, Junction-to-Foot

SOP-8 PACKAGE OUTLINE







SIDE VIEW

COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	ТҮР	MAX
А	1.30	1.60	1.85
Al	0.03	0.15	0.28
A2	1.20	1.45	1.70
b	0.26	0.40	0.54
С	0.132	0.203	0.273
D	4.50	4.90	5.30
Е	3.50	3.00	4.30
E1	5.50	6.00	6.50
L	0.30	0.70	1.10
θ	2°	4°	6°
L1	1.04REF		
e	1.27BSC		
R1	0.07TYP		
R2	0.07TYP		



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