

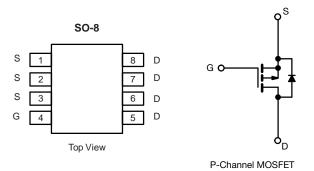


P-Channel 20 V (D-S) MOSFET



COMPLIANT

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^d	Q _g (Typ.)		
- 20	0.019 at V _{GS} = - 4.5 V	- 9.8	5.5 nC		
- 20	0.026 at V _{GS} = - 2.5 V	- 7	3.3 110		



FEATURES

- DT-Trench Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested

APPLICATIONS

- Adapter switch
- · Load switch
- DC/DC converters
- High speed switching
- Power management in battery-operated, mobile and wearable devices

ABSOLUTE MAXIMUM RATINGS ($T_A = 25 ^{\circ}\text{C}$, uparameter		SYMBOL LIMIT		UNIT	
Drain-source voltage		V _{DS}	-20		
Gate-source voltage		V _{GS}	± 12	V	
	T _C = 25 °C	ao l	-9.8 ^e		
0 11 150 100	T _C = 70 °C	1 .	-6.2		
Continuous drain current (T _J = 150 °C)	T _A =25 °C	l _D	-4.9 ^{b, c}		
	T _A = 70 °C		-4.1 ^{b, c}	A	
Pulsed drain current (t = 100 µs)		I _{DM}	-36 ^a		
	T _C = 25 °C	,	-4.5		
Continuous source-drain diode current	T _A = 70 °C	l _s	-2.7 ^{b, c}		
	T _C = 25 °C		2.8		
Maximum power dissipation	T _C = 70 °C		1.8	w	
	T _A = 25 °C	P _D	1.4 ^{b, c}		
	T _A = 70 °C		0.9 b, c		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150		
Soldering recommendations (peak temperature)			260	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum junction-to-ambient b, d	t ≤ 10 s	R _{thJA}	40	55	°C/W	
Maximum junction-to-foot (drain)	Steady state	R_{thJF}	20	25	C/VV	

Notes

- a. Package limited
- b. Surface mounted on 1" x 1" FR4 board
- d. Maximum under steady state conditions is 100 °C/W
- e. $T_C = 25$ °C



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-20	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	DS/TJ		-12.5	-	mV/°C	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	-	26.5	-] """ C	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4	-	-1	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	-	-	± 100	nA	
Zoro goto voltago droin overent	I _{DSS}	V _{DS} = -16 V, V _{GS} = 0 V	-	-	-1	μА	
Zero gate voltage drain current		V _{DS} = -16 V, V _{GS} = 0 V, T _J = 55 °C	-	-	-10		
On-state drain current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = 0 \text{ V}$	-5	-	-	Α	
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = -4.5 V, I _D = -5 A	-	0.019	0.025	Ω	
Drain Source on state resistance	TUS(on)	$V_{GS} = -2.5 \text{ V}, I_D = -3 \text{ A}$	-	0.026	0.036		
Forward transconductance ^a	9 _{fs}	$V_{DS} = -10 \text{ V}, I_D = -5 \text{ A}$	-	25	-	S	
Dynamic ^b							
Input capacitance	C _{iss}		-	4253	-	pF	
Output capacitance	C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	446	-		
Reverse transfer capacitance	C _{rss}		-	125	-		
Total gate charge	Qg	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -5 \text{ A}$	-	5.5	-	nC	
Total gate charge		$V_{DS} = -10 \text{ V}, V_{GS} = -2.5 \text{ V}, I_{D} = -3 \text{ A}$	-	3.9	-		
Gate-source charge	Q _{gs}	V _{DS} = -10 V, V _{GS} = -4.5 V, I _D = -5 A	-	1.7	-	- nc	
Gate-drain charge	Q _{gd}	$v_{DS} = -10 \text{ V}, v_{GS} = -4.5 \text{ V}, i_D = -5 \text{ A}$	-	2.3	-		
Gate resistance	R_g	f = 1 MHz	-	9.5	-	Ω	
Turn-on delay time	t _{d(on)}		-	11	-		
Rise time	t _r	V_{DD} = -10 V, R_L = 2 Ω , $I_D \cong$ -5 A,	-	25	-	ns	
Turn-off delay time	t _{d(off)}	V_{GEN} = -4.5 V, R_g = 1 Ω	-	50	-		
Fall time	t _f		-	33	-		
Drain-Source Body Diode Characteristi	cs						
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	-9.8		
Pulse diode forward current	I _{SM}		-	-	-30	A	
Body diode voltage	V_{SD}	I _S = -5 A, V _{GS} = 0 V	-	-0.8	-1.2	V	
Body diode reverse recovery time	t _{rr}		-	21	-	ns	
Body diode reverse recovery charge	Q _{rr}	1	-	8	-	nC	
Reverse recovery fall time	ta	$I_F = -5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$	-	12	-		
Reverse recovery rise time	t _b			19	-	ns	

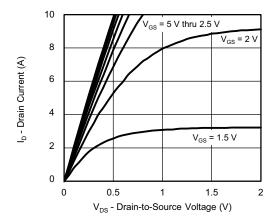
Notes

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

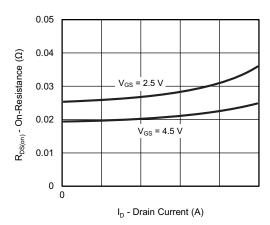
a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

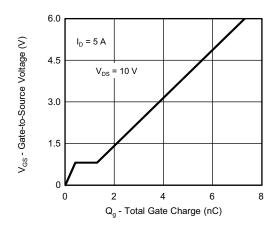




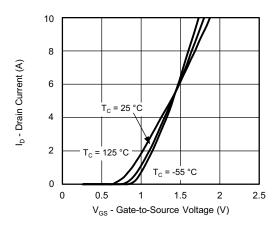
Output Characteristics



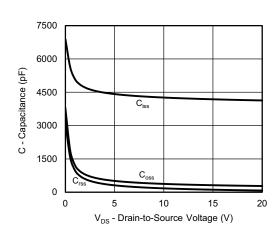
On-Resistance vs. Drain Current and Gate Voltage



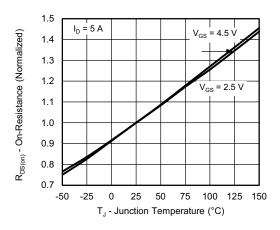
Gate Charge



Transfer Characteristics

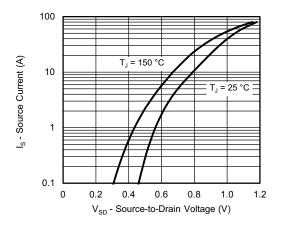


Capacitance

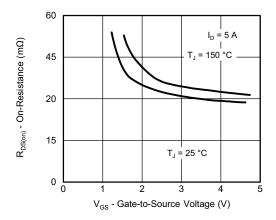


On-Resistance vs. Junction Temperature

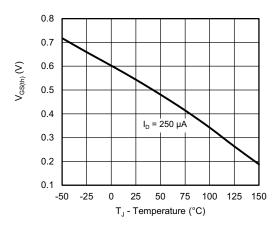




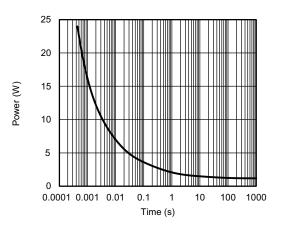
Source-Drain Diode Forward Voltage



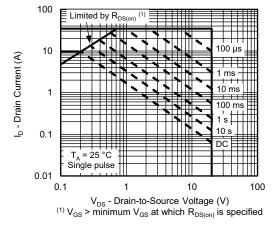
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

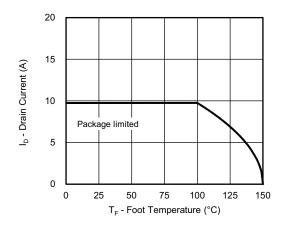


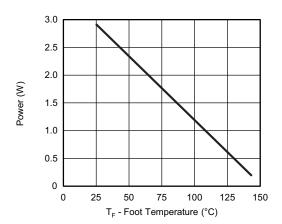
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient







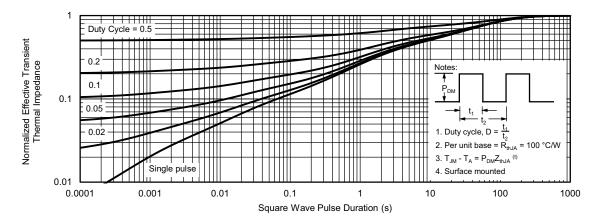
Current Derating a

Power, Junction-to-Foot

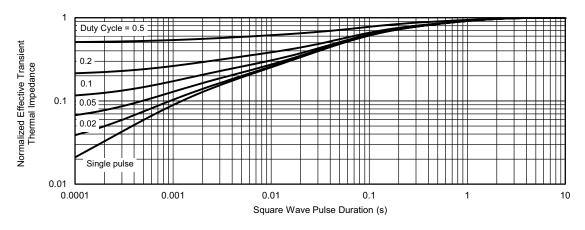
Note

a. The power dissipation P_D is based on T_J max. = 25 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





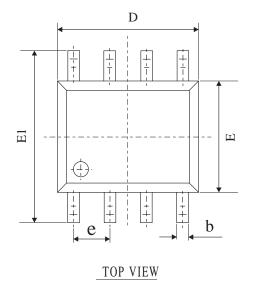
Normalized Thermal Transient Impedance, Junction-to-Ambient

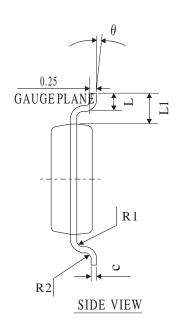


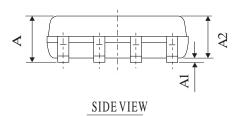
Normalized Thermal Transient Impedance, Junction-to-Foot

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SOP-8 PACKAGE OUTLINE







COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX		
A	1.30	1.60	1.85		
A1	0.03	0.15	0.28		
A2	1.20	1.45	1.70		
b	0.26	0.40	0.54		
С	0.132	0.203	0.273		
D	4.50	4.90	5.30		
Е	3.50	3.00	4.30		
E1	5.50	6.00	6.50		
L	0.30	0.70	1.10		
θ	2°	4° 6°			
L1	1.04REF				
e	1.27BSC				
R1	0.07TYP				
R2	0.07TYP				





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