1

P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)			
- 20	0.042 at V_{GS} = - 4.5 V	- 6.6			
- 20	0.054 at V _{GS} = - 2.5 V	- 4.4			

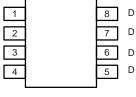


• DT-Trench Power MOSFET

GO

• Compliant to RoHS Directive 2002/95/EC





SO-8

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Top View

D P-Channel MOSFET

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ABSOLUTE MAXIMUM RATINGS $T_A = 25 \text{ °C}$, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 20		V	
Gate-Source Voltage		V _{GS}	± 12			
	T _A = 25 °C	– I _D	- 6.6	- 4.1	٨	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 4.6	- 3.2		
Pulsed Drain Current		I _{DM}	- 30		A	
Continuous Source Current (Diode Conduction) ^a		۱ _S	- 2.3	- 1.1		
	T _A = 25 °C	- P _D	2.5	1.3	w	
Maximum Power Dissipation ^a	T _A = 70 °C		1.6	0.8		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manimum haration to Anthia (a	t ≤ 10 s	R _{thJA}	40	50	
Maximum Junction-to-Ambient ^a	Steady State		70	95	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	24	30	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.





RoHS COMPLIANT

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Parameter	Symbol	/mbol Test Conditions		Symbol Test Conditions Min. Ty		Typ. ^a	Max.	Unit
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$			- 3.0	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA		
		V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 70 °C			- 5			
		$V_{DS}{\leq}$ - 10V, $V_{GS}{=}$ - 4.5 V	- 20					
On-State Drain Current ^b	I _{D(on)}	$V_{DS} \le$ - 5 V, V_{GS} = - 4.5 V	- 5			A		
Drain-Source On-State Resistance ^b	R _{DS(on)}	V_{GS} = - 4.5 V, I _D = - 5 A		0.042	0.047	Ω		
	''DS(on)	V_{GS} = - 2.5 V, I _D = - 4.4 A		0.054	0.060	52		
Forward Transconductance ^b	9 _{fs}	V _{DS} = - 15 V, I _D = - 5.8 A		13		S		
Diode Forward Voltage ^b	V_{SD}	$I_{S} = -2.3 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.8	- 1.1	V		
Dynamic ^a								
Total Gate Charge	Qg			16	24			
Gate-Source Charge	Q _{gs}	V_{DS} = - 15 V, V_{GS} = - 4.5 V, I_{D} = - 3.5 A		2.3		nC		
Gate-Drain Charge	Q _{gd}			4.5				
Gate Resistance	Rg			8.8		Ω		
Turn-On Delay Time	t _{d(on)}			14	25			
Rise Time	t _r	V_{DD} = - 15 V, R _L = 15 Ω		14	25			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 1 A, V_{GEN} = - 4.5 V, R_g = 6 Ω		42	70	ns		
Fall Time	t _f			30	50			
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.2 A, dl/dt = 100 A/μs		30	60			

Notes:

a. Guaranteed by design, not subject to production testing.

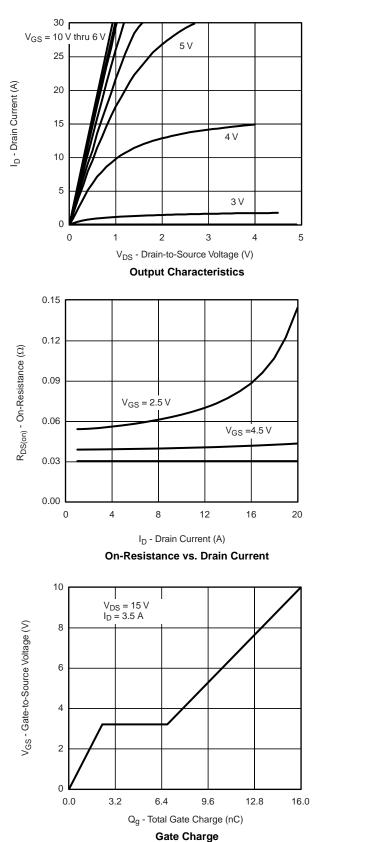
b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

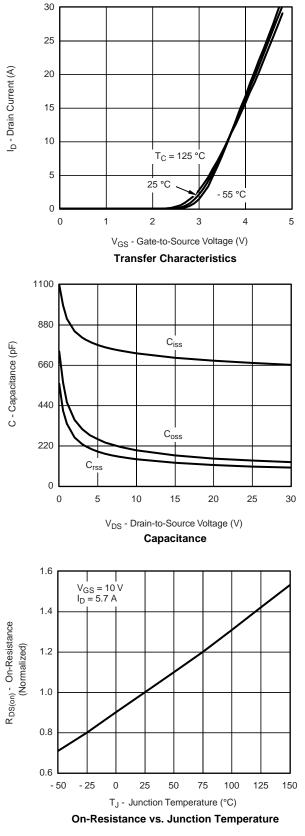
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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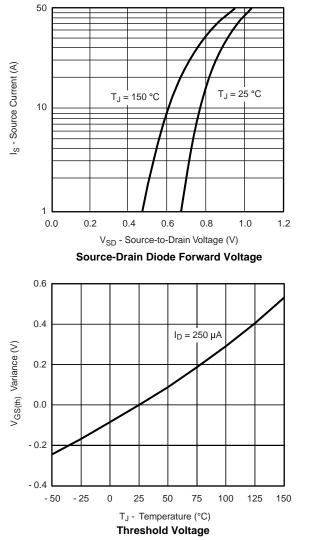




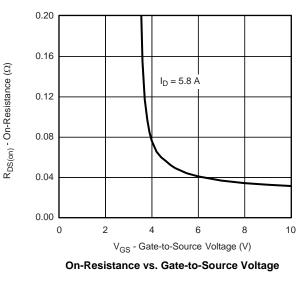
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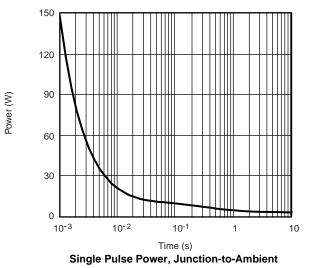
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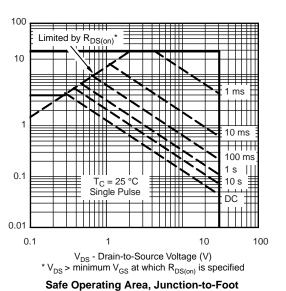




I_D - Drain Current (A)



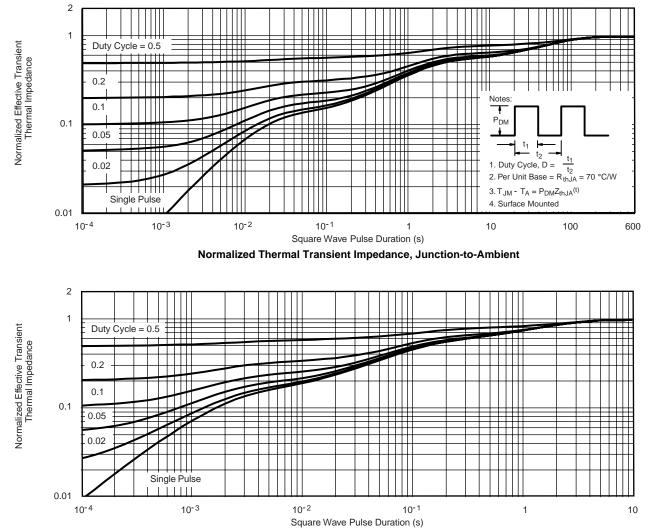






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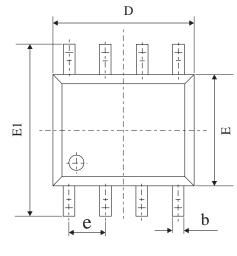
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

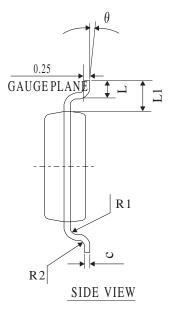


Normalized Thermal Transient Impedance, Junction-to-Foot

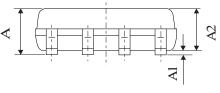


SOP-8 PACKAGE OUTLINE









SIDE VIEW

COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX		
А	1.30	1.60	1.85		
A1	0.03	0.15	0.28		
A2	1.20	1.45	1.70		
b	0.26	0.40	0.54		
С	0.132	0.203	0.273		
D	4.50	4.90	5.30		
Е	3.50	3.00	4.30		
E1	5.50	6.00	6.50		
L	0.30	0.70	1.10		
θ	2°	4° 6°			
L1	1.04REF				
e	1.27BSC				
R1	0.07TYP				
R2	0.07TYP				



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