Dual N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (mΩ)(Typ.)	I_D (A)[a]	Q_g (Typ.)
100	70 at V_{GS} = 10 V	7	20.5 nC
	80 at V_{GS} = 4.5 V		

FEATURES

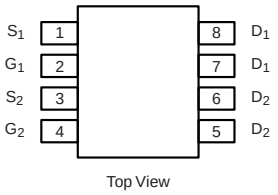
- DT-Trench Power MOSFET
- 100 % R_g and UIS tested
- High Power and current handing capability
- Super Low Gate Charge

RoHS COMPLIANT

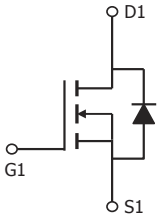
APPLICATIONS

- Networking DC-DC Power System
- Load Switch

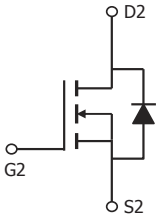
SO-8



Top View



N-Channel MOSFET



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)[a]	T_C = 25 °C	I_D	7	A
	T_C = 100 °C		5.2	
Pulsed Drain Current[b]		I_{DM}	28	
Single Avalanche Energy		E_{AS}	6.5	mJ
Maximum Power Dissipation[c]	T_C = 25 °C	P_D	2	W
	T_C = 100 °C		0.8	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to +150	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount)[d]	R_{thJA}	85	°C/W
Junction-to-Case (Drain)	R_{thJC}	62	

Notes

a. Calculated continuous current based on maximum allowablejunction temperature.
b. Repetitive rating; pulse width limited by max. junction temperature.
c. Pd is based on max. junction temperature, using junction-case thermal resistance.
d. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.

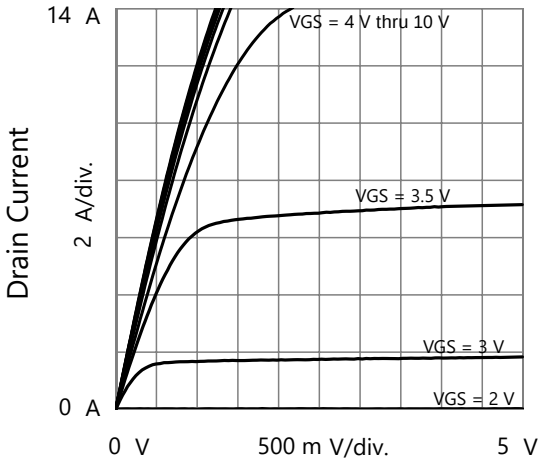
SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1.2	-	2.5	
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 100 V, V_{GS} = 0 V	-	-	1	µA
		V_{DS} = 80 V, V_{GS} = 0 V, T_J = 125 °C	-	-	100	
On-State Drain Current [a]	$I_{D(on)}$	V_{DS} ≥ 5 V, V_{GS} = 10 V	7	-	-	A
Drain-Source On-State Resistance [a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 5 A	-	70	98	mΩ
		V_{GS} = 4.5 V, I_D = 5 A		80	120	
Forward Transconductance [a]	g_{fs}	V_{DS} = 5 V, I_D = 5 A	-	15	-	S
Dynamic [b]						
Input Capacitance	C_{iss}	V_{GS} = 0 V, V_{DS} = 50 V, f = 1 MHz	-	1150	-	pF
Output Capacitance	C_{oss}		-	32	-	
Reverse Transfer Capacitance	C_{rss}		-	26	-	
Total Gate Charge [c]	Q_g	V_{DS} = 50 V, V_{GS} = 10 V, I_D = 5 A	-	20.5	-	nC
Gate-Source Charge [c]	Q_{gs}		-	2.5	-	
Gate-Drain Charge [c]	Q_{gd}		-	4	-	
Gate Resistance	R_g	f = 1 MHz	-	1.6	-	Ω
Turn-On Delay Time [c]	$t_{d(on)}$	V_{DD} = 50 V, I_D = 5 A , R_g = 3 Ω V_{GS} = 10 V	-	10	-	ns
Rise Time [c]	t_r		-	3	-	
Turn-Off Delay Time [c]	$t_{d(off)}$		-	15	-	
Fall Time [c]	t_f		-	4	-	
Drain-Source Body Diode Ratings and Characteristics [b] (T_C = 25 °C)						
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C	-	-	7	A
Pulsed Current	I_{SM}		-	-	28	A
Forward Voltage [a]	V_{SD}	I_F = 1 A, V_{GS} = 0 V	-	-	1.2	V
Reverse Recovery Time	t_{rr}	I_F = 5 A, di/dt = 100 A/µs	-	20	-	ns
Reverse Recovery Charge	Q_{rr}		-	26	-	nC

Notes

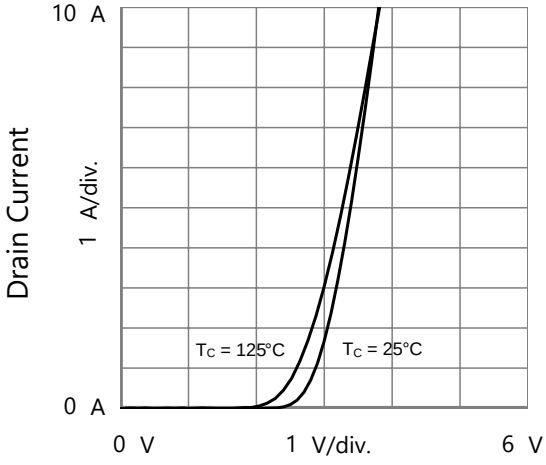
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.
c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

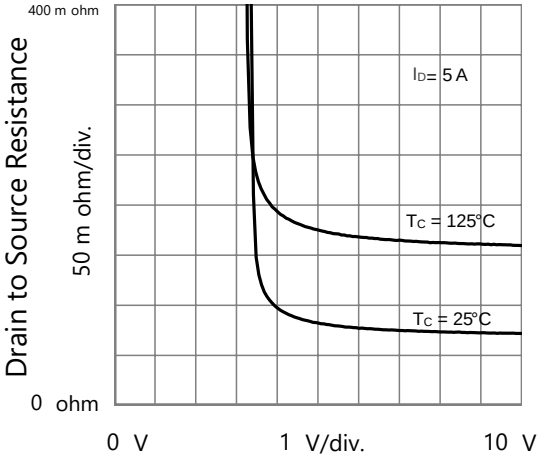
TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)



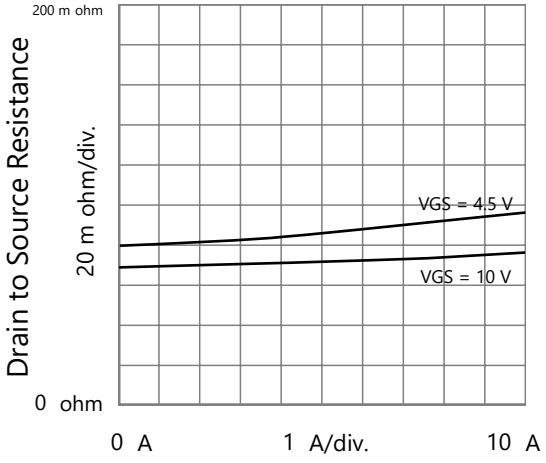
Drain to Source Voltage
Output Characteristics



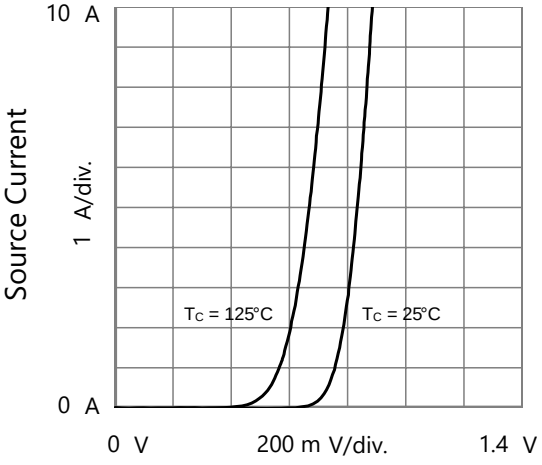
Gate to Source Voltage
Transfer Characteristics



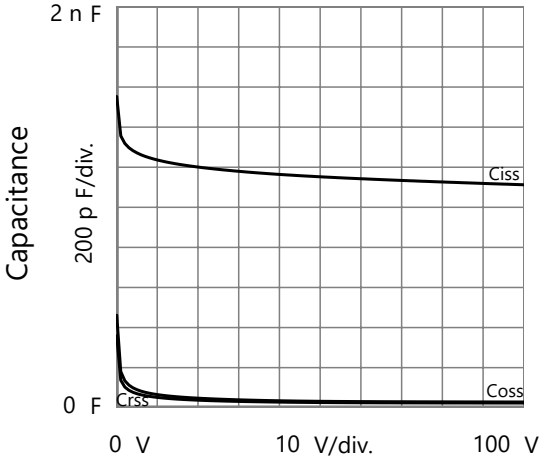
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



Drain Current
Drain to Source Resistance vs. Drain Current

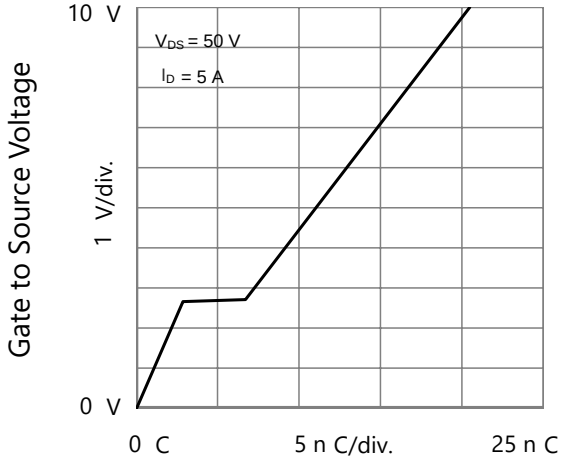


Source to Drain Voltage
Body Diode Forward Characteristics

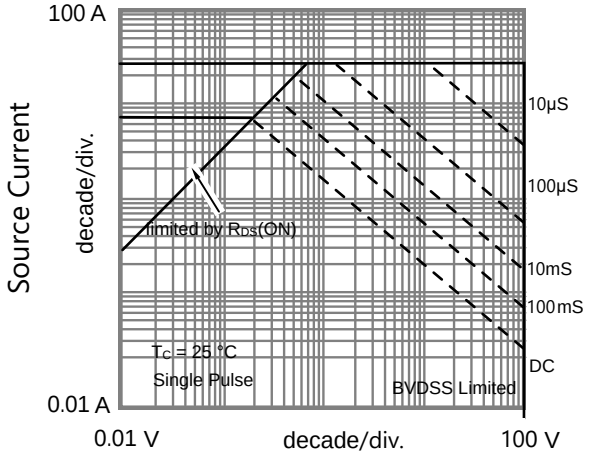


Drain to Source Voltage
Capacitances

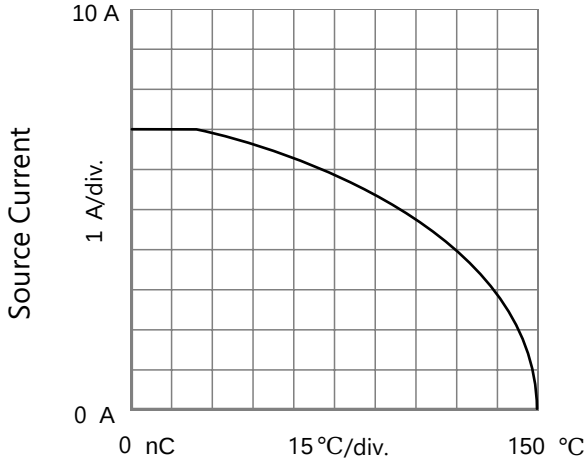
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

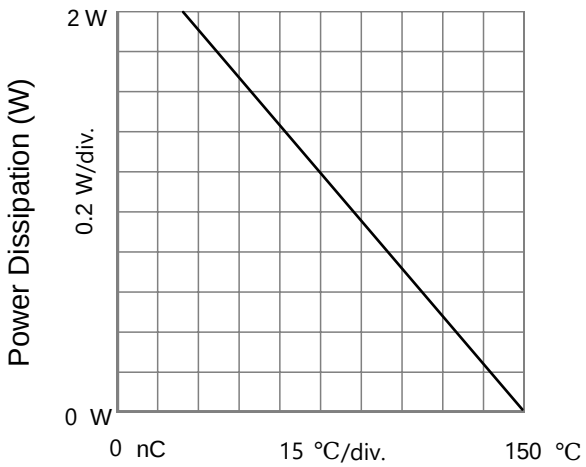


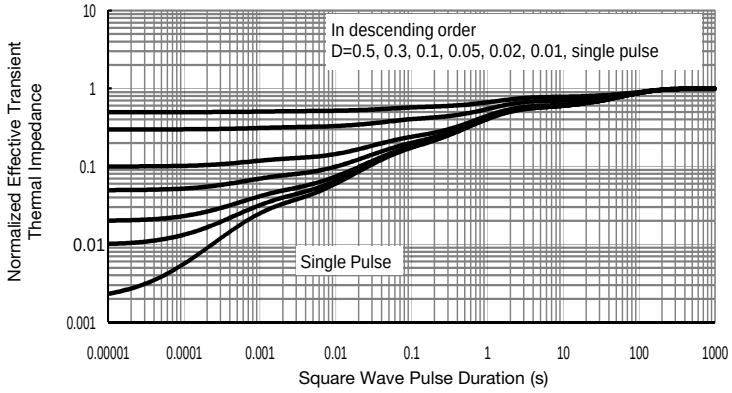
Gate to Source Voltage vs. GateCharge



Safe Operating Area, Junction-to-Ambient

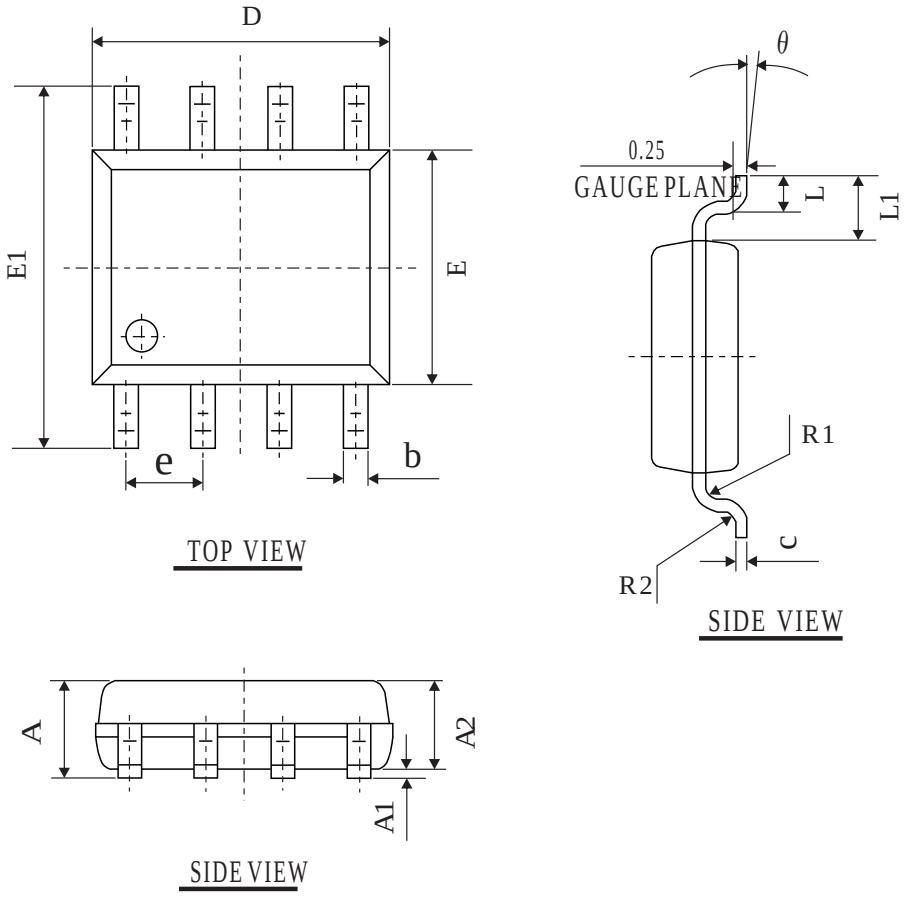






Normalized Thermal Transient Impedance, Junction-to-Ambient

SOP-8 PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX
A	1.30	1.60	1.85
A1	0.03	0.15	0.28
A2	1.20	1.45	1.70
b	0.26	0.40	0.54
C	0.132	0.203	0.273
D	4.50	4.90	5.30
E	3.50	3.00	4.30
E1	5.50	6.00	6.50
L	0.30	0.70	1.10
θ	2°	4°	6°
L1	1.04REF		
e	1.27BSC		
R1	0.07TYP		
R2	0.07TYP		

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