

#### 1200V N-Channel Silicon Carbide Power MOSFET

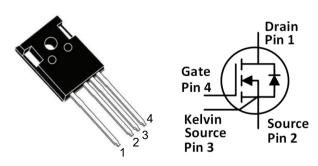
#### **Features**

- High blocking voltage with low on-resistance
- High speed switching with low capacitance
- High operating junction temperature capability
- Very fast and robust intrinsic body diode
- Kelvin gate input easing driver circuit design

#### **Applications**

- Solar inverters
- Motor drivers
- High voltage DC/DC converters
- Switch mode power supplies

#### **Package**



Top View

Part Number	Package
DTN125N120SC4	TO247-4

#### **Absolute Maximum Ratings** (T<sub>c</sub>=25°C unless otherwise specified)

Symbol	Parameter	Value	Un	Test Conditions	Note
			it		
V <sub>DS</sub>	Drain-Source voltage	1200	V	V <sub>GS</sub> =0V, I <sub>D</sub> =100μA	
V <sub>GSmax</sub> (DC)	Maximum DC voltage	-6 to 18	V	Static (DC)	
V <sub>GSmax</sub> (Spike)	Maximum spike voltage	-10 to 22	٧	<1% duty cycle, and pulse width<200ns	
	Drain current (continuous)	125	Α	V <sub>GS</sub> =20V, T <sub>C</sub> =25°C	Fir. 01
<b>I</b> D		90	А	V <sub>GS</sub> =20V, T <sub>C</sub> =100°C	Fig. 21
I <sub>DM</sub>	Drain current (pulsed)	200	Α	Pulse width limited by SOA	Fig. 22
P <sub>TOT</sub>	Total power dissipation	600	W	Tc=25°C	Fig. 20
T <sub>stg</sub>	Storage temperature range	-55 to 175	°C		
Tı	Operating junction temperature	-55 to 175	°C		
TL	Solder Temperature	260	°C	wave soldering only allowed at leads, 1.6mm from case for 10 s	

#### **Thermal Data**

Symbol	Parameter	Value	Unit	Note
$R_{\theta(J-C)}$	Thermal Resistance from Junction to Case	0.25	°C/W	Fig. 21



# **Electrical Characteristics** ( $T_c$ =25°C unless otherwise specified)

Symbol	Parameter	Value			Unit	Test Conditions	Note	
-		Min.	Тур.	Max.				
<b>I</b> DSS	Zero gate voltage drain current		5	50	μΑ	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V		
Igss	Gate leakage current			±100	nA	V <sub>DS</sub> =0V, V <sub>GS</sub> =-6~18V		
		2.2	3.2	4.5	V	$V_{GS}=V_{DS}$ , $I_{D}=23mA$		
$V_{TH}$	Gate threshold voltage		2.2			$V_{GS} = V_{DS}, I_{D} = 23 \text{mA}$ @ $T_{C} = 175^{\circ}\text{C}$	Fig. 11	
	Static drain-source on-		16	23	mΩ	V <sub>GS</sub> =18V, I <sub>D</sub> =75A @T <sub>J</sub> =25°C	Fig. 5, 6	
Ron	resistance		27		mΩ	V <sub>GS</sub> =18V, I <sub>D</sub> =75A @T <sub>J</sub> =175°C	-	
Ciss	Input capacitance		5225		pF		Fig. 18	
$C_{\text{oss}}$	Output capacitance		240		pF	V <sub>DS</sub> =800V, V <sub>GS</sub> =0V,		
$C_{rss}$	Reverse transfer capacitance		29		рF	f=100kHz, Vac=25mV		
Eoss	Coss stored energy		102		μЈ		Fig. 16	
Qg	Total gate charge		293		nC	\/ -000\/ 1 -754	Fig. 12	
$Q_{\text{gs}}$	Gate-source charge		66		nC	$V_{DS}=800V$ , $I_{D}=75A$ , $V_{GS}=-5$ to $18V$		
$Q_{\text{gd}}$	Gate-drain charge		155		nC	V <sub>GS</sub> =-3 tO 10V		
$R_g$	Gate input resistance		1.7		Ω	f=1MHz		
Eon	Turn-on switching energy		4980		μͿ			
Eoff	Turn-off switching energy		1466		μJ	$V_{DS}$ =800V, $I_{D}$ =75A,	Fig. 23, 24	
t <sub>d(on)</sub>	Turn-on delay time		102			$V_{GS}$ =-5 to 18V,		
<b>t</b> r	Rise time		55		nc	$R_{G(ext)}=10\Omega$ ,		
$t_{\text{d(off)}}$	Turn-off delay time		136		ns	L=99μH		
<b>t</b> f	Fall time		43					



# Reverse Diode Characteristics (T<sub>c</sub>=25°C unless otherwise specified)

Symbol	Parameter	Value			Unit	Test Conditions	Note
		Min.	Тур.	Max.			
<b>I</b> s	Maximum continuous diode forward current		125		А	T <sub>3</sub> =25°C	
			5.2		V	I <sub>SD</sub> =37.5A, V <sub>GS</sub> =-4V	Fig. 10
V <sub>SD</sub>	Diode forward voltage		4.7		V	I <sub>SD</sub> =37.5A, V <sub>GS</sub> =-4V, T <sub>J</sub> =175°C	Fig. 10, 11, 12
<b>t</b> rr	Reverse recovery time		35		ns	V <sub>GS</sub> =-4V,	
Qrr	Reverse recovery charge		498		nC	I <sub>SD</sub> =75A, V <sub>R</sub> =800V,	
I <sub>RRM</sub>	Peak reverse recovery current		26.5		А	di/dt=1400A/us	



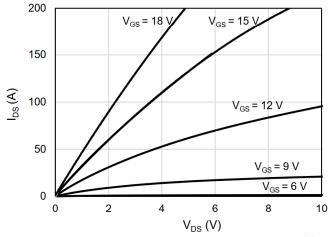


Figure 1: Output Characteristics  $T_J = -40^{\circ}C$ 

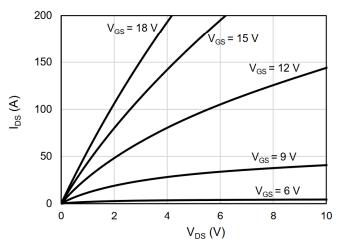


Figure 2: Output Characteristics T<sub>J</sub> = 25°C

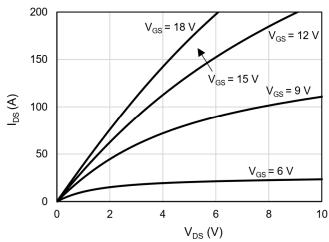
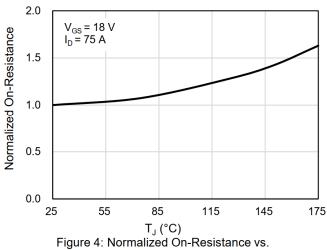
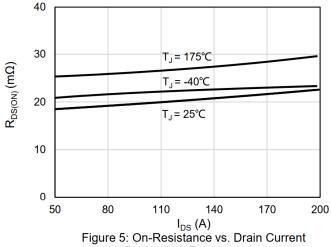


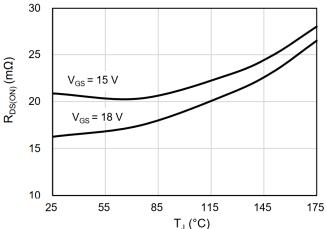
Figure 3: Output Characteristics T<sub>J</sub> = 175°C



Temperature

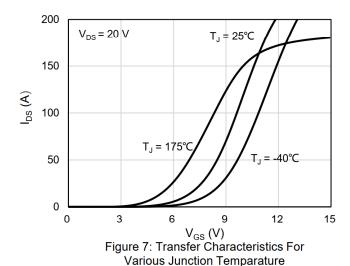


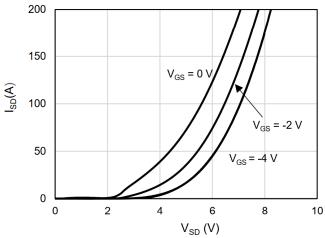
For Various Temperatures

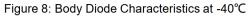


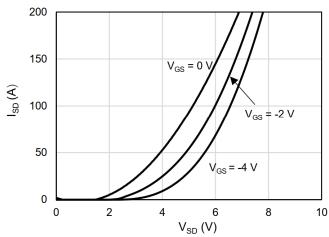
 $\text{T}_{\text{J}}\left(^{\circ}\text{C}\right)$  Figure 6: On-Resistance vs. Temperature For Various Gate Voltage

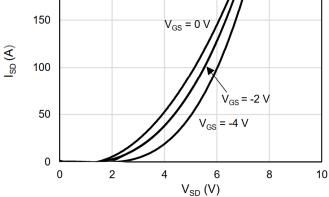






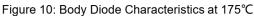


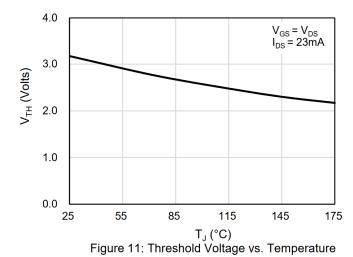


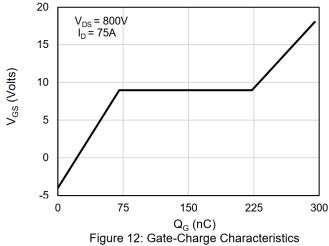


200

Figure 9: Body Diode Characteristics at 25°C









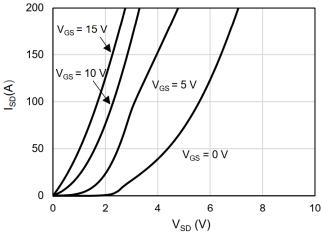


Figure 13: 3rd Quadrant Characteristics at -40°C

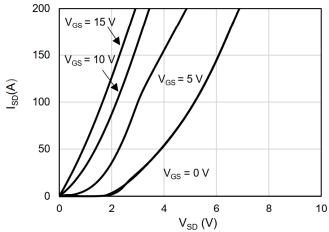


Figure 14: 3rd Quadrant Characteristics at 25°C

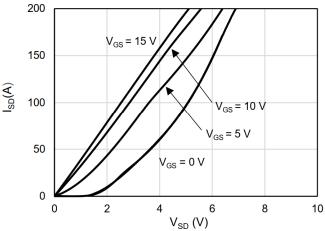
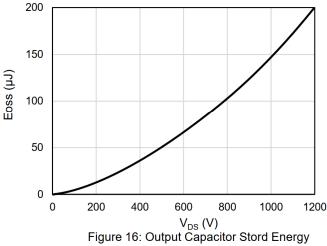
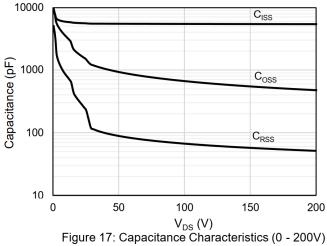
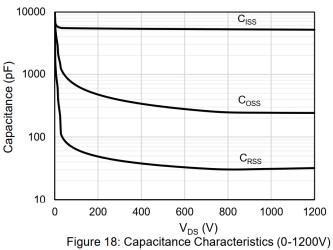


Figure 15: 3rd Quadrant Characteristics at 175°C









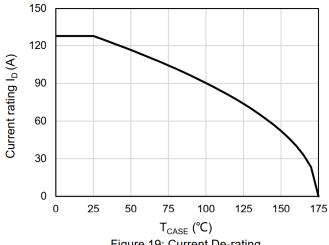


Figure 19: Current De-rating

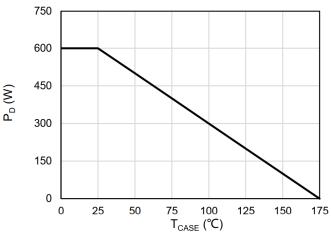


Figure 20: Maximum Power Disspation Derating vs CaseTemperature

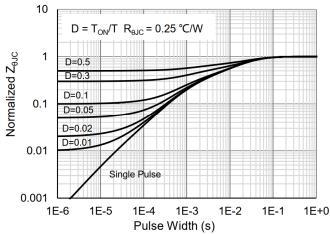
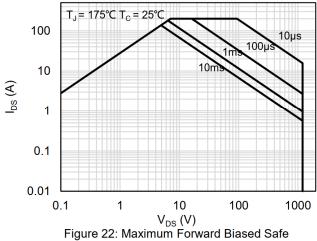
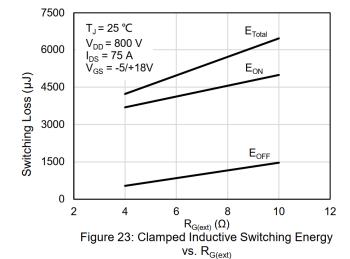


Figure 21: Normalized Maximum Transient Thermal Impedance



**Operating Area** 



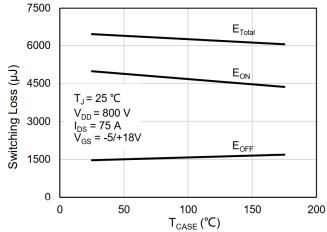
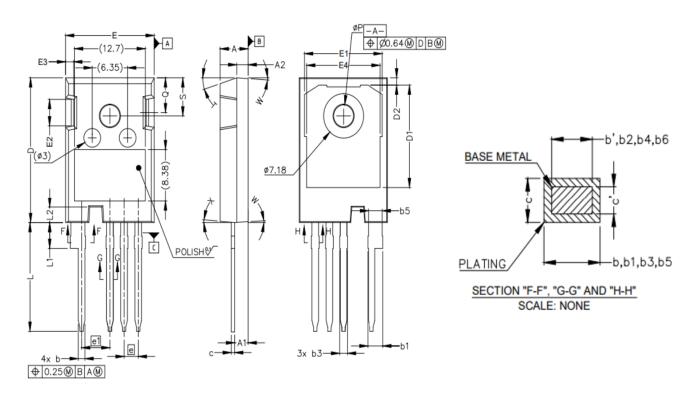


Figure 24: Clamped Inductive Switching Energy vs. Temperature



## **TO-247\_4L PACKAGE OUTLINE**



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX	SYMBOL	MIN	TYP	MAX	
A	4.50	5.02	5.50	E1	12.50	14.02	15.00	
A1	2.00	2.40	2.80	E2	3.00	4.40	5.00	
A2	1.80	2.00	2.30	E3	0.80	1.45	2.10	
b'	0.90	1.20	1.40	E4	11.50	13.26	14.00	
b	0.90	1.20	1.45	e		2.54BSC		
b1	2.15	2.67	3.10	e1	5.08BSC			
b2	2.15	2.67	3.05	N	4.00			
b3	0.90	1.30	1.80	L	16.00	17.57	19.00	
b4	0.90	1.30	1.70	L1	3.47	4.19	4.87	
b5	2.20	2.53	2.89	L2	2.05	2.50	2.95	
b6	2.20	2.53	2.84	ФР	3.21	3.61	3.95	
c'	0.48	0.60	0.75	Q	5.09	5.79	6.40	
С	0.48	0.60	0.78	S	5.74 6.21		6.60	
D	22.50	23.45	24.50	T	17.5°REF			
D1	15.50	16.55	18.10	W	3.5°REF			
D2	0.85	1.19	1.35	X	4°REF			
Е	15.00	15.94	17.00					

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