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# N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) MAX.	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.)			
100	0.0019 at V <sub>GS</sub> = 10 V	250	258 nC			

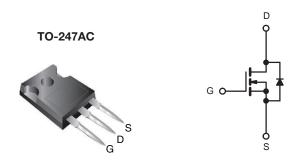
#### **FEATURES**

- DT-TrenchPower MOSFET
- Maximum 175 °C junction temperature
- 100 % R<sub>g</sub> and UIS tested



### **APPLICATIONS**

- Power supplies:
  - Uninterruptible power supplies
  - AC/DC switch-mode power supplies
  - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- Hard Switched and High Frequency Circuits



N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V <sub>DS</sub>	100	V			
Gate-Source Voltage	V <sub>GS</sub>	± 20	7 v			
Continuous Proin Current (T. – 150 °C)	T <sub>C</sub> = 25 °C		250	^		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 125 °C	I <sub>D</sub>	189			
Pulsed Drain Current (t = 100 μs)	I <sub>DM</sub>	1150	А			
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	153			
Single Avalanche Energy <sup>a</sup>	L = 0.1 IIII	E <sub>AS</sub>	825	mJ		
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	В	553 <sup>b</sup>	W		
Maximum Fower Dissipation 4	T <sub>C</sub> = 125 °C	$ P_D$	184 <sup>b</sup>	VV		
Operating Junction and Storage Temperature F	T <sub>.I</sub> , T <sub>sta</sub>	-55 to +175	°C			

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	LIMIT	UNIT			
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W			
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.3	C/VV			

### Notes

- a. Duty cycle  $\leq 1 \%$ .
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR4 material).



<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS MIN.		TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100	-	-	V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	2	-	4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
7 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V		-	1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	-	-	100	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	250	-	-	Α	
Drain-Source On-State Resistance a	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A	-	0.0019	0.0025	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 50 A	-	330	-	S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>		-	19260	-	pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 50 \text{ V}, f = 1 \text{ MHz}$	-	1664	-		
Reverse Transfer Capacitance	C <sub>rss</sub>		-	399	-		
Total Gate Charge <sup>c</sup>	Qg		-	258	660	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 50 \text{ A}$	-	92	-		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	105	-		
Gate Resistance	$R_g$	f = 1 MHz	-	1.0	-	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	35	-		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_{L} = 1.67 \Omega$	-	190	-	ns	
Turn-Off Delay Time c	t <sub>d(off)</sub>	$I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	110	-		
Fall Time <sup>c</sup>	t <sub>f</sub>		-	102	-		
Drain-Source Body Diode Ratings at	nd Characteri	stics <sup>b</sup> (T <sub>C</sub> = 25 °C)					
Pulsed Current (t = 100 μs)	I <sub>SM</sub>		-	-	1150	Α	
Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0 V	-	-	1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	L 50 A 31/41 400 A/	-	95	-	ns	
Reverse Recovery Charge	Q <sub>rr</sub>	$I_F = 50 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$	-	350	-	μC	

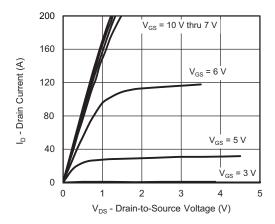
### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

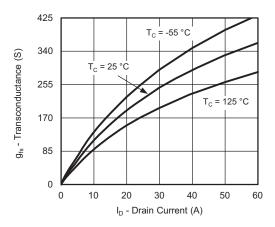
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



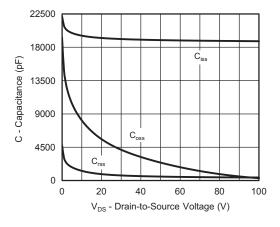
## **TYPICAL CHARACTERISTICS** ( $T_A = 25$ °C, unless otherwise noted)



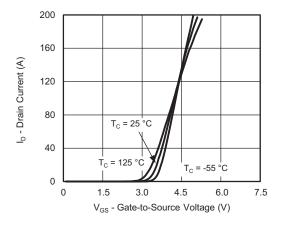
#### **Output Characteristics**



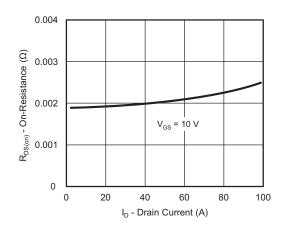
Transconductance



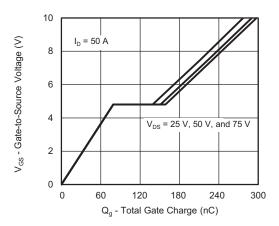
Capacitance



**Transfer Characteristics** 



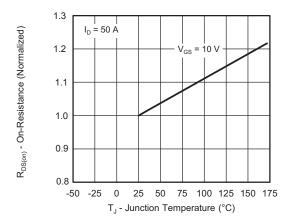
On-Resistance vs. Drain Current



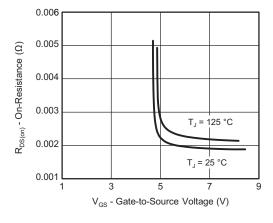
**Gate Charge** 



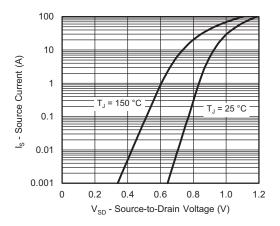
## **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



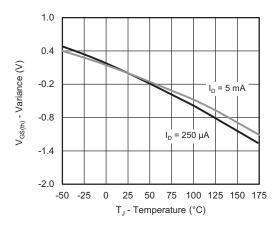
On-Resistance vs. Junction Temperature



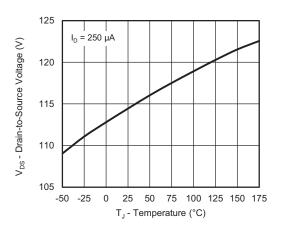
On-Resistance vs. Gate-to-Source Voltage



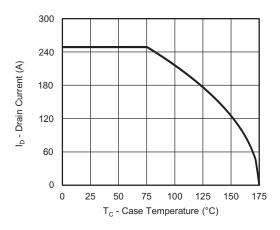
**Source Drain Diode Forward Voltage** 



**Threshold Voltage** 



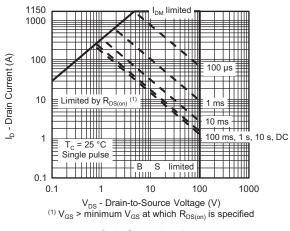
**Drain Source Breakdown vs. Junction Temperature** 

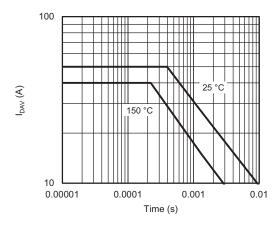


**Current De-Rating** 



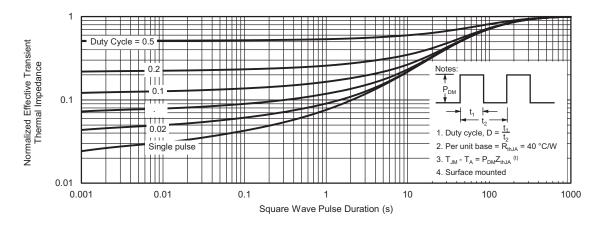
## **THERMAL RATINGS** ( $T_A = 25$ °C, unless otherwise noted)





Safe Operating Area

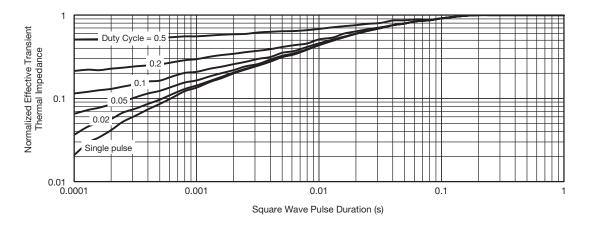
 $I_{\text{DAV}}$  vs. Time



Normalized Thermal Transient Impedance, Junction-to-Ambient



## **THERMAL RATINGS** ( $T_A = 25$ °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

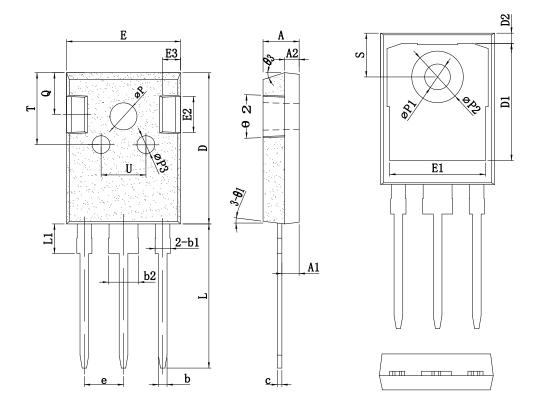
#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



# **TO-247\_3L PACKAGE OUTLINE**



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX	SYMBOL	MIN	TYP	MAX
A	4.60	5.00	5.40	e	2.10	5.44	5.70
A1	2.10	2.41	2.70	L	19.00	19.98	21.00
A2	1.70	2.00	2.30	L1	ı	-	4.50
b	1.00	1.20	1.40	ФР	3.30	3.70	4.00
bl	1.80	2.10	2.40	ФР1	3.25	3.55	3.85
b2	2.80	3.10	3.40	ФР2	6.80	7.18	7.60
С	0.45	0.60	0.75	ФР3	2.30	2.50	3.30
D	19.00	21.00	23.00	Q	5.50	5.80	6.30
D1	16.00	16.55	17.00	S	5.60	6.15	6.30
D2	0.95	1.20	1.45	T	9.50	10.00	10.50
Е	15.70	15.80	16.50	U	6.00	-	8.00
E1	12.80	13.25	13.70	θ1	5°	7°	9°
E2	4.20	5.00	5.30	θ2	1°	3°	5°
E3	2.20	2.50	2.80	θ3	13°	15°	17°





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