

N-Channel 650 V (D-S) MOSFET



PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A)	Q _g (Typ.)
650	60 at V _{GS} = 10 V	50	77 nC

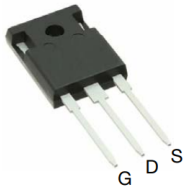
FEATURES

- DT-Trench Power MOSFET
- 100 % Rg and UIS tested
- Low Gate Charge
- Low R_{DS(ON)}(Typ 60mΩ)@V_{GS}=10 V

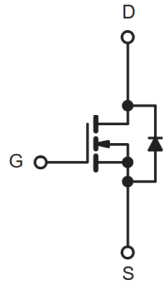
APPLICATIONS

- High efficient switched mode power supplies
- Adapter/charger
- Server Power
- LED Lighting

TO-247 Pin Configuration



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	650	V
Gate-Source Voltage		V _{GS}	± 30	
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C	I _D	50	A
	T _C = 100 °C		32	
Pulsed Drain Current (t = 100 μs)		I _{DM}	150	
Avalanche Current	L = 0.1 mH	I _{AS}	45	
Single Avalanche Energy ^a		E _{AS}	906	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	P _D	339	W
	T _C = 125 °C		88	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +150	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^c		R _{thJA}	35	°C/W
Junction-to-Case (Drain)		R _{thJC}	0.4	

Notes

- Duty cycle ≤ 1 %.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR4 material).

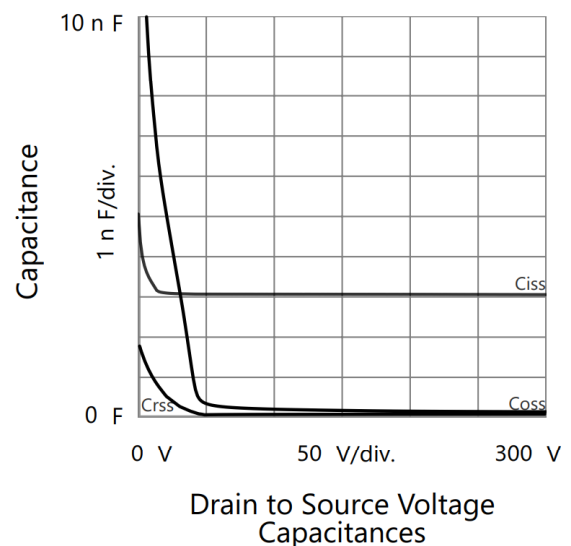
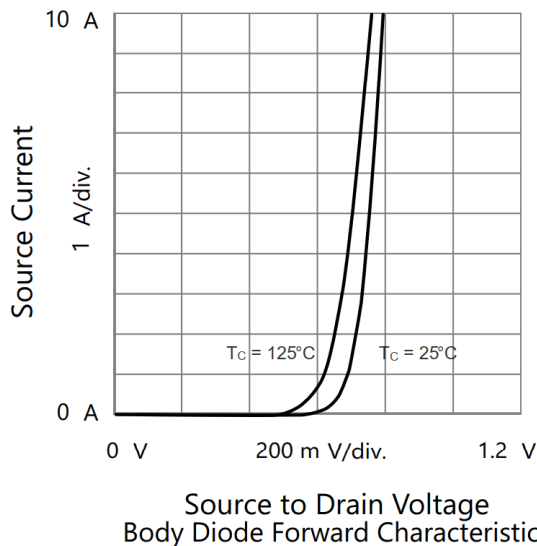
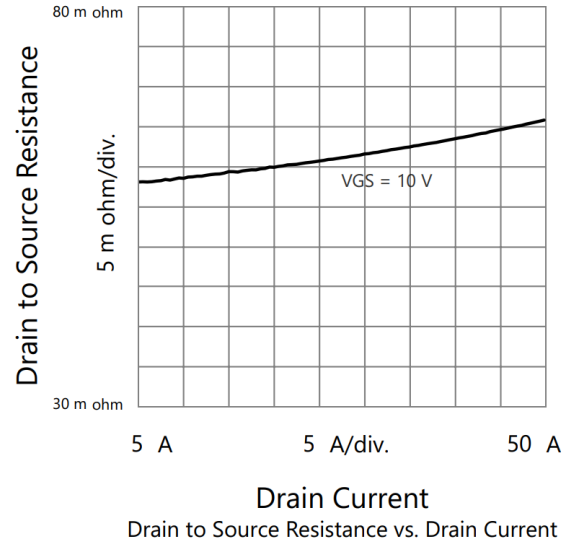
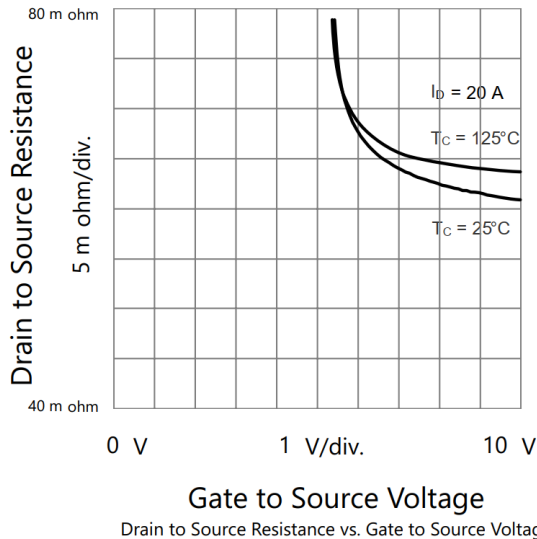
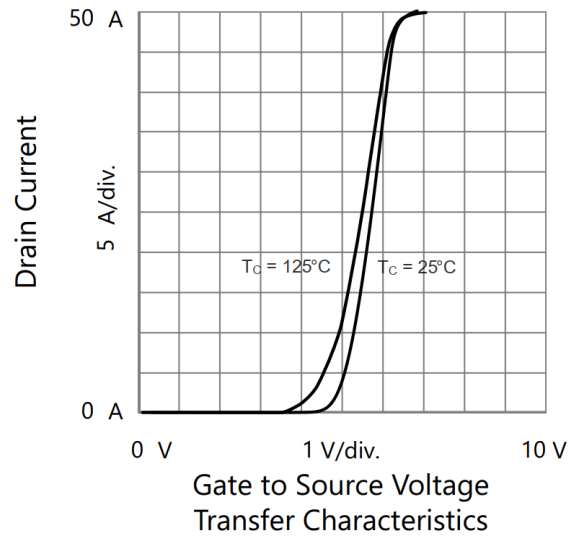
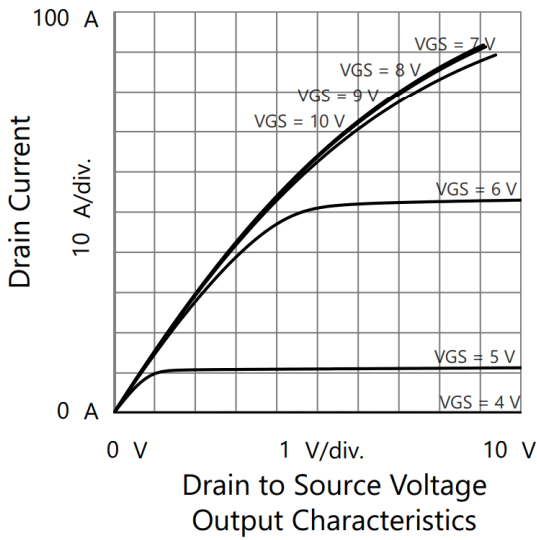
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	650	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2	-	4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	μA
		$V_{DS} = 480\text{ V}, V_{GS} = 0\text{ V}, T_J = 100\text{ }^\circ\text{C}$	-	-	100	
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 20\text{ A}$	-	60	76	m Ω
Forward Transconductance ^a	g_{fs}	$V_{DS} = 5\text{ V}, I_D = 20\text{ A}$	-	40	-	S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 300\text{ V}, f = 1\text{ MHz}$	-	3060	-	pF
Output Capacitance	C_{oss}		-	105	-	
Reverse Transfer Capacitance	C_{rss}		-	3	-	
Total Gate Charge ^c	Q_g	$V_{DS} = 30\text{ V}, V_{GS} = 10\text{ V}, I_D = 20\text{ A}$	-	77	-	nC
Gate-Source Charge ^c	Q_{gs}		-	19	-	
Gate-Drain Charge ^c	Q_{gd}		-	36	-	
Gate Resistance	R_g	$f = 1\text{ MHz}$	-	1.0	-	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 300\text{ V}, R_L = 120\text{ }\Omega$ $I_D = 20\text{ A}, V_{GEN} = 10\text{ V}, R_g = 20\text{ }\Omega$	-	35	-	ns
Rise Time ^c	t_r		-	72	-	
Turn-Off Delay Time ^c	$t_{d(off)}$		-	88	-	
Fall Time ^c	t_f		-	60	-	
Drain-Source Body Diode Ratings and Characteristics ^b ($T_C = 25\text{ }^\circ\text{C}$)						
Continuous Source Current	I_S	$T_C = 25\text{ }^\circ\text{C}$	-	-	50	A
Pulsed Current ($t = 100\text{ }\mu\text{s}$)	I_{SM}		-	-	150	A
Forward Voltage ^a	V_{SD}	$I_F = 20\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 20\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$	-	388	-	ns
Reverse Recovery Charge	Q_{rr}		-	6.5	-	μC

Notes

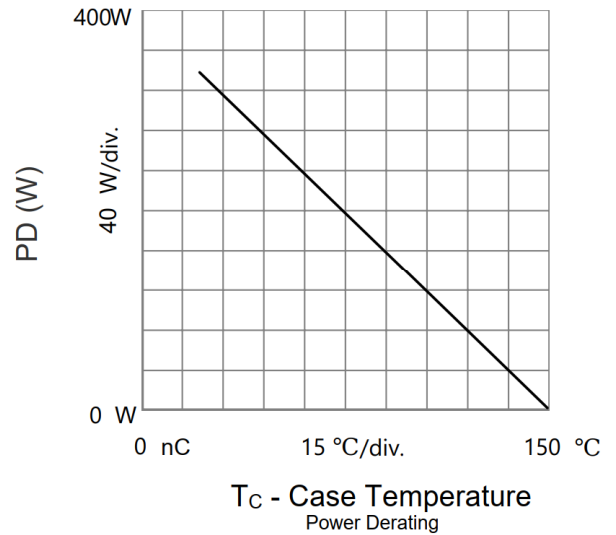
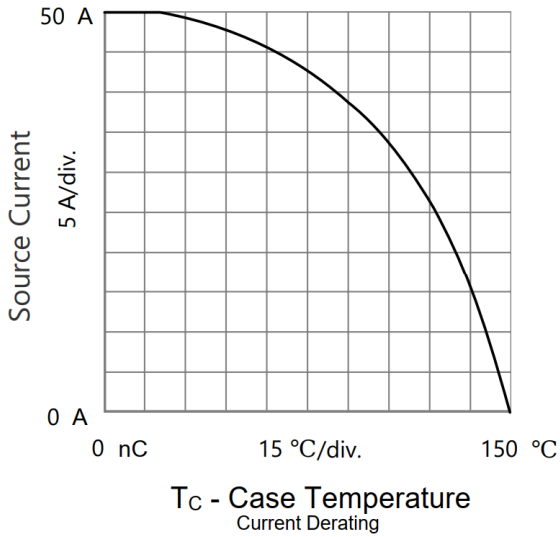
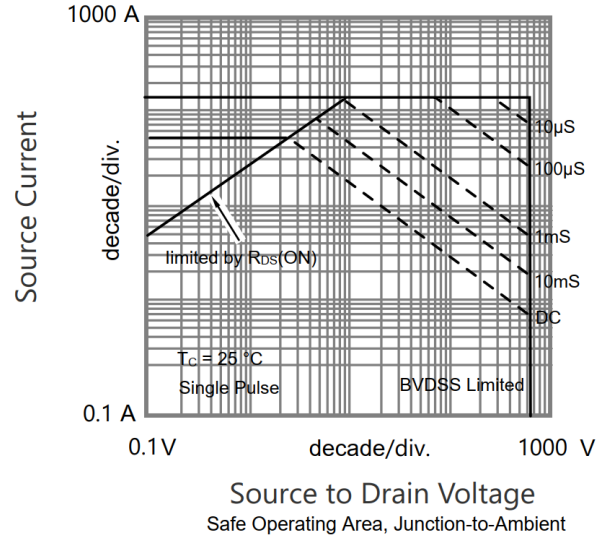
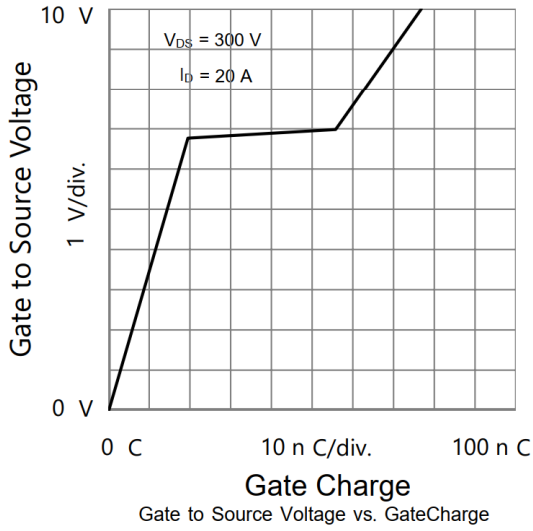
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

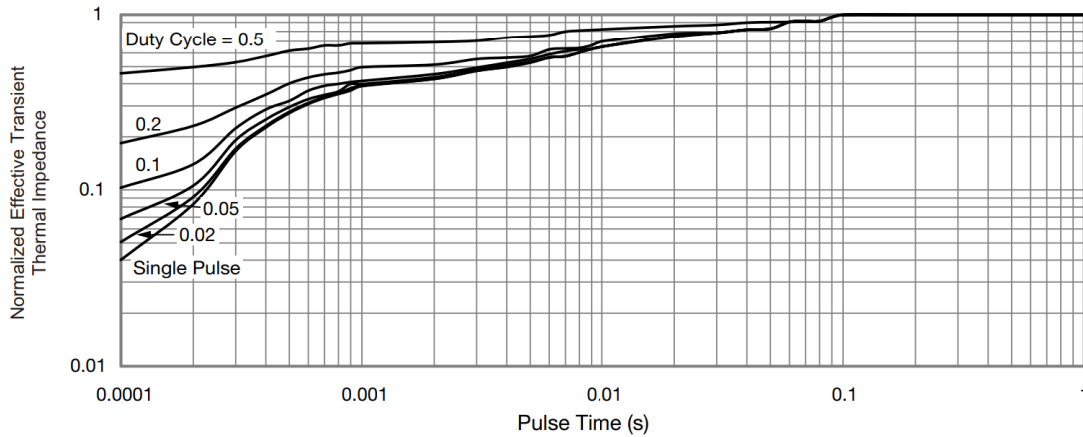
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)



TYPICAL CHARACTERISTICS ($T = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)

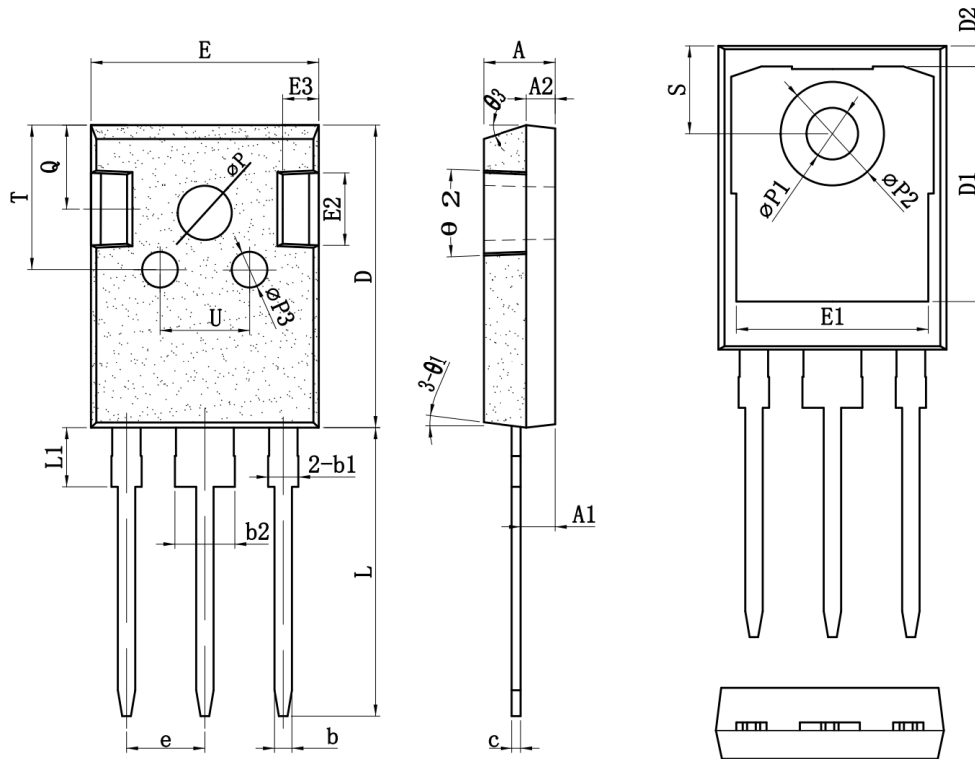


Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient ($25\text{ }^\circ\text{C}$)
 - Normalized Transient Thermal Impedance Junction to Case ($25\text{ }^\circ\text{C}$)
 are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

TO-247_3L PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	TYP	MAX	SYMBOL	MIN	TYP	MAX
A	4.60	5.00	5.40	e	2.10	5.44	5.70
A1	2.10	2.41	2.70	L	19.00	19.98	21.00
A2	1.70	2.00	2.30	L1	-	-	4.50
b	1.00	1.20	1.40	ΦP	3.30	3.70	4.00
b1	1.80	2.10	2.40	$\Phi P1$	3.25	3.55	3.85
b2	2.80	3.10	3.40	$\Phi P2$	6.80	7.18	7.60
C	0.45	0.60	0.75	$\Phi P3$	2.30	2.50	3.30
D	19.00	21.00	23.00	Q	5.50	5.80	6.30
D1	16.00	16.55	17.00	S	5.60	6.15	6.30
D2	0.95	1.20	1.45	T	9.50	10.00	10.50
E	15.70	15.80	16.50	U	6.00	-	8.00
E1	12.80	13.25	13.70	$\theta 1$	5°	7°	9°
E2	4.20	5.00	5.30	$\theta 2$	1°	3°	5°
E3	2.20	2.50	2.80	$\theta 3$	13°	15°	17°

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