

# N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY							
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Typ)				
30	0.0012 at V <sub>GS</sub> = 10 V	300	72 nC				
	$0.0017$ at $V_{GS} = 4.5 \text{ V}$	210	72110				

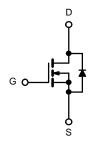
#### **FEATURES**

- **DT-Trench Power MOSFET**
- 100 %  $\rm R_{\rm g}$  and UIS Tested Compliant to RoHS Directive 2011/65/EU



#### **APPLICATIONS**

- OR-ing
- Server
- DC/DC



N-Channel MOSFET

#### TO-220 Pin Configuration



Top View

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	30	.,,		
Gate-Source Voltage	V <sub>GS</sub>	± 20	V		
	T <sub>C</sub> = 25 °C		300 <sup>a, e</sup>		
Continuous Drain Current /T = 175 °C	T <sub>C</sub> = 70 °C		220 <sup>e</sup>		
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	115 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		89 <sup>b, c</sup>	A	
Pulsed Drain Current		I <sub>DM</sub> 850			
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	90		
Single Pulse Avalanche Energy		E <sub>AS</sub>	845	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	1	90 <sup>a, e</sup>	Α	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	3.13 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		302 <sup>a</sup>		
Manianus Pauca Discipation	T <sub>C</sub> = 70 °C	В	184	10/	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	10.2 <sup>b, c</sup>	W	
	T <sub>A</sub> = 70 °C		5.9 <sup>b, c</sup>		
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Тур.	Max.	Unit			
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 sec	R <sub>thJA</sub>	8	13	°C/W		
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.38	0.45	J		

#### Notes:

- a. Based on T<sub>C</sub> = 25 °C.
  b. Surface mounted on 1" x 1" FR4 board.

- b. Surface mounted on F. A. F. F. Scale.

  c. t = 10 sec.

  d. Maximum under steady state conditions is 90 °C/W.

  e. Calculated based on maximum junction temperature. Package limitation current is 90 A.





Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			,				
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		35		\//00	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	ι <sub>D</sub> = 250 μΑ		- 7.5		mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0		2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Oata Valla va Busin Oamani	1	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	^	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	300			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 40 A		0.0012	0.00135	-	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 40 \text{ A}$		0.0017	0.0019	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 40 \text{ A}$		110		S	
Dynamic <sup>b</sup>			L	I.	<u> </u>		
Input Capacitance	C <sub>iss</sub>			6998			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1901		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			910			
Total Gate Charge	Qg	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 40 A		72	87		
				41	56	- 0	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 40 \text{A}$		22		nC	
Gate-Drain Charge	$Q_{gd}$			9			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.2	1.8	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			15			
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.625 $\Omega$		16			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 30 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		77			
Fall Time	t <sub>f</sub>			13			
Turn-On Delay Time	t <sub>d(on)</sub>			25		ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.67 $\Omega$		180			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 30$ A, $V_{GEN}=4.5$ V, $R_g=1$ $\Omega$		55			
Fall Time	t <sub>f</sub>			12		1	
Drain-Source Body Diode Characteristic	s		L		<u> </u>		
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			300	۸	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				850	Α	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = 22 A		0.5	1.1	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			25		ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	1 20 A di/dt 100 A/va T 25 °C		86		nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$		27			
Reverse Recovery Rise Time	t <sub>b</sub>			15	1	ns	

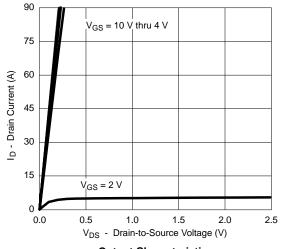
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

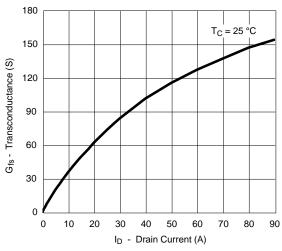
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



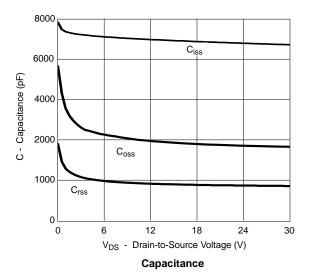
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

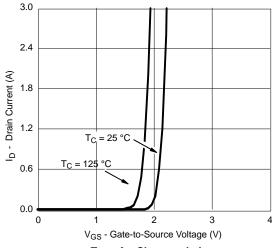


#### **Output Characteristics**

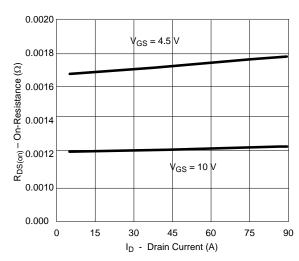


Transconductance

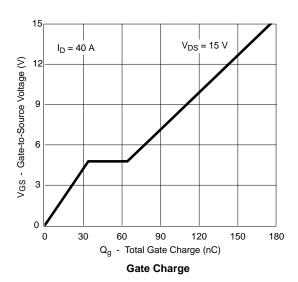




**Transfer Characteristics** 

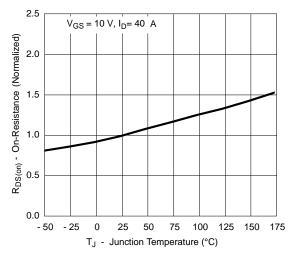


R<sub>DS(on)</sub> vs. Drain Current

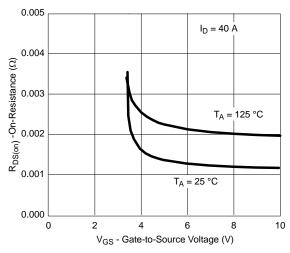




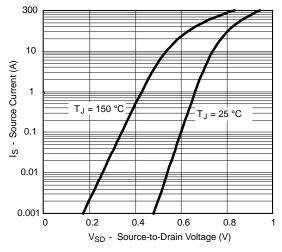
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



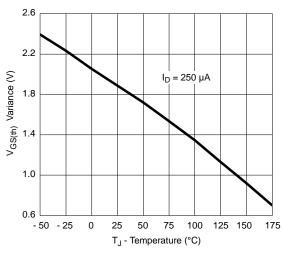
#### On-Resistance vs. Junction Temperature



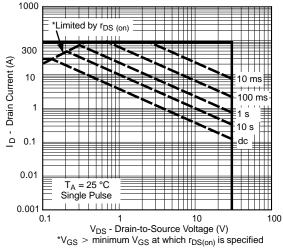
 $R_{DS(on)}$  vs.  $V_{GS}$  vs. Temperature



Forward Diode Voltage vs. Temperature



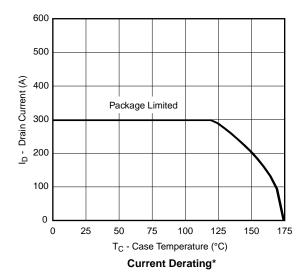
Threshold Voltage

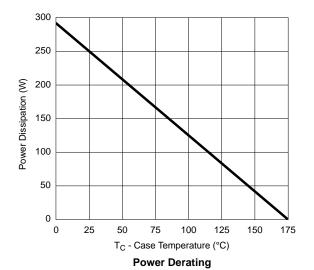


Safe Operating Area, Junction-to-Ambient

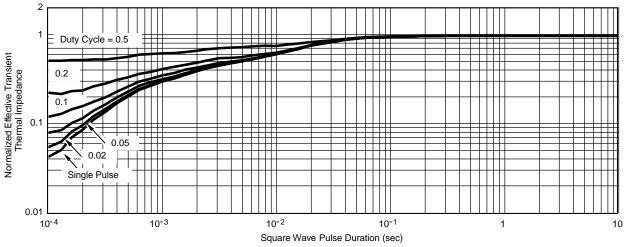


### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





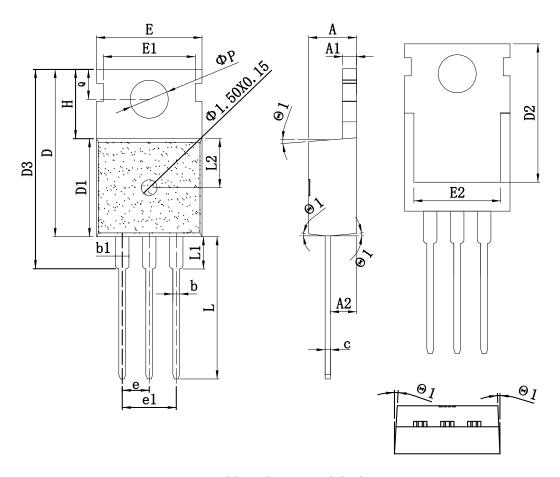
\*The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Case



# **TO-220\_3L-A PACKAGE OUTLINE**

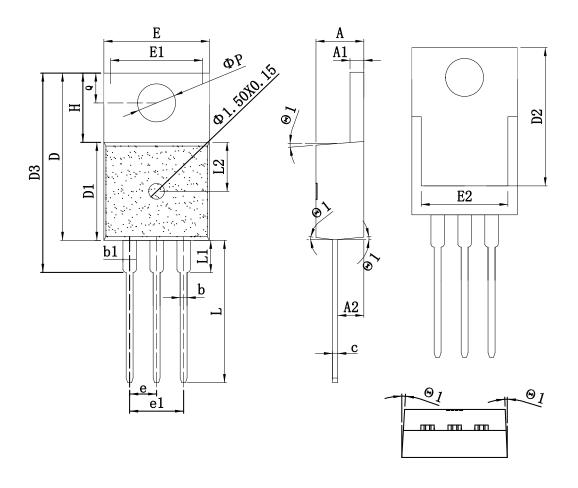


**COMMON DIMENSIONS** (UNITS OF MEASURE=MILLIMETER)

SYMBOL	mm			SYMBOL	mm			
	MIN	TYP	MAX	SIMBOL	MIN	TYP	MAX	
A	4.15	4.50	4.80	E1	8.25	8.70	9.15	
A1	1.15	1.30	1.50	E2	7.20	8.00	8.80	
A2	2.10	2.40	2.65	e	2.38	2.54	2.74	
ь	0.65	0.80	1.00	e1	5.08REF			
b1	1.10	1.33	1.80	Н	6.20	6.50	6.90	
С	0.35	0.50	0.65	L	12.75	13.28	13.70	
D	14.25	15.75	16.15	L1	1	-	3.50	
D1	8.70	9.20	9.60	L2	2.30	4.65	7.00	
D2	12.30	13.10	13.85	φP	3.40	3.65	3.85	
D3	16.20	18.80	20.60	Q	2.50	2.80	3.00	
Е	8.68	10.02	11.00	θ	2°	-	7°	



## **TO-220\_3L-B PACKAGE OUTLINE**



**COMMON DIMENSIONS** (UNITS OF MEASURE=MILLIMETER)

SYMBOL	mm			SYMBOL	mm		
	MIN	TYP	MAX	SIMBOL	MIN	TYP	MAX
A	4.15	4.50	4.80	E1	8.25	8.70	9.15
A1	1.15	1.30	1.50	E2	7.20	8.00	8.80
A2	2.10	2.40	2.65	e	2.38	2.54	2.74
b	0.65	0.80	1.00	e1	5.08REF		
b1	1.10	1.33	1.80	Н	6.20	6.50	6.90
c	0.35	0.50	0.65	L	12.75	13.28	13.70
D	14.25	15.75	16.15	L1	-	-	3.50
D1	8.70	9.20	9.60	L2	2.30	4.65	7.00
D2	12.30	13.10	13.85	φP	3.40	3.65	3.85
D3	16.20	18.80	20.60	Q	2.50	2.80	3.00
Е	8.68	10.02	11.00	θ	2°	-	7°





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