P-Channel 200 V (D-S) Power MOSFET

PRODUCT SUMMARY							
V _{DS} (V)	$R_{DS(on)}(\Omega)(Max.)$		Q _g (Max.)				
- 200	0.5 at V _{GS} = - 10 V	- 10	32 nC				

FEATURES

- DT-Trench Power MOSFET
- 100 % UIS Tested
- Fast switching

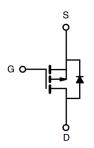
RoHS COMPLIANT

APPLICATIONS

Load Switch







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	LIMIT	UNIT				
Drain-Source Voltage	V _{DS}	-200	V				
Gate-Source Voltage	V_{GS}	± 20]				
Continuous Drain Current d	T _C = 25 °C		-10				
$(T_J = 175 ^{\circ}C)$	T _C = 100 °C	l _D	-6.6				
Pulsed Drain Current	I _{DM}	-40	Α				
Avalanche Current L = 0.1 mH		I _{AS}			-38		
Single Pulse Avalanche Energy ^a		E _{AS}	713	mJ			
Power Dissipation	T _C = 25 °C °	В	128	W			
rower dissipation	T _A = 25 °C b	P _D	4.86]			
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +175	°C				

THERMAL RESISTANCE RATINGS							
PARAMETER	SYMBOL	MAX.	UNIT				
Junction-to-Ambient	PCB mount ^b	R _{thJA}	62	°C/W			
Junction-to-Case		R _{thJC}	1.0	C/VV			

Notes

- a. Duty cycle \leq 1 %.
- b. When mounted on 1" square PCB (FR4 material).
- c. See SOA curve for voltage derating.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 200			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T	I _D = - 250 μA		-193		>1/06
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 5.2		mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 2		- 4	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zara Cata Voltaga Drain Current		V _{DS} = - 200 V, V _{GS} = 0 V			- 100	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 160 V, V _{GS} = 0 V, T _J = 125 °C			- 500	μΑ
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 6 A			0.5	Ω
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -50 \text{ V}, I_D = -6 \text{ A}$	4.5			S
Dynamic ^b						
Input Capacitance	C _{iss}			1230		
Output Capacitance	C _{oss}	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		390		pF
Reverse Transfer Capacitance	C _{rss}			85		1
Total Gate Charge	Q_g			32	45	
Gate-Source Charge	Q _{gs}	$V_{DS} = -160 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -6 \text{ A}$		6		nC
Gate-Drain Charge	Q_{gd}			23		
Gate Resistance	R_g	f = 1 MHz		1.2		Ω
Turn-On Delay Time	t _{d(on)}			15		
Rise Time	t _r	$V_{DD} = -100 \text{ V}, R_{L} = 10 \Omega$		45		ns
Turn-Off Delay Time	t _{d(off)}	$I_D = -6 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 9 \Omega$		40		
Fall Time	t _f			36		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 10	
Pulse Diode Forward Current ^a	I _{SM}				- 40	A
Body Diode Voltage	V_{SD}	I _S = - 6 A			- 5	V
Body Diode Reverse Recovery Time	t _{rr}			205	330	ns
Body Diode Reverse Recovery Charge	Q _{rr}			3	4	μC
Reverse Recovery Fall Time	t _a	$I_F = -10 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		109		ns
Reverse Recovery Rise Time	t _b			116		

Notes:

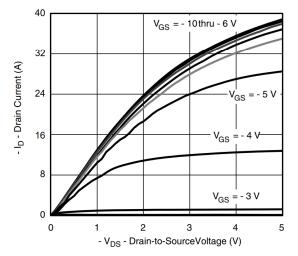
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

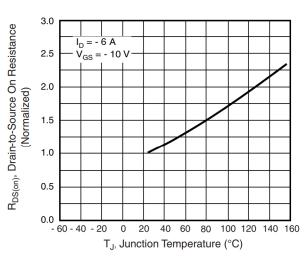
b. Guaranteed by design, not subject to production testing.



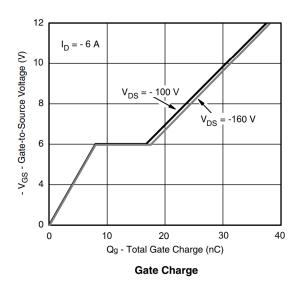
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

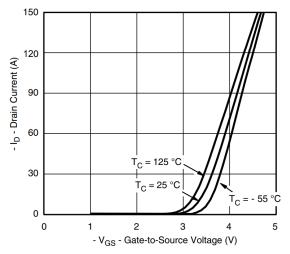


Output Characteristics

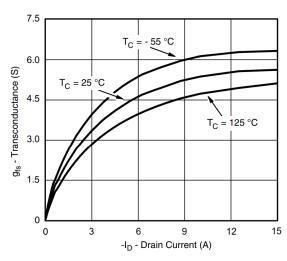


Normalized On-Resistance vs. Temperature

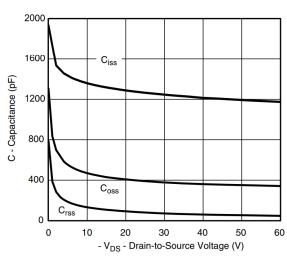




Transfer Characteristics



Transconductance

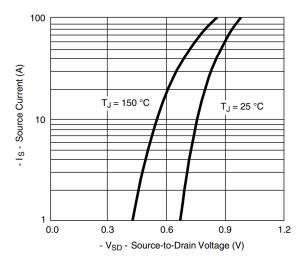


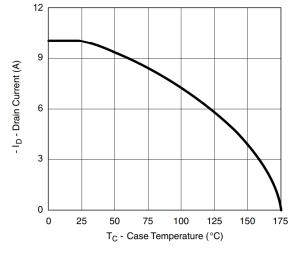
Capacitance





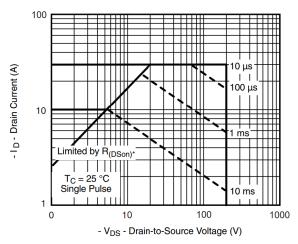
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Source-Drain Diode Forward Voltage

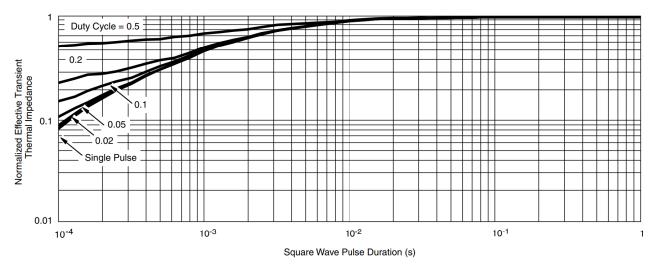
Maximum Avalanche and Drain Current vs. Case Temperature



Maximum Safe Operating Area

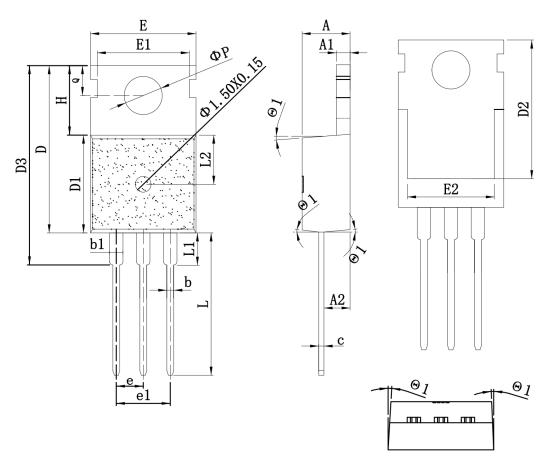


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

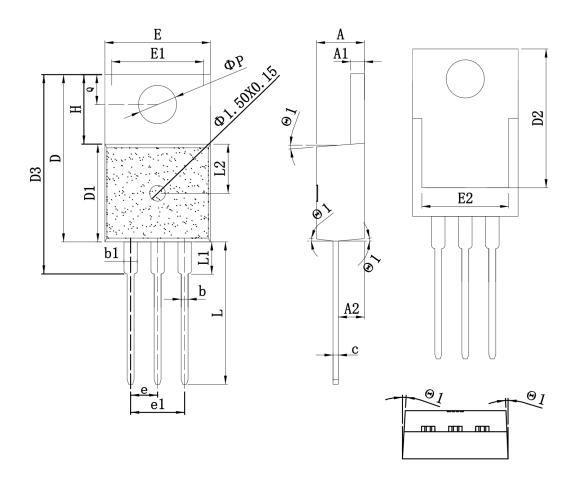
TO-220_3L-A PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	mm			SYMBOL	mm		
STMBOL	MIN	TYP	MAX	STMBOL	MIN	TYP	MAX
A	4.15	4.50	4.80	E1	8.25	8.70	9.15
A1	1.15	1.30	1.50	E2	7.20	8.00	8.80
A2	2.10	2.40	2.65	e	2.38	2.54	2.74
b	0.65	0.80	1.00	e1	5.08REF		
b1	1.10	1.33	1.80	Н	6.20	6.50	6.90
С	0.35	0.50	0.65	L	12.75	13.28	13.70
D	14.25	15.75	16.15	L1	-	-	3.50
D1	8.70	9.20	9.60	L2	2.30	4.65	7.00
D2	12.30	13.10	13.85	φP	3.40	3.65	3.85
D3	16.20	18.80	20.60	Q	2.50	2.80	3.00
Е	8.68	10.02	11.00	θ	2°	-	7°

TO-220_3L-B PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL		mm		SYMBOL	mm			
SIMBOL	MIN	MIN TYP MAX STMBOL	SIMBOL	MIN	TYP	MAX		
A	4.15	4.50	4.80	E1	8.25	8.70	9.15	
A1	1.15	1.30	1.50	E2	7.20	8.00	8.80	
A2	2.10	2.40	2.65	e	2.38	2.54	2.74	
b	0.65	0.80	1.00	e1	5.08REF			
b1	1.10	1.33	1.80	Н	6.20	6.50	6.90	
c	0.35	0.50	0.65	L	12.75	13.28	13.70	
D	14.25	15.75	16.15	L1	-	-	3.50	
D1	8.70	9.20	9.60	L2	2.30	4.65	7.00	
D2	12.30	13.10	13.85	φP	3.40	3.65	3.85	
D3	16.20	18.80	20.60	Q	2.50	2.80	3.00	
Е	8.68	10.02	11.00	θ	2°	-	7°	



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