

N-Channel 200 V (D-S) MOSFET

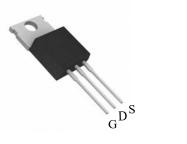
PRODUCT SUMMARY						
V _{DS} (V)	I _D (A)					
200	0.017 at V _{GS} = 10 V	120				

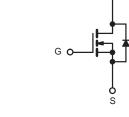
FEATURES

- DT-Trench Power MOSFET
- 175 °C Junction Temperature
- PWM Optimized
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



TO-220 Pin Configuration





Top View

N-Channel MOSFET

APPLICATIONS

Primary Side Switch

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)							
Parameter	Symbol	Limit	Unit				
Drain-Source Voltage	V _{DS}	200	V				
Gate-Source Voltage		V_{GS}	± 20	7 v			
Continuous Drain Current /T 175 9C\D	T _C = 25 °C		120				
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 125 °C	l I _D	80	1			
Pulsed Drain Current	I _{DM}	320	А				
Continuous Source Current (Diode Conduction)	I _S	80	1				
Avalanche Current	I _{AS}	78	1				
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	116	mJ			
Maximum Power Dissipation	T _C = 25 °C	P _D	356 ^b	W			
iviaximum Fower Dissipation	T _A = 25 °C	'D	9 ^a] vv			
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 175	°C				

THERMAL RESISTANCE RATINGS								
Parameter	Symbol	Typical	Maximum	Unit				
lungting to Ameliant	t ≤ 10 s	R _{thJA}		16	°C/W			
Junction-to-Ambient ^a	Steady State	\ \thJA		48				
Junction-to-Case (Drain)	R _{thJC}		1.0					

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. See SOA curve for voltage derating.



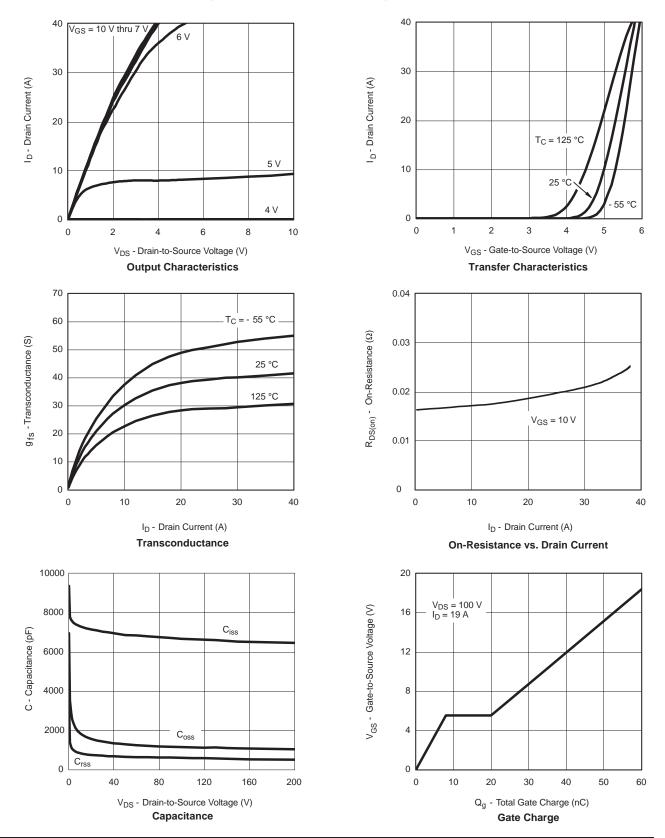
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static					<u> </u>		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	200			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2		5	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = 200 V, V _{GS} = 0 V			1	μА	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 200 V, V _{GS} = 0 V, T _J = 125 °C			50		
		V _{DS} = 200 V, V _{GS} = 0 V, T _J = 175 °C			250		
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	100			Α	
		V _{GS} = 10 V, I _D = 5 A		0.017	0.022	Ω	
	D	V _{GS} = 10 V, I _D = 5 A, T _J = 125 °C			0.040		
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 10 V, I _D = 5 A, T _J = 175 °C			0.060		
		V _{GS} = 6 V, I _D = 5 A		0.030	0.102		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 19 A		76		S	
Dynamic ^a							
Input Capacitance	C _{iss}			6800		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, F = 1 \text{ MHz}$		960			
Reverse Transfer Capacitance	C _{rss}			660			
Total Gate Charge ^c	Q_g			94			
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 19 \text{ A}$		38		nC	
Gate-Drain Charge ^c	Q_{gd}			42			
Gate Resistance	R _g		0.5		2.9	Ω	
Turn-On Delay Time ^c	t _{d(on)}			15	25		
Rise Time ^c	t _r	$V_{DD} = 100 \text{ V}, R_{L} = 5.2 \Omega$		50	75		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong 19 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5 \Omega$		30	45	ns	
Fall Time ^c	t _f			60	90	1	
Source-Drain Diode Ratings and Char	acteristics (7	_C = 25 °C)		•	'		
Pulsed Current	I _{SM}				320	Α	
Diode Forward Voltage ^b	V _{SD}	I _F = 19 A, V _{GS} = 0 V		0.9	1.5	V	
Source-Drain Reverse Recovery Time		I _F = 19 A, dl/dt = 100 A/μs		180	250	ns	

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %. c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

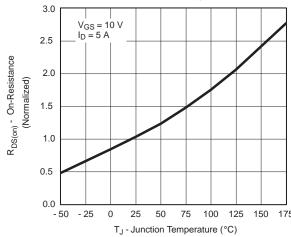


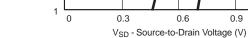
 $T_J = 25 \, ^{\circ}C$

1.2

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





T_J = 150 °C

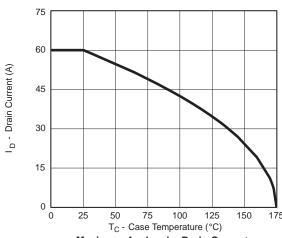
100

10

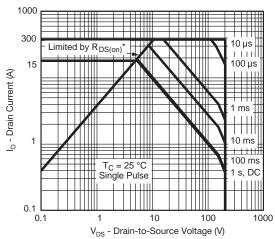
Is - Source Current (A)

On-Resistance vs. Junction Temperature

THERMAL RATINGS



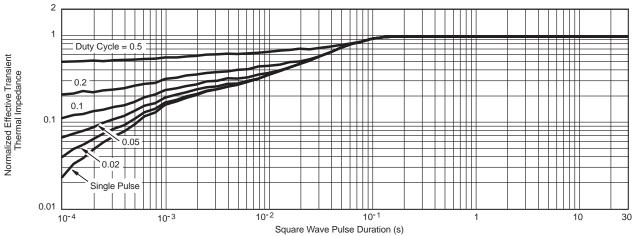
Maximum Avalanche Drain Current vs. Case Temperature



Source-Drain Diode Forward Voltage

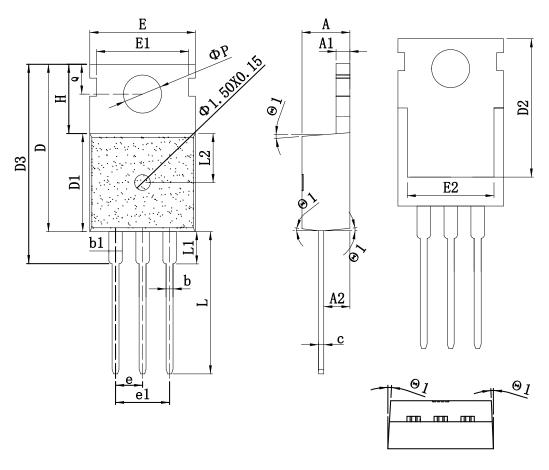
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

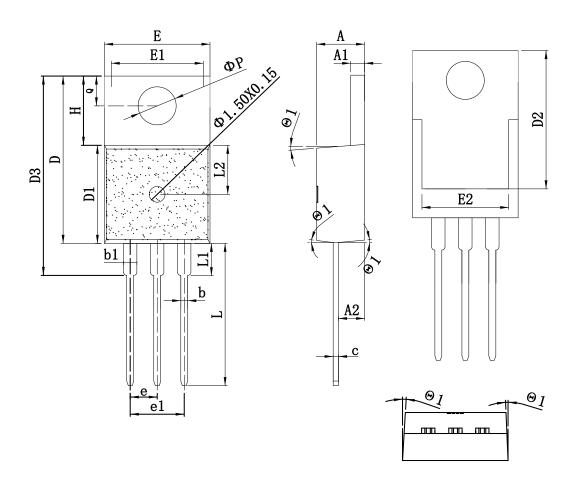
TO-220_3L-A PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	mm			SYMBOL	mm			
SIMBOL	MIN	TYP	MAX	SIMBOL	MIN	TYP	MAX	
A	4.15	4.50	4.80	E1	8.25	8.70	9.15	
A1	1.15	1.30	1.50	E2	7.20	8.00	8.80	
A2	2.10	2.40	2.65	e	2.38	2.54	2.74	
ь	0.65	0.80	1.00	e1	5.08REF			
b1	1.10	1.33	1.80	Н	6.20	6.50	6.90	
c	0.35	0.50	0.65	L	12.75	13.28	13.70	
D	14.25	15.75	16.15	L1	1	-	3.50	
D1	8.70	9.20	9.60	L2	2.30	4.65	7.00	
D2	12.30	13.10	13.85	φP	3.40	3.65	3.85	
D3	16.20	18.80	20.60	Q	2.50	2.80	3.00	
Е	8.68	10.02	11.00	θ	2°	-	7°	

TO-220_3L-B PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	mm			SYMBOL	mm		
	MIN	TYP	MAX	SIMBOL	MIN	TYP	MAX
A	4.15	4.50	4.80	E1	8.25	8.70	9.15
A1	1.15	1.30	1.50	E2	7.20	8.00	8.80
A2	2.10	2.40	2.65	e	2.38	2.54	2.74
b	0.65	0.80	1.00	e1	5.08REF		
b1	1.10	1.33	1.80	Н	6.20	6.50	6.90
c	0.35	0.50	0.65	L	12.75	13.28	13.70
D	14.25	15.75	16.15	L1	-	-	3.50
D1	8.70	9.20	9.60	L2	2.30	4.65	7.00
D2	12.30	13.10	13.85	φP	3.40	3.65	3.85
D3	16.20	18.80	20.60	Q	2.50	2.80	3.00
Е	8.68	10.02	11.00	θ	2°	-	7°

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