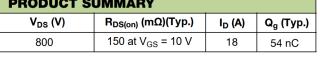


N-Channel 800 V (D-S) Super Junction Power MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A)	Q _g (Typ.)			
800	150 at V _{GS} = 10 V	18	54 nC			





TO-220 FULLPAK Top View

FEATURES

- DT-SJ Power MOSFET
- 100 % Rg and UIS tested
- Low R_{DS(ON)}×FOM
- Extremely low switching loss
- · Excellent stability and uniformity

APPLICATIONS

- PC power
- Telecom power
- Server power
- LED Light
- EV Charger

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage(Typ.)		V _{DS}	800	V	
Gate-Source Voltage		V _{GS}	± 30		
Continuous Dunin Courset II 150 °C)	T _C = 25 °C		18		
Continuous Drain Current (T _J = 150 °C) ^a	T _C = 100 °C	I _D	11.4	A	
Pulsed Drain Current ^b		I _{DM}	39		
Single Avalanche Energy ^e		E _{AS}	245	mJ	
Maximum Power Dissipation ^c	T _C = 25 °C	В	34	W	
	T _C = 100 °C	$ P_D$	13.6	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +150	°C	

N-Channel MOSFET

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	62	°C/W		
Junction-to-Case (Drain)	R _{thJC}	3.67			

Notes

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-case thermal resistance.
- d. The value of R_{0JA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.
- e. V_{DD} =100 V, V_{GS} =10 V, L=80 mH, starting T_j =25 °C.



SPECIFICATIONS (T _J = 25 °C) PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	800	-	-	V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.9	-	3.9	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 30 \text{ V}$	-	-	± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 800 V, V _{GS} = 0 V	-	-	10	μA	
Drain-Source On-State Resistance a	Book)	V _{GS} = 10 V, I _D = 9 A	-	150	200		
Drain-Source On-State Nesistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 9 A, T _j =150 °C	-	420	-	mΩ	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 50 V, I _D = 9 A	-	15	-	S	
Dynamic ^b							
Input Capacitance	C _{iss}		-	2620	-	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 50 \text{ V}, f = 100 \text{ KHz}$	-	111	-		
Reverse Transfer Capacitance	C _{rss}		-	4.4	-		
Total Gate Charge ^c	Qg		-	54	-		
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 9 \text{ A}$	-	11	-	nC	
Gate-Drain Charge ^c	Q _{gd}		-	17	-		
Gate Resistance	R_g	f = 1 MHz	-	17.2	-	Ω	
Turn-On Delay Time ^c	t _{d(on)}		-	28	-		
Rise Time ^c	t _r	$V_{DD} = 400 \text{ V}, I_D = 9 \text{ A},$	-	17	-		
Turn-Off Delay Time ^c	t _{d(off)}	$V_{GEN} = 10 \text{ V}, R_g = 2 \Omega$	-	146	-	ns	
Fall Time ^c	t _f		-	18	-		
Drain-Source Body Diode Ratings ar	nd Characteris	stics ^b (T _C = 25 °C)					
Continuous Source Current	Is	T _C = 25 °C	1	-	18	Α	
Pulsed Current (t = 100 µs)	I _{SM}		-	-	39	Α	
Forward Voltage ^a	V _{SD}	I _S = 18 A, V _{GS} = 0 V	-	-	1.3	V	
Reverse Recovery Time	t _{rr}		-	342	-	ns	
Reverse Recovery Charge	Q _{rr}	$I_S = 9 A$, $di/dt = 100 A/\mu s$	-	5	-	μC	
Peak reverse recovery current	I _{rrm}		-	29	-	Α	

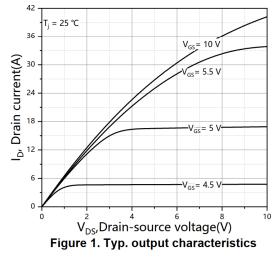
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



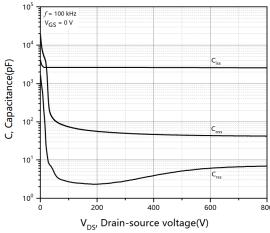


Figure 3. Typ. capacitances

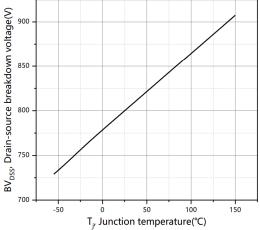


Figure 5. Drain-source breakdown voltage

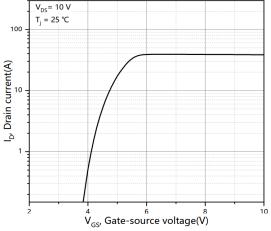


Figure 2. Typ. transfer characteristics

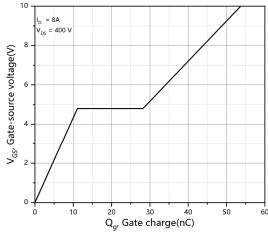


Figure 4. Typ. gate charge

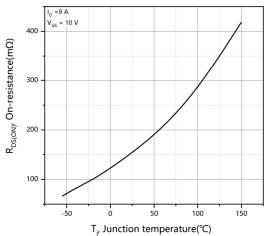
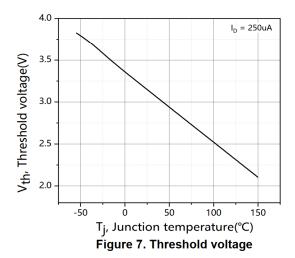


Figure 6. Drain-source on-state resistance



TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



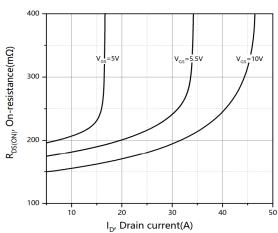


Figure 9. Drain-source on-state resistance

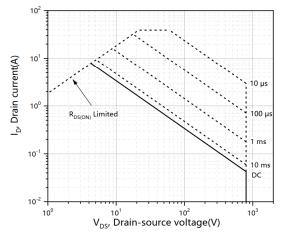


Figure 11. Safe operation area Tc=25 °C

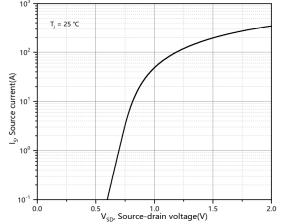


Figure 8. Forward characteristic of body diode

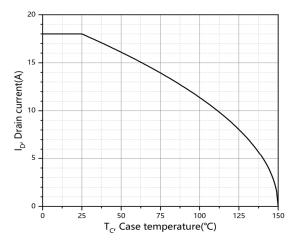


Figure 10. Drain current

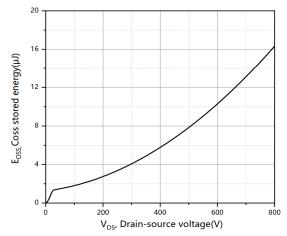
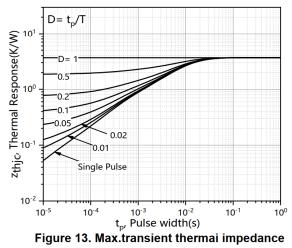


Figure 12. Typ. Coss stored energy

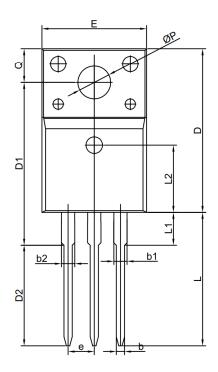
TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)

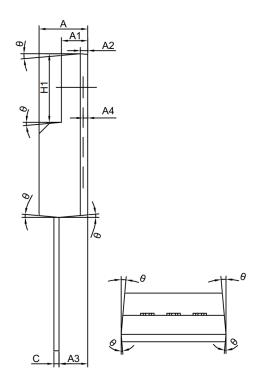


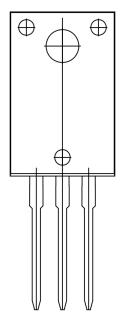
Din-Tek



TO-220F-3L PACKAGE OUTLINE







COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
Α	4.30	4.72	5.10
A1	2.25	2.56	2.90
A2	0.72 REF		
A3	2.28 2.78 3.50		
A4	C).45 MA)	(
b	0.65	-	0.95
b1	1.00	-	1.55
b2	-	-	1.55
С	0.40	0.50	0.65
D	15.47	15.87	16.37
D1	15.35	15.75	16.25
Е	9.76	10.16	10.76
е	2.54 BSC		
H1	6.28	6.68	7.08
L	12.48	12.98	13.50
L1	2.90	-	3.80
L2	2.54 BSC		
ØP	2.98	3.18	3.50
Q	3.00	-	3.60
θ	3°	5°	7°





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