

N-Channel 650 V (D-S) Super Junction Power MOSFET



RoHS
COMPLIANT

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (mΩ)(Max.)	I _D (A)	Q _g (Typ.)
650	190 at V _{GS} = 10 V	20	44 nC

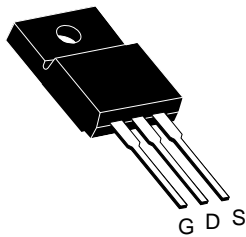
FEATURES

- DT-SJ Power MOSFET
- 100 % Rg and UIS tested
- Low R_{DS(ON)}×FOM
- Extremely low switching loss
- Excellent stability and uniformity

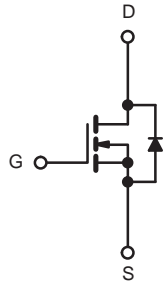
APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

TO-220 FULLPAK



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage(Typ.)	V _{DS}	650	V
Gate-Source Voltage	V _{GS}	± 30	
Continuous Drain Current (T _J = 150 °C) ^a	I _D	T _C = 25 °C	20
		T _C = 100 °C	13
Pulsed Drain Current ^b	I _{DM}	60	A
Single Avalanche Energy ^e	E _{AS}	390	mJ
Maximum Power Dissipation ^c	P _D	T _C = 25 °C	27
		T _C = 100 °C	10.8
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	50.6	°C/W
Junction-to-Case (Drain)	R _{thJC}	4.6	

Notes

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- P_d is based on max. junction temperature, using junction-case thermal resistance.
- The value of R_{thJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
- V_{DS}=150 V, V_{GS}=10 V, L=10.8 mH, starting T_J=25 °C.

SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	650	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2.6	-	4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 30\text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	-	-	10	μA
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$	-	140	190	m Ω
		$V_{GS} = 10\text{ V}, I_D = 10\text{ A}, T_J = 150\text{ }^\circ\text{C}$	-	390	-	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10\text{ V}, I_D = 4\text{ A}$	-	9	-	S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 100\text{ V}, f = 100\text{ KHz}$	-	1670	-	pF
Output Capacitance	C_{oss}		-	83	-	
Reverse Transfer Capacitance	C_{rss}		-	0.8	-	
Total Gate Charge ^c	Q_g	$V_{DS} = 520\text{ V}, V_{GS} = 10\text{ V}, I_D = 10\text{ A}$	-	4	-	nC
Gate-Source Charge ^c	Q_{gs}		-	9	-	
Gate-Drain Charge ^c	Q_{gd}		-	18	-	
Gate Resistance	R_g	$f = 1\text{ MHz}$	-	20	-	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 520\text{ V}, I_D = 20\text{ A},$ $V_{GEN} = 10\text{ V}, R_g = 25\text{ }\Omega$	-	50	-	ns
Rise Time ^c	t_r		-	61	-	
Turn-Off Delay Time ^c	$t_{d(off)}$		-	195	-	
Fall Time ^c	t_f		-	55	-	
Drain-Source Body Diode Ratings and Characteristics ^b ($T_C = 25\text{ }^\circ\text{C}$)						
Continuous Source Current	I_S	$T_C = 25\text{ }^\circ\text{C}$	-	-	20	A
Pulsed Current ($t = 100\text{ }\mu\text{s}$)	I_{SM}		-	-	60	A
Forward Voltage ^a	V_{SD}	$I_S = 8\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.2	V
Reverse Recovery Time	t_{rr}	$I_S = 20\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$	-	350	-	ns
Reverse Recovery Charge	Q_{rr}		-	6.2	-	μC
Peak reverse recovery current	I_{rrm}		-	-36	-	A

Notes

- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)

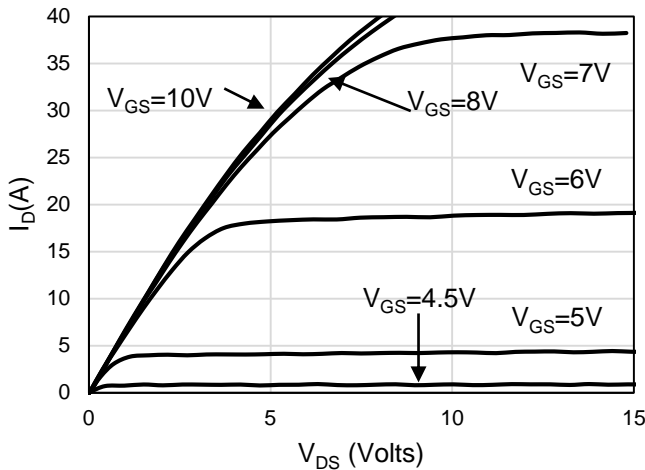


Figure 1: On-Region Characteristics

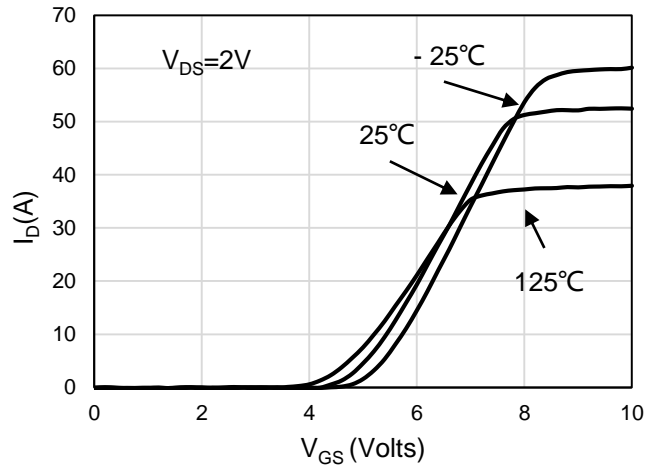


Figure 2: Transfer Characteristics

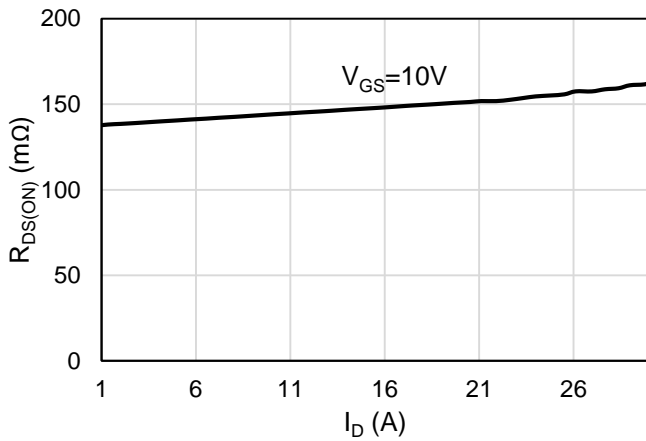


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

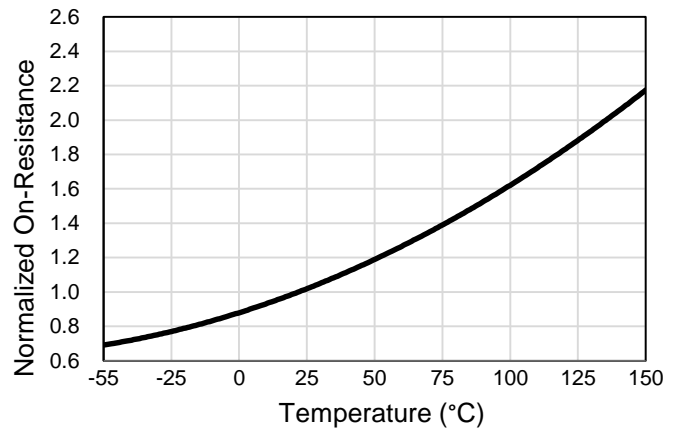


Figure 4: On-Resistance vs. Junction Temperature

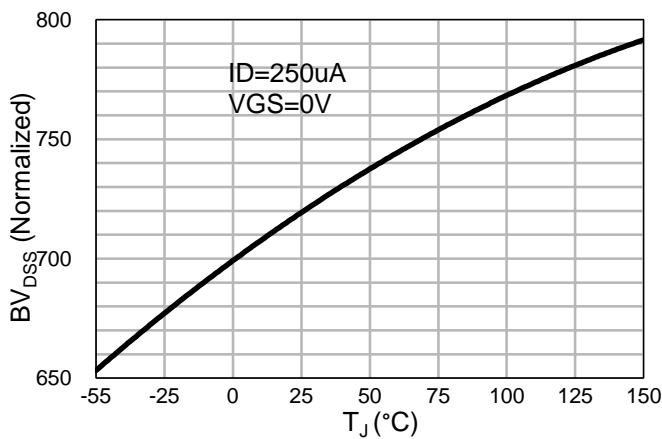


Figure 5: Break Down vs. Junction Temperature

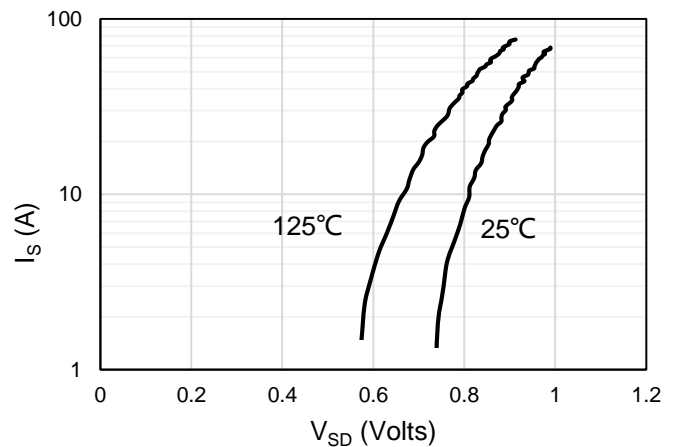


Figure 6: Body-Diode Characteristics

TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

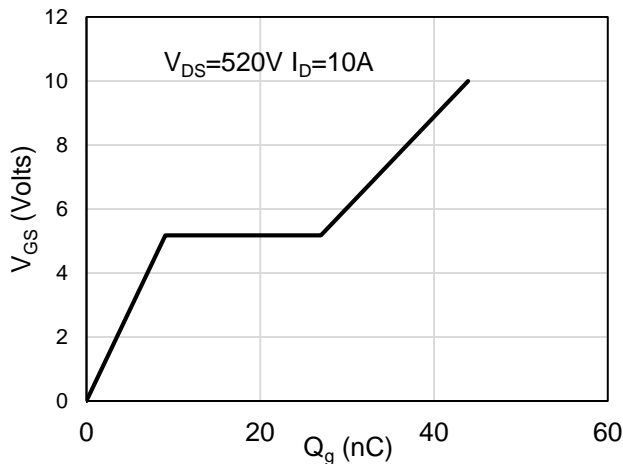


Figure 7: Gate-Charge Characteristics

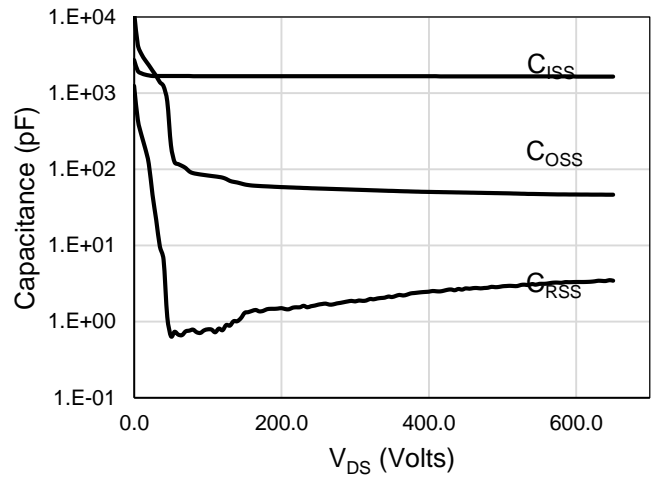


Figure 8: Capacitance Characteristics

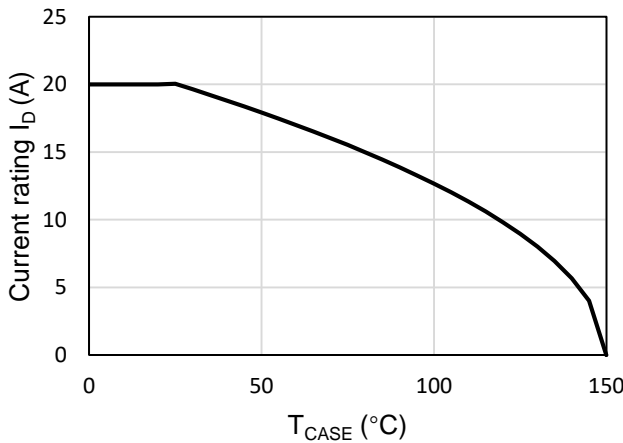


Figure 9: Current De-rating

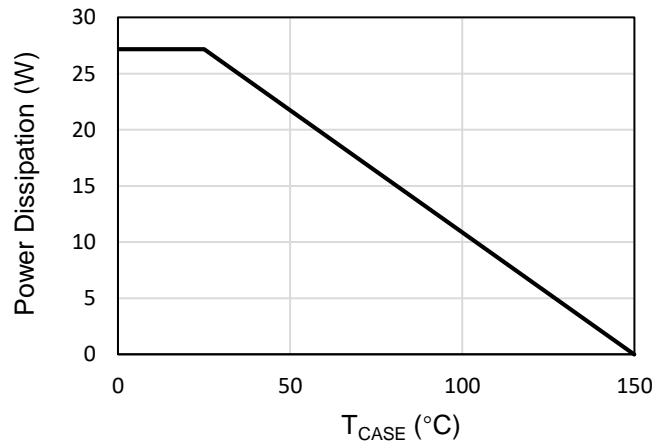


Figure 10: Power De-rating

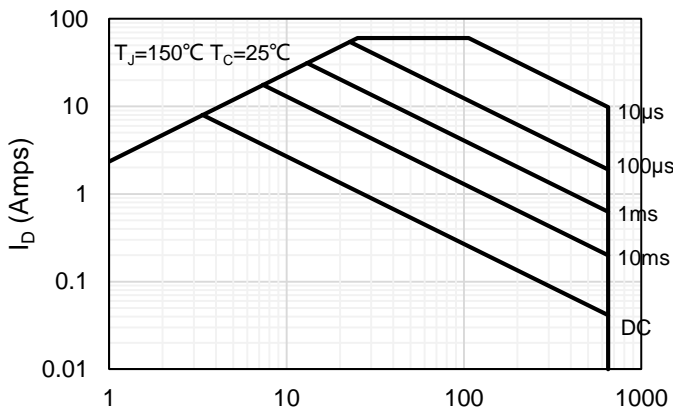


Figure 11: Maximum Forward Biased Safe Operating Area

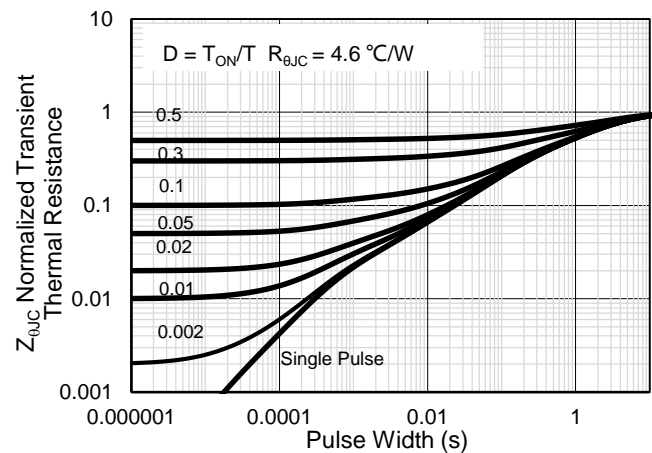
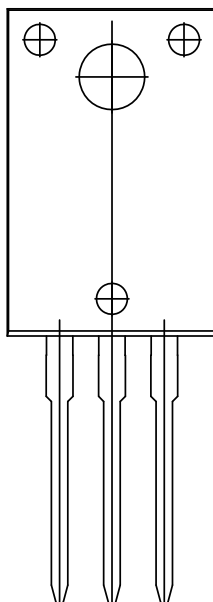
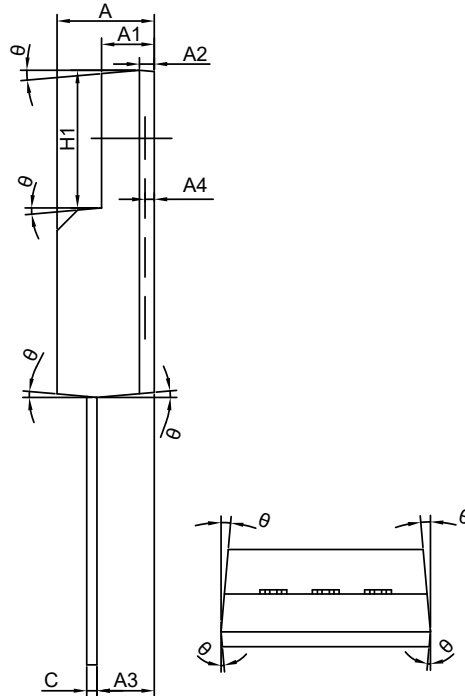
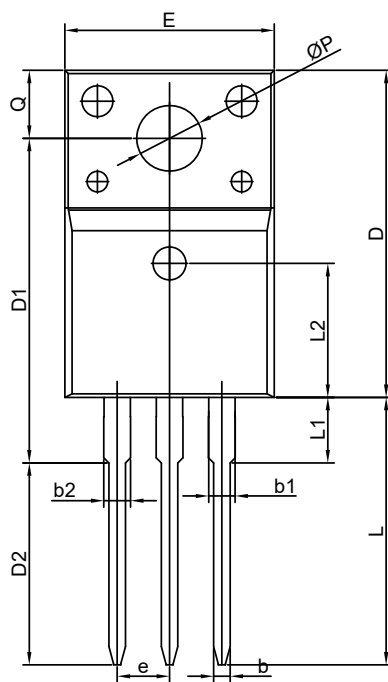


Figure 12: Normalized Maximum Transient Thermal Impedance

TO-220F-3L PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	4.30	4.72	5.10
A1	2.25	2.56	2.90
A2	0.72 REF		
A3	2.28	2.78	3.50
A4	0.45 MAX		
b	0.65	-	0.95
b1	1.00	-	1.55
b2	-	-	1.55
c	0.40	0.50	0.65
D	15.47	15.87	16.37
D1	15.35	15.75	16.25
E	9.76	10.16	10.76
e	2.54 BSC		
H1	6.28	6.68	7.08
L	12.48	12.98	13.50
L1	2.90	-	3.80
L2	2.54 BSC		
ØP	2.98	3.18	3.50
Q	3.00	-	3.60
θ	3°	5°	7°

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