

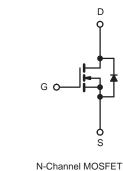
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## N-Channel 150 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) MAX.	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.)			
150	0.028 at $V_{GS}$ = 10 V	48	35 nC			

# TO-220 Pin Configuration





### **FEATURES**

- Maximum 175 °C junction temperature
- 100 % R<sub>g</sub> and UIS tested

### APPLICATIONS

- Power supplies:
  - Uninterruptible power supplies
  - AC/DC switch-mode power supplies
  - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Battery management

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_c = 25 \text{ °C}$ , unless otherwise noted)							
PARAMETER	SYMBOL	LIMIT	UNIT				
Drain-Source Voltage	V <sub>DS</sub>	150	V				
Gate-Source Voltage	V <sub>GS</sub>	± 20	v				
Continuous Drain Current (T. 150 °C)	T <sub>C</sub> = 25 °C		48	A			
Continuous Drain Current ( $T_J = 150 \ ^{\circ}C$ )	T <sub>C</sub> = 125 °C	I <sub>D</sub>	35				
Pulsed Drain Current (t = 100 µs)	I <sub>DM</sub>	150	A				
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	41				
Single Avalanche Energy <sup>a</sup>	L = 0.1 mm	E <sub>AS</sub>	85	mJ			
Manimum Davier Diable ation 3	T <sub>C</sub> = 25 °C	– P <sub>D</sub>	120 <sup>b</sup>	w			
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 125 °C	۳D	50 <sup>b</sup>	vv			
Operating Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C				

THERMAL RESISTANCE RATINGS						
PARAMETER	LIMIT	UNIT				
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C M/			
Junction-to-Case (Drain)	R <sub>thJC</sub>	1.75	°C/W			

Notes

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.

c. When mounted on 1" square PCB (FR4 material).

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SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)   PARAMETER SYMBOL TEST CONDITIONS MIN. TYP. MAX. UNIT								
	STWIDOL	TEST CONDITIONS	IVIIIN.	116.	IVIAA.	UNIT		
Static				T		-		
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 250 \mu A$	150	-	-	v		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2	-	4			
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V$ , $V_{GS} = \pm 20 V$	-	-	± 100	nA		
		$V_{DS} = 120 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	μA		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = 120 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 $^{\circ}\text{C}$	-	-	100	μ, ,		
		$V_{DS}$ = 120 V, $V_{GS}$ = 0 V, $T_{J}$ = 175 $^{\circ}\text{C}$	-	-	2	mA		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \! \geq \! 10 \text{ V},  V_{GS} \! = \! 10 \text{ V}$	150	-	-	А		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$	-	0.028	0.038	Ω		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	-	15	-	S		
Dynamic <sup>b</sup>				•				
Input Capacitance	C <sub>iss</sub>		-	2230	-	pF		
Output Capacitance	Coss	$V_{GS}$ = 0 V, $V_{DS}$ = 120V, f = 1 MHz	-	119	-			
Reverse Transfer Capacitance	C <sub>rss</sub>		-	45	-			
Total Gate Charge <sup>c</sup>	Qg		-	35	-			
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS}$ = 120 V, $V_{GS}$ = 10 V, $I_D$ = 30 A	-	11.5	-	nC		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>		-	9.2	-			
Gate Resistance	Rg	f = 1 MHz	0.2	1	1.8	Ω		
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	15	-			
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DS} = 120 \text{ V}, \text{ R}_{\text{I}} = 1.67 \Omega$	-	16	-			
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 30$ A, $V_{GS}$ = 10 V, $R_g$ = 1 $\Omega$	-	27	-	- ns		
Fall Time <sup>c</sup>	t <sub>f</sub>		-	7.5	-			
Drain-Source Body Diode Ratings a	nd Characteri	stics <sup>b</sup> (T <sub>C</sub> = 25 °C)						
Pulsed Current (t = 100 µs)	I <sub>SM</sub>		-	-	150	А		
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0 V	-	0.77	1.2	V		
Reverse Recovery Time	t <sub>rr</sub>		-	60	116	ns		
Peak Reverse Recovery Charge	I <sub>RM(REC)</sub>	I <sub>F</sub> = 30 A, di/dt = 100 A/μs	-	5	10	А		
Reverse Recovery Charge	Q <sub>rr</sub>		-	0.12		μC		

Notes

a. Pulse test; pulse width  $\leq 300~\mu\text{s},$  duty cycle  $\leq 2~\%.$ 

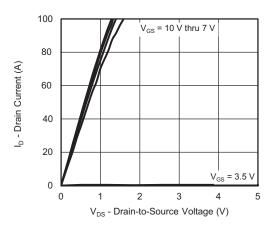
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

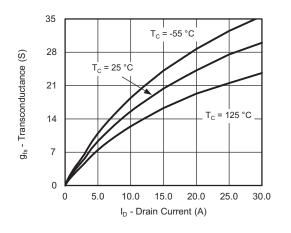
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



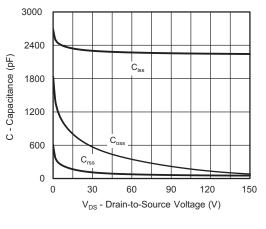
## **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



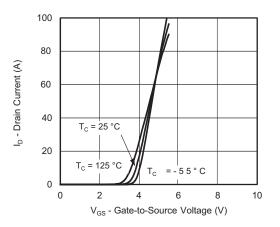
**Output Characteristics** 



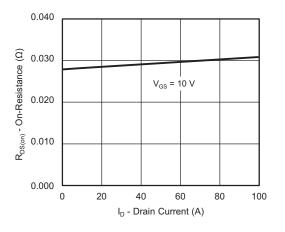
Transconductance



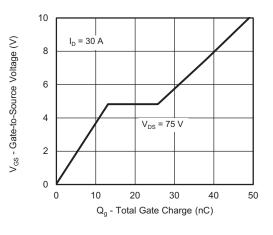
Capacitance



Transfer Characteristics



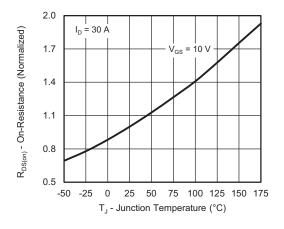
**On-Resistance vs. Drain Current** 



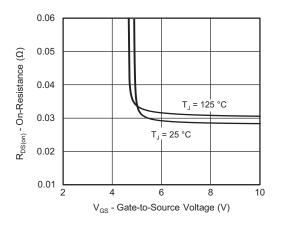
Gate Charge



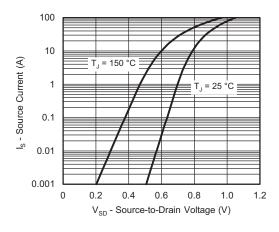
### **TYPICAL CHARACTERISTICS** ( $T_A = 25 \text{ °C}$ , unless otherwise noted)



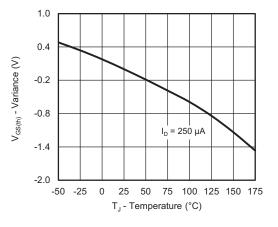
**On-Resistance vs. Junction Temperature** 



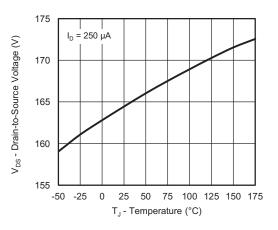
On-Resistance vs. Gate-to-Source Voltage



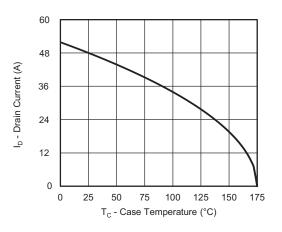
Source Drain Diode Forward Voltage



Threshold Voltage

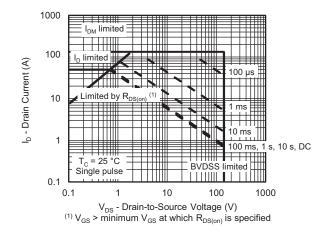


Drain Source Breakdown vs. Junction Temperature

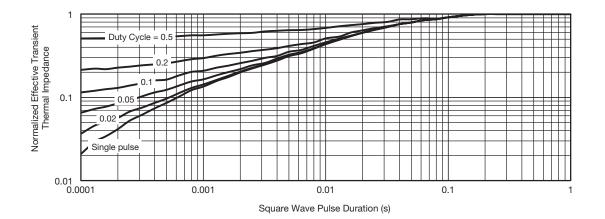


**Current De-Rating** 

### THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

#### Note

• The characteristics shown in the two graphs

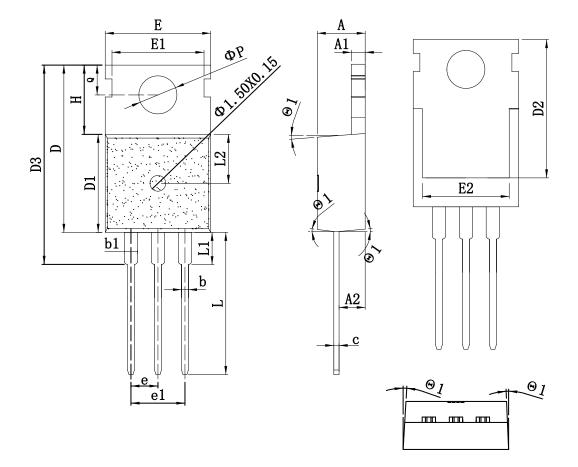
- Normalized Transient Thermal Impedance Junction to Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction to Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



## TO-220\_3L-A PACKAGE OUTLINE

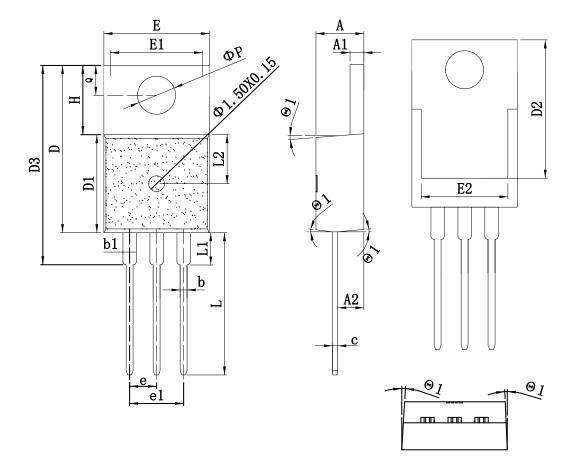


### COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	mm			SYMBOL	mm		
SIMBOL	MIN	TYP	MAX	SIMBOL	MIN	TYP	MAX
А	4.15	4.50	4.80	E1	8.25	8.70	9.15
A1	1.15	1.30	1.50	E2	7.20	8.00	8.80
A2	2.10	2.40	2.65	e	2.38	2.54	2.74
b	0.65	0.80	1.00	e1	5.08REF		
b1	1.10	1.33	1.80	Н	6.20	6.50	6.90
с	0.35	0.50	0.65	L	12.75	13.28	13.70
D	14.25	15.75	16.15	L1	-	-	3.50
D1	8.70	9.20	9.60	L2	2.30	4.65	7.00
D2	12.30	13.10	13.85	φP	3.40	3.65	3.85
D3	16.20	18.80	20.60	Q	2.50	2.80	3.00
Е	8.68	10.02	11.00	θ	2°	-	7°



## TO-220\_3L-B PACKAGE OUTLINE



### COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	mm			SYMBOL	mm		
SIMBOL	MIN	TYP	MAX	SIMBOL	MIN	TYP	MAX
А	4.15	4.50	4.80	E1	8.25	8.70	9.15
A1	1.15	1.30	1.50	E2	7.20	8.00	8.80
A2	2.10	2.40	2.65	e	2.38	2.54	2.74
b	0.65	0.80	1.00	e1	5.08REF		
b1	1.10	1.33	1.80	Н	6.20	6.50	6.90
с	0.35	0.50	0.65	L	12.75	13.28	13.70
D	14.25	15.75	16.15	L1	-	-	3.50
D1	8.70	9.20	9.60	L2	2.30	4.65	7.00
D2	12.30	13.10	13.85	φP	3.40	3.65	3.85
D3	16.20	18.80	20.60	Q	2.50	2.80	3.00
Е	8.68	10.02	11.00	θ	2°	-	7°



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