

N-Channel 700 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY		
V_{DS} (V)	700	
$R_{DS(on)}$ (Ω)	$V_{GS} = 10$ V	1.2
Q_g (Max.) (nC)	60	
Q_{gs} (nC)	8.3	
Q_{gd} (nC)	30	
Configuration	Single	

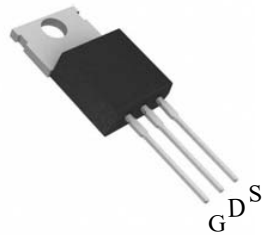
FEATURES

- Isolated Package
- Low Thermal Resistance
- Sink to Lead Creepage Dist. = 4.8 mm
- High Voltage Isolation = 2.5 kV_{RMS} (t = 60 s, f = 60 Hz)
- Dynamic dV/dt Rating
- Lead (Pb)-free Available

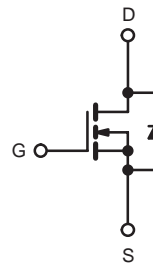


Available
RoHS*
COMPLIANT

TO-220 Pin Configuration



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	700	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	V_{GS} at 10 V	$T_C = 25$ °C	A
		$T_C = 100$ °C	
Pulsed Drain Current ^a	I_{DM}	14	
Linear Derating Factor		0.32	W/°C
Single Pulse Avalanche Energy ^b	E_{AS}	500	mJ
Repetitive Avalanche Current ^a	I_{AR}	3.5	A
Repetitive Avalanche Energy ^a	E_{AR}	4.0	mJ
Maximum Power Dissipation	$T_C = 25$ °C	P_D	40
Peak Diode Recovery dV/dt ^c		dV/dt	3.0
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to + 150	°C
Soldering Recommendations (Peak Temperature)	for 10 s	300 ^d	
Mounting Torque	6-32 or M3 screw		10
			1.1
			lbf · in
			N · m

Notes


- Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- $V_{DD} = 50$ V, starting $T_J = 25$ °C, L = 74 mH, $R_G = 25$ Ω , $I_{AS} = 3.5$ A (see fig. 12).
- $I_{SD} \leq 3.2$ A, $dI/dt \leq 80$ A/ μ s, $V_{DD} \leq V_{DS}$, $T_J \leq 150$ °C.
- 1.6 mm from case.

* Pb containing terminations are not RoHS compliant, exemptions may apply

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R_{thJA}	-	65	°C/W
Maximum Junction-to-Case (Drain)	R_{thJC}	-	3.1	

SPECIFICATIONS $T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	700	-	-	V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	Reference to $25\text{ }^\circ\text{C}$, $I_D = 1\text{ mA}$	-	0.70	-	V/°C
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2.0	-	4.0	V
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 600\text{ V}, V_{GS} = 0\text{ V}$	-	-	100	μA
		$V_{DS} = 480\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	500	
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}$ $I_D = 2.1\text{ A}^b$	-	-	1.2	Ω
Forward Transconductance	g_{fs}	$V_{DS} = 50\text{ V}, I_D = 2.1\text{ A}$	4.9	-	-	S
Dynamic						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V},$ $V_{DS} = 25\text{ V},$ $f = 1.0\text{ MHz}$, see fig. 5	-	1300	-	pF
Output Capacitance	C_{oss}		-	160	-	
Reverse Transfer Capacitance	C_{rss}		-	30	-	
Drain to Sink Capacitance	C	$f = 1.0\text{ MHz}$	-	12	-	
Total Gate Charge	Q_g	$V_{GS} = 10\text{ V}$ $I_D = 3.2\text{ A}, V_{DS} = 360\text{ V},$ see fig. 6 and 13 ^b	-	-	60	nC
Gate-Source Charge	Q_{gs}		-	-	8.3	
Gate-Drain Charge	Q_{gd}		-	-	30	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 300\text{ V}, I_D = 3.2\text{ A},$ $R_G = 9.1\text{ }\Omega, R_D = 47\text{ }\Omega,$ see fig. 10 ^b	-	13	-	ns
Rise Time	t_r		-	18	-	
Turn-Off Delay Time	$t_{d(off)}$		-	55	-	
Fall Time	t_f		-	20	-	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	MOSFET symbol showing the integral reverse p - n junction diode 	-	-	3.5	A
Pulsed Diode Forward Current ^a	I_{SM}		-	-	14	
Body Diode Voltage	V_{SD}	$T_J = 25\text{ }^\circ\text{C}, I_S = 3.5\text{ A}, V_{GS} = 0\text{ V}^b$	-	-	1.5	V
Body Diode Reverse Recovery Time	t_{rr}	$T_J = 25\text{ }^\circ\text{C}, I_F = 3.2\text{ A}, di/dt = 100\text{ A}/\mu\text{s}^b$	-	470	940	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	4.0	7.9	μC
Forward Turn-On Time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by L_S and L_D)				

Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
 b. Pulse width $\leq 300\text{ }\mu\text{s}$; duty cycle $\leq 2\%$.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

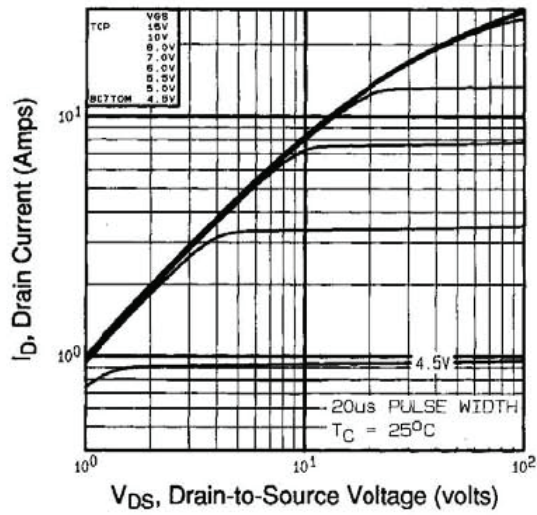


Fig. 1 - Typical Output Characteristics, $T_C = 25\text{ }^\circ\text{C}$

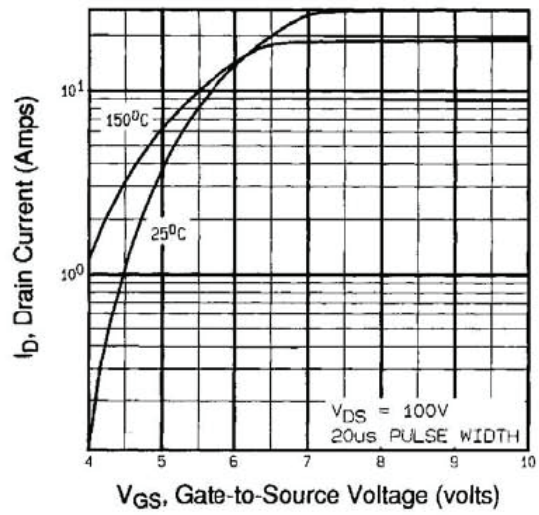


Fig. 3 - Typical Transfer Characteristics

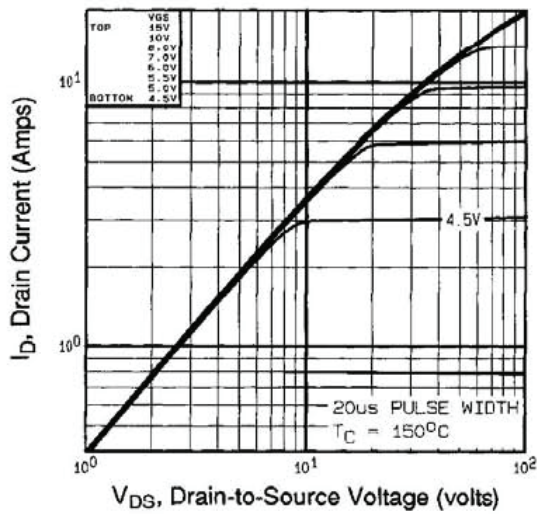


Fig. 2 - Typical Output Characteristics, $T_C = 150\text{ }^\circ\text{C}$

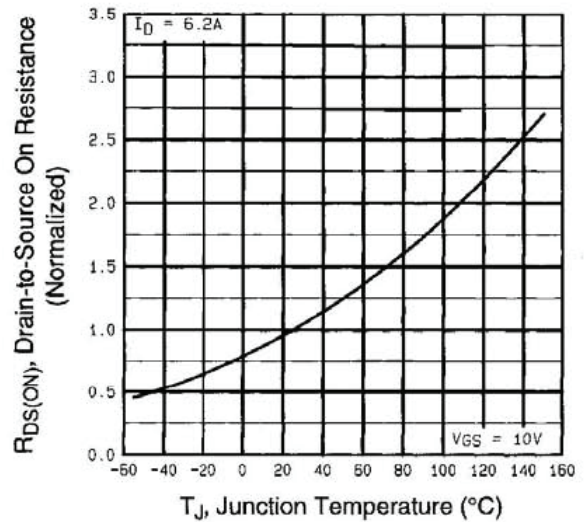


Fig. 4 - Normalized On-Resistance vs. Temperature

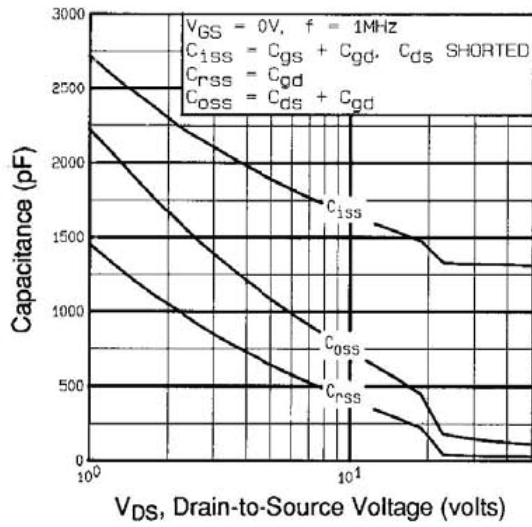


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

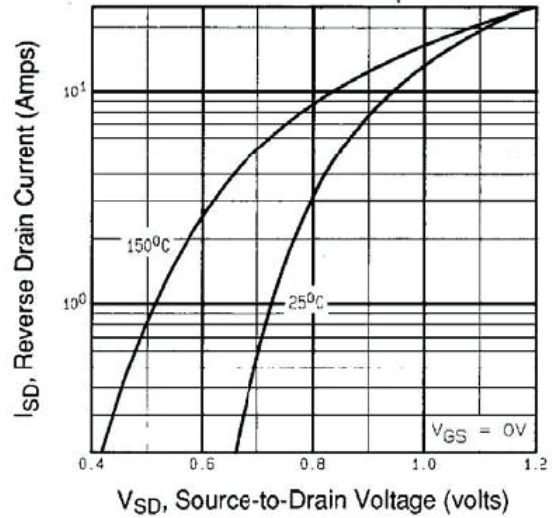


Fig. 7 - Typical Source-Drain Diode Forward Voltage

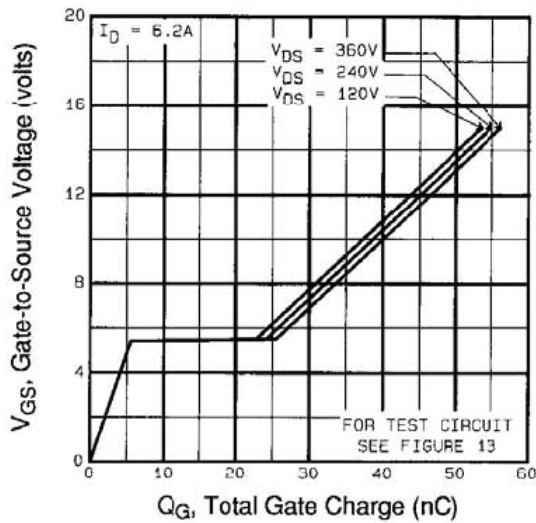


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

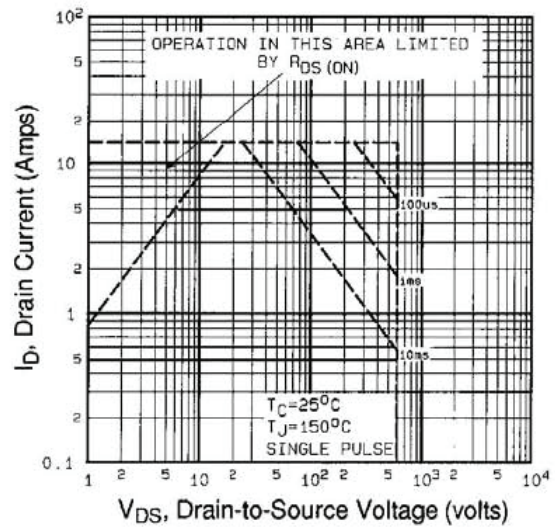


Fig. 8 - Maximum Safe Operating Area

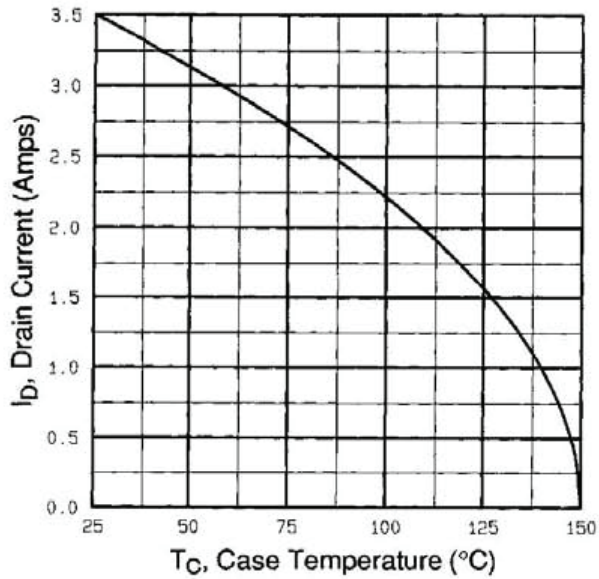


Fig. 9 - Maximum Drain Current vs. Case Temperature

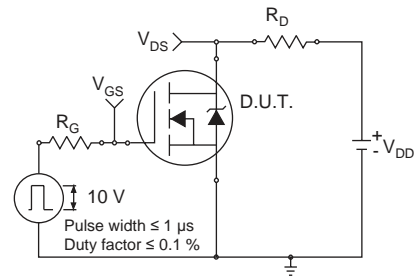


Fig. 10a - Switching Time Test Circuit

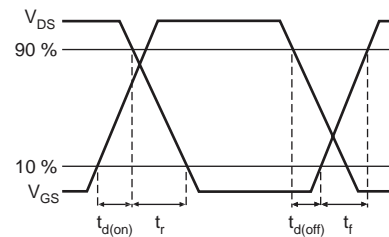


Fig. 10b - Switching Time Waveforms

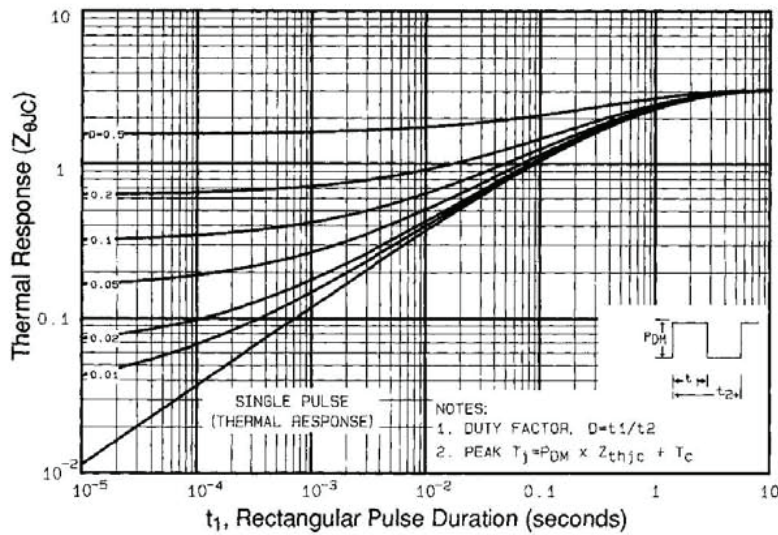


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

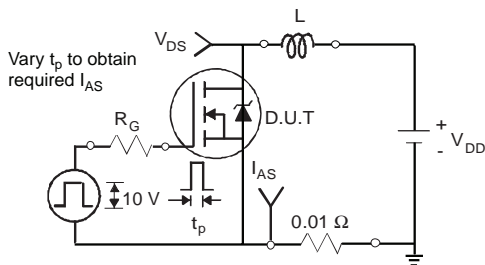


Fig. 12a - Unclamped Inductive Test Circuit

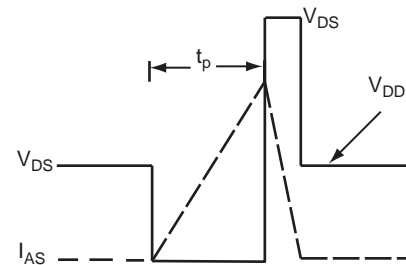


Fig. 12b - Unclamped Inductive Waveforms

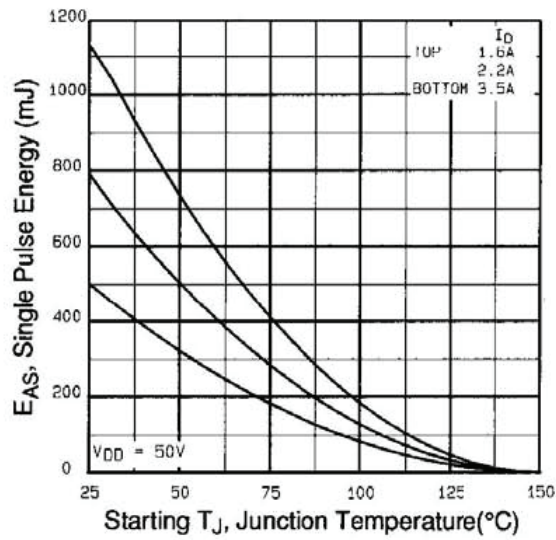


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

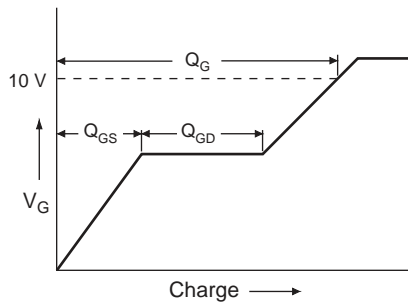


Fig. 13a - Basic Gate Charge Waveform

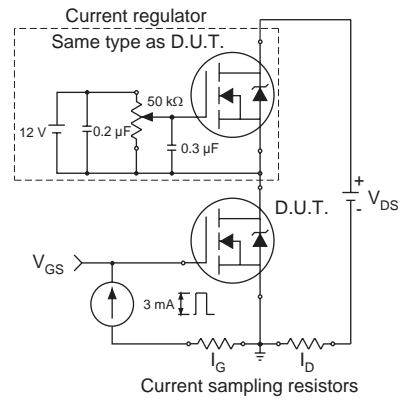
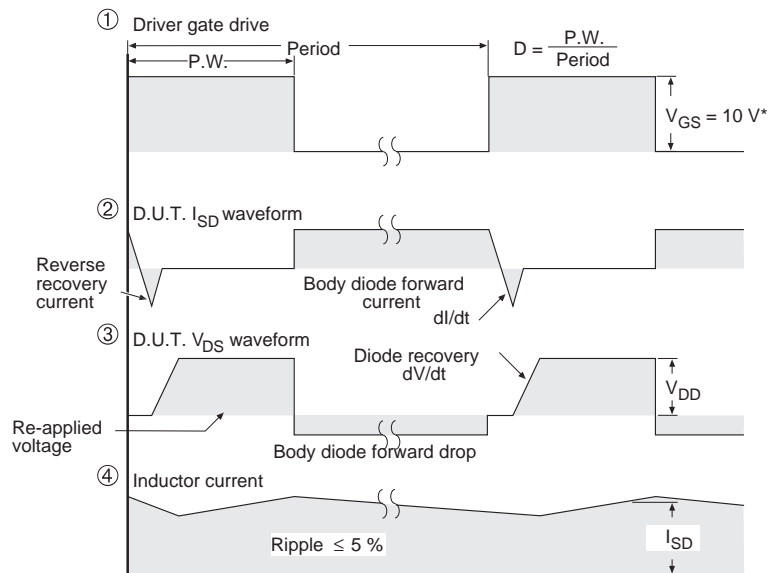
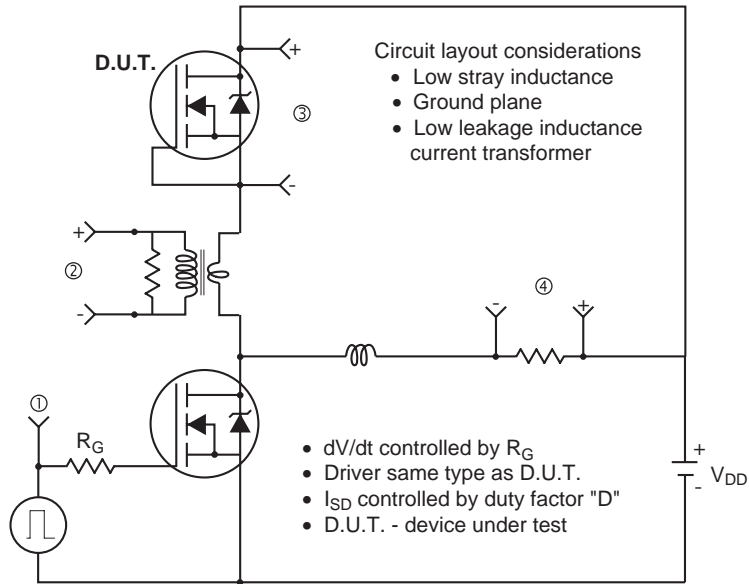


Fig. 13b - Gate Charge Test Circuit

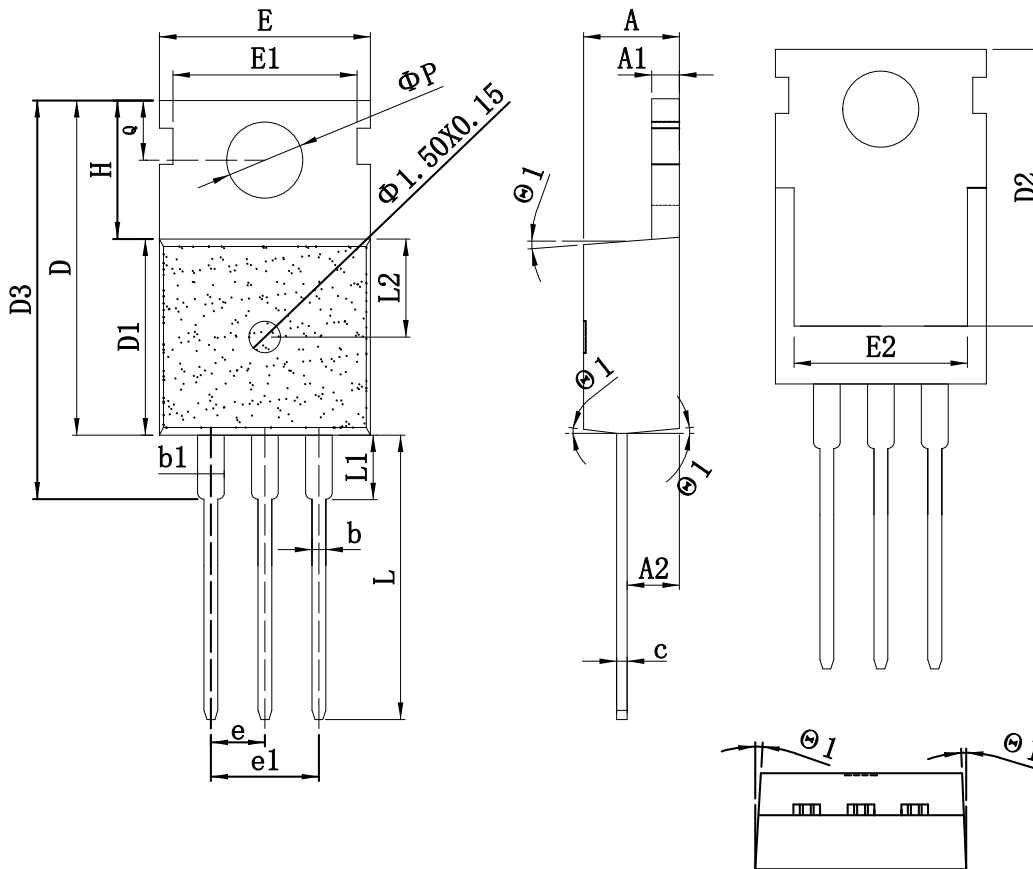
Peak Diode Recovery dV/dt Test Circuit



* $V_{GS} = 5 V$ for logic level devices

Fig. 14 - For N-Channel

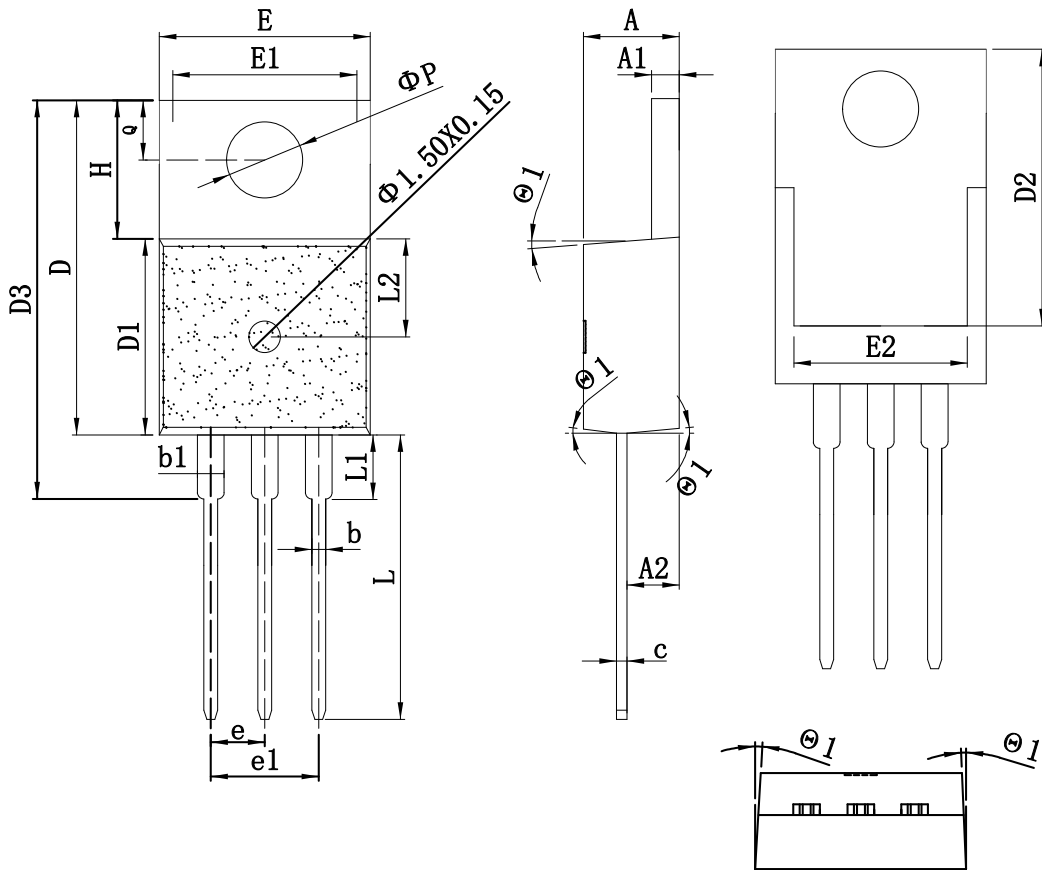
TO-220_3L-A PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	mm			SYMBOL	mm		
	MIN	TYP	MAX		MIN	TYP	MAX
A	4.15	4.50	4.80	E1	8.25	8.70	9.15
A1	1.15	1.30	1.50	E2	7.20	8.00	8.80
A2	2.10	2.40	2.65	e	2.38	2.54	2.74
b	0.65	0.80	1.00	e1	5.08REF		
b1	1.10	1.33	1.80	H	6.20	6.50	6.90
c	0.35	0.50	0.65	L	12.75	13.28	13.70
D	14.25	15.75	16.15	L1	-	-	3.50
D1	8.70	9.20	9.60	L2	2.30	4.65	7.00
D2	12.30	13.10	13.85	φP	3.40	3.65	3.85
D3	16.20	18.80	20.60	Q	2.50	2.80	3.00
E	8.68	10.02	11.00	θ	2°	-	7°

TO-220_3L-B PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	mm			SYMBOL	mm		
	MIN	TYP	MAX		MIN	TYP	MAX
A	4.15	4.50	4.80	E1	8.25	8.70	9.15
A1	1.15	1.30	1.50	E2	7.20	8.00	8.80
A2	2.10	2.40	2.65	e	2.38	2.54	2.74
b	0.65	0.80	1.00	e1	5.08REF		
b1	1.10	1.33	1.80	H	6.20	6.50	6.90
c	0.35	0.50	0.65	L	12.75	13.28	13.70
D	14.25	15.75	16.15	L1	-	-	3.50
D1	8.70	9.20	9.60	L2	2.30	4.65	7.00
D2	12.30	13.10	13.85	ϕP	3.40	3.65	3.85
D3	16.20	18.80	20.60	Q	2.50	2.80	3.00
E	8.68	10.02	11.00	θ	2°	-	7°

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