

## N-Channel 700 V (D-S) Super Junction MOSFET



**RoHS**  
COMPLIANT

### PRODUCT SUMMARY

V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (mΩ) (Typ.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
700	520 at V <sub>GS</sub> = 10 V	7	18 nC

### FEATURES

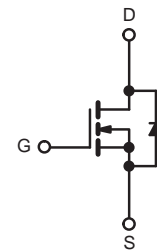
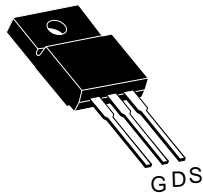
- DT- SJ Power MOSFET
- 100 % R<sub>g</sub> and UIS tested
- Periodic avalanche rated
- High peak current capability

### APPLICATIONS

- Flyback for SMPS
- Charger ,PD Adapter, TV, lighting.

### TO-220F Pin Configuration

Top View



N-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V <sub>DS</sub>	700	V
Gate-Source Voltage	V <sub>GS</sub>	± 30	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	I <sub>D</sub>	T <sub>C</sub> = 25 °C	7
		T <sub>C</sub> = 100 °C	4.4
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	28	A
Single Avalanche Energy	E <sub>AS</sub>	400	mJ
Maximum Power Dissipation <sup>c</sup>	P <sub>D</sub>	T <sub>C</sub> = 25 °C	35
		T <sub>C</sub> = 100 °C	14
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to +150	°C

### THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) <sup>d</sup>	R <sub>thJA</sub>	62	°C/W
Junction-to-Case (Drain)	R <sub>thJC</sub>	3.57	

### Notes

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- P<sub>d</sub> is based on max. junction temperature, using junction-case thermal resistance.
- The value of R<sub>thJA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25 °C.

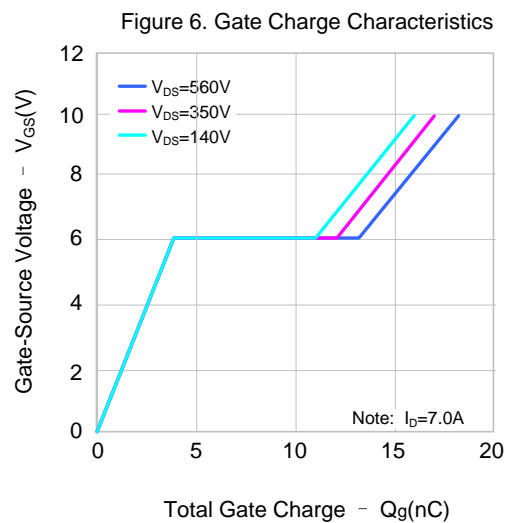
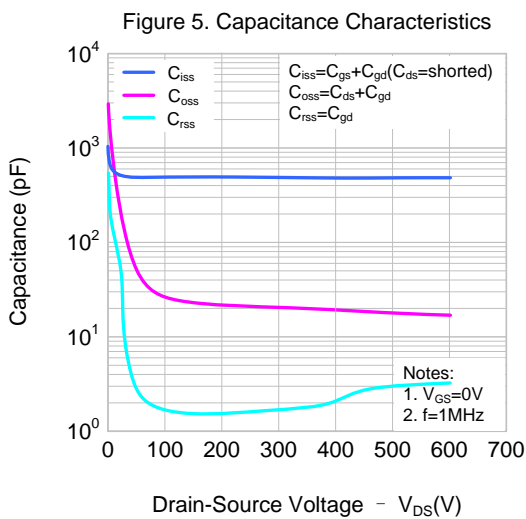
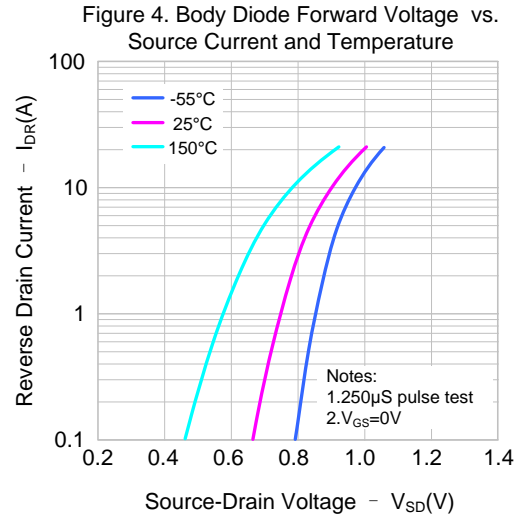
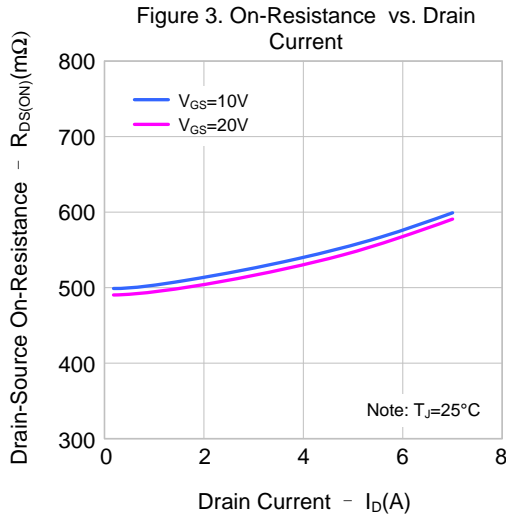
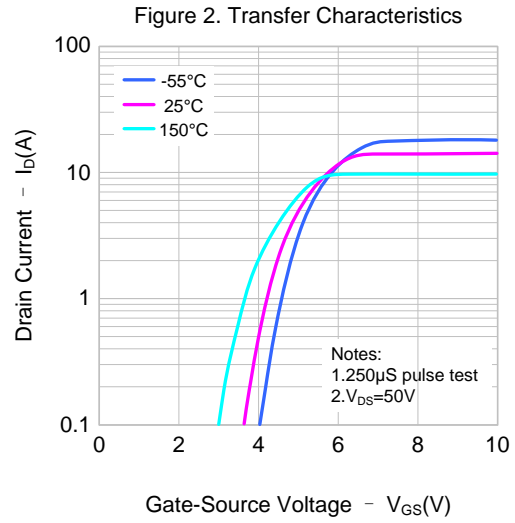
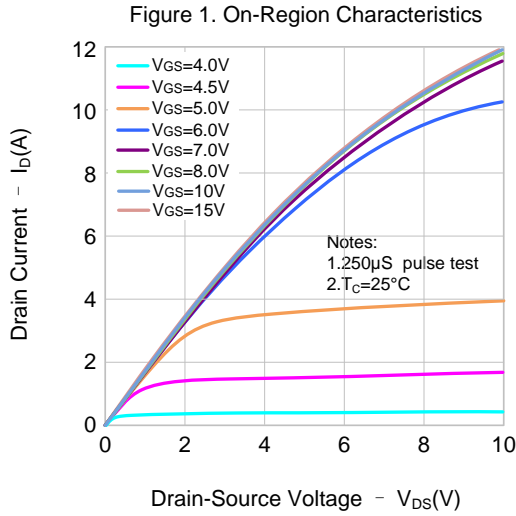
SPECIFICATIONS ( $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	700	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2	-	4	
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 30\text{ V}$	-	-	$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 700\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	$\mu\text{A}$
		$V_{DS} = 560\text{ V}, V_{GS} = 0\text{ V}, T_J = 100\text{ }^\circ\text{C}$	-	-	100	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	7	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 3.5\text{ A}$	-	520	600	m $\Omega$
Forward Transconductance <sup>a</sup>	$g_{fs}$	$V_{DS} = 5\text{ V}, I_D = 3.5\text{ A}$	-	8.5	-	S
<b>Dynamic <sup>b</sup></b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}, V_{DS} = 100\text{ V}, f = 1\text{ MHz}$	-	494	-	pF
Output Capacitance	$C_{oss}$		-	27	-	
Reverse Transfer Capacitance	$C_{rss}$		-	1.7	-	
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{DS} = 560\text{ V}, V_{GS} = 10\text{ V}, I_D = 7\text{ A}$	-	18	-	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$		-	3.9	-	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	9.3	-	
Gate Resistance	$R_g$	$f = 1\text{ MHz}$	-	4.9	-	$\Omega$
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = 350\text{ V}, I_D = 7\text{ A}, R_g = 24\text{ }\Omega$ $V_{GS} = 10\text{ V}$	-	10	-	ns
Rise Time <sup>c</sup>	$t_r$		-	28	-	
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$		-	53	-	
Fall Time <sup>c</sup>	$t_f$		-	26	-	
<b>Drain-Source Body Diode Ratings and Characteristics <sup>b</sup> (<math>T_C = 25\text{ }^\circ\text{C}</math>)</b>						
Continuous Source-Drain Diode Current	$I_S$	$T_C = 25\text{ }^\circ\text{C}$	-	-	7	A
Pulsed Current	$I_{SM}$		-	-	28	A
Forward Voltage <sup>a</sup>	$V_{SD}$	$I_F = 1\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F = 7\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$	-	317	-	ns
Reverse Recovery Charge	$Q_{rr}$		-	2.8	-	$\mu\text{C}$

**Notes**

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.  
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**



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Figure 7. Breakdown Voltage vs. Temperature

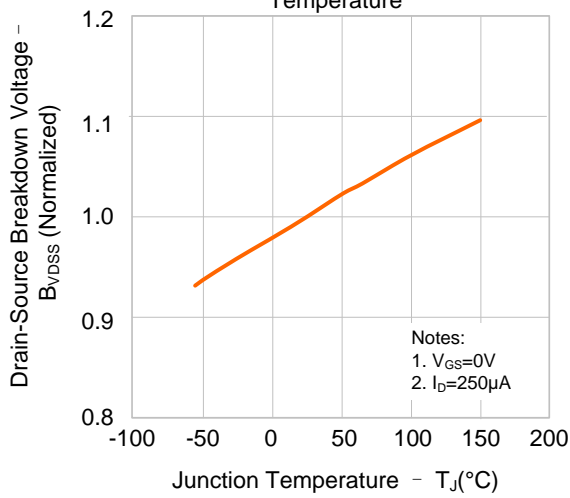


Figure 8. On-Resistance vs. Temperature

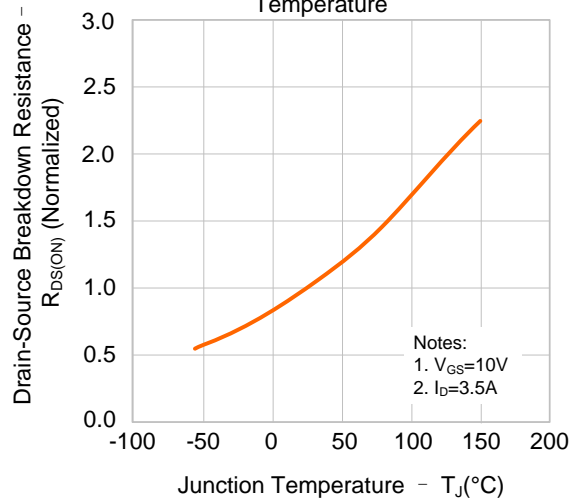
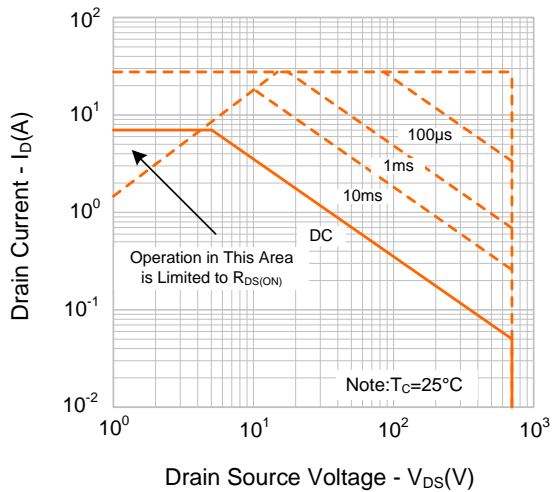
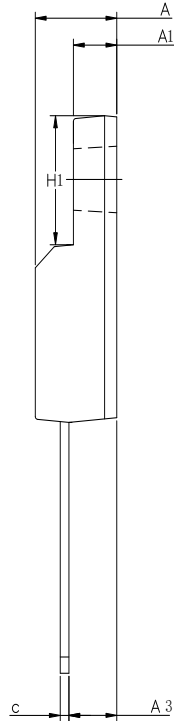
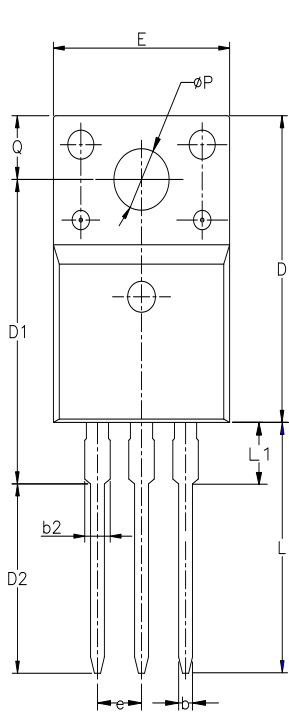


Figure 9. Max. Safe Operating Area



TO-220F-3L PACKAGE OUTLINE



SYMBOL	MIN	NOM	MAX
A	4.42	4.70	5.02
A1	2.30	2.54	2.80
A3	2.50	2.76	3.10
b	0.70	0.80	0.90
b2	—	—	1.47
c	0.35	0.50	0.65
D	15.25	15.87	16.25
D1	15.30	15.75	16.30
D2	9.30	9.80	10.30
E	9.73	10.16	10.36
e	2.54BCS		
H1	6.40	6.68	7.00
L	12.48	12.98	13.48
L1	/	/	3.50
$\phi P$	3.00	3.18	3.40
Q	3.05	3.30	3.55

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