## **Dual N-Channel 18 V (D-S) Power MOSFET**

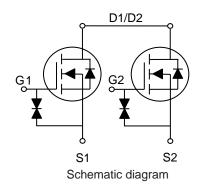
#### **PRODUCT SUMMARY** Q<sub>g</sub> (Typ.) V<sub>DS</sub> (V) $R_{DS(on)}(m\Omega)(Typ.)$ ID (A)a $6.2 \text{ at V}_{GS} = 4.5 \text{ V}$ 18 12 16 nC $7.5 \text{ at V}_{GS} = 2.5 \text{ V}$

#### **FEATURES**

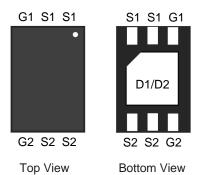
- DT- Trench Power MOSFET
- $\bullet$  100 % R<sub>q</sub> and UIS Tested
- . ESD Rating: HBM 2KV
- · Low Gate Charge

## **APPLICATIONS**

- · Handheld Instruments
- · POL Applications



### **DFN2X3-6L Pin Configuration**



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V <sub>DS</sub>	18	V
Gate-Source Voltage		$V_{GS}$	± 12	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	L	12	А
	T <sub>A</sub> = 100 °C	- I <sub>D</sub>	8.8	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	48	
Single Avalanche Energy		E <sub>AS</sub>	35	mJ
Maximum Power Dissipation <sup>c</sup>	T <sub>A</sub> = 25 °C	В	2.5	W
	T <sub>A</sub> = 100 °C	P <sub>D</sub>	1	VV
Operating Junction and Storage Temperature R	ange	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150	°C

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	LIMIT	UNIT	
Junction-to-Ambient (Drain)	R <sub>thJA</sub>	50	°C/W	

#### **Notes**

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-case thermal resistance.
- d. The value of R<sub>6JA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.

<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	18	-	-	V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.4	-	1		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$	-	-	± 100	nA	
Zana Cata Valtana Busin Comus d		V <sub>DS</sub> = 18 V, V <sub>GS</sub> = 0 V	-	-	1	μA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 12 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	-	-	100		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	12	-	-	Α	
Drain-Source On-State Resistance <sup>a</sup>	Pro/	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 5 A	-	6.2	7.2	0	
Drain-Gouice On-Glate Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 5 A	-	7.5	10.5	mΩ	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 5 \text{ V}, I_{D} = 5 \text{ A}$	-	18	-	S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>		-	1450	-	pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 9 \text{ V}, f = 1 \text{ MHz}$	-	195	-		
Reverse Transfer Capacitance	C <sub>rss</sub>		-	165	-		
Total Gate Charge <sup>c</sup>	$Q_g$		-	16	-	nC	
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 9 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5 \text{ A}$	-	3	-		
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	4.8	-		
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	4.5	-		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 9 \text{ V}, I_{D} = 5 \text{ A}, R_{g} = 3 \Omega$	-	6.8	-	ns	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	V <sub>GS</sub> = 4.5 V	-	45	-		
Fall Time <sup>c</sup>	t <sub>f</sub>		-	55	-		
Drain-Source Body Diode Ratings and	Characterist	ics <sup>b</sup> (T <sub>A</sub> = 25 °C)					
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>A</sub> = 25 °C	-	-	12	А	
Pulsed Current	I <sub>SM</sub>		-	-	48	Α	
Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>F</sub> = 1 A, V <sub>GS</sub> = 0 V	-	-	1.2	V	

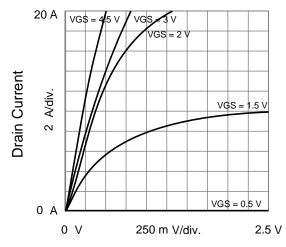
#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

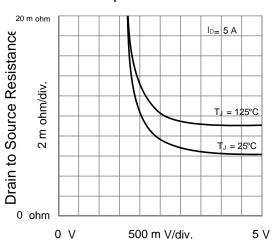
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those in dicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



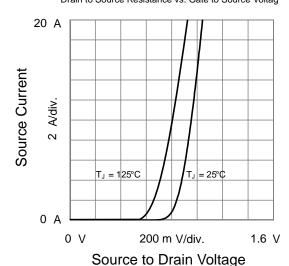
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



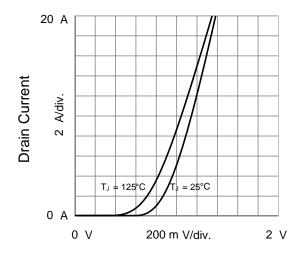
Drain to Source Voltage **Output Characteristics** 



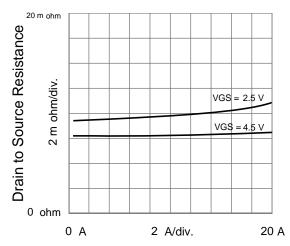
Gate to Source Voltage Drain to Source Resistance vs. Gate to Source Voltag



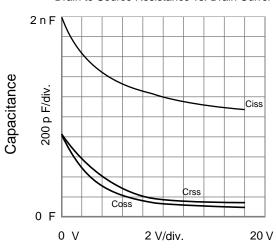
**Body Diode Forward Characteristics** 



Gate to Source Voltage **Transfer Characteristics** 



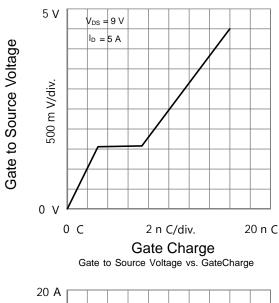
**Drain Current** Drain to Source Resistance vs. Drain Currer

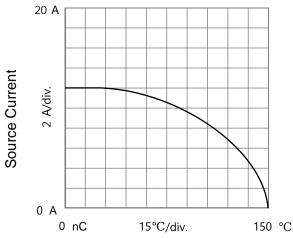


Drain to Source Voltage Capacitances



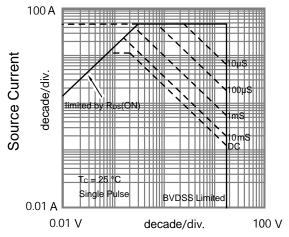
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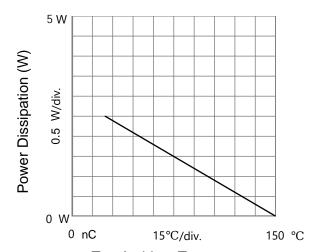


T<sub>A</sub> - Ambient Temperature





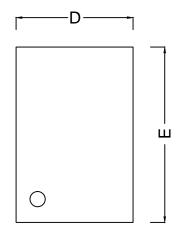
Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



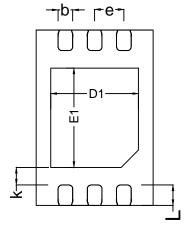
T<sub>A</sub> - Ambient Temperature



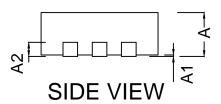
# DFN2X3-6L-D PACKAGE OUTLINE



**TOP VIEW** 



**BOTTOM VIEW** 



SYMBOL	mm				
STIVIDOL	MIN	TYP	MAX		
Α	0.70	0.75	0.80		
A1	0.00	0.02	0.05		
A2	0.203 REF				
b	0.20	0.25	0.30		
D	1.90	2.00	2.10		
Е	2.90	3.00	3.10		
е	0.500 BSC				
D1	1.45	1.50	1.55		
E1	1.65	1.70	1.75		
k	0.25	0.30	0.35		
Ĺ	0.30	0.35	0.40		





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