

P-Channel 15 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (m Ω)(Typ.)	I_D (A) ^a	Q_g (Typ.)
- 15	12 at $V_{GS} = - 4.5$ V	- 12.5	30 nC
	15 at $V_{GS} = - 2.5$ V		
	22 at $V_{GS} = - 1.8$ V		

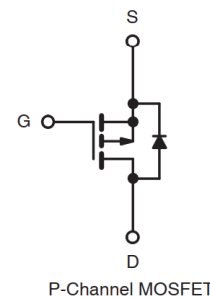
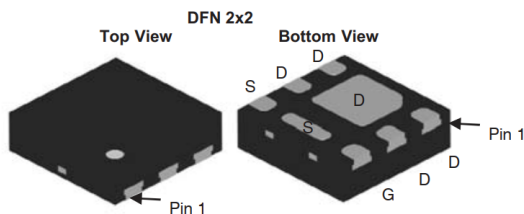
FEATURES

- DT-Trench Power MOSFET
- Ultra Small DFN2x2 Chip Scale Packaging Reduces Footprint Area, Profile (0.62 mm) and On-Resistance Per Footprint Area
- Compliant to RoHS Directive 2002/95/EC


RoHS
 COMPLIANT

APPLICATIONS

- PA Switch
- Battery Switch
- Load Switch



ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	- 15	V
Gate-Source Voltage	V_{GS}	± 12	
Continuous Drain Current ($T_J = 150$ °C) ^a	I_D	$T_C = 25$ °C - 12.5	A
		$T_C = 100$ °C - 8.2	
Pulsed Drain Current ^b	I_{DM}	- 36	
Maximum Power Dissipation ^c	P_D	$T_C = 25$ °C 6.57	W
		$T_C = 100$ °C 2.63	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to +150	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^d	R_{thJA}	48	°C/W
Junction-to-Case (Drain)	R_{thJC}	19	

Notes

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- P_D is based on max. junction temperature, using junction-case thermal resistance.
- The value of R_{thJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_a = 25$ °C.

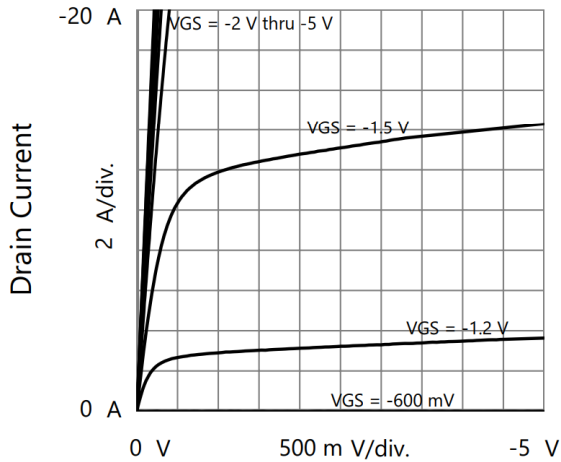
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 15	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 0.35	-	- 0.9	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 10 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 12 V, V _{GS} = 0 V	-	-	- 1	μA
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1 A	-	12	19	mΩ
		V _{GS} = - 2.5 V, I _D = - 1 A	-	15	24	
		V _{GS} = - 1.8 V, I _D = - 1 A	-	22	37	
Forward Transconductance ^a	g _{fs}	V _{DS} = - 4 V, I _D = - 1 A	-	5.1	-	S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = - 6 V, f = 1 MHz	-	1860	-	pF
Output Capacitance	C _{oss}		-	368	-	
Reverse Transfer Capacitance	C _{rss}		-	338	-	
Total Gate Charge ^c	Q _g	V _{DS} = - 6 V, V _{GS} = -4.5 V, I _D = - 1 A	-	30	-	nC
Gate-Source Charge ^c	Q _{gs}		-	7.3	-	
Gate-Drain Charge ^c	Q _{gd}		-	5.9	-	
Gate Resistance	R _g	f = 1 MHz	-	14	-	Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DS} = - 6 V, I _D = - 1 A, V _{GS} = - 4.5 V, R _g = 6 Ω	-	14	-	ns
Rise Time ^c	t _r		-	25	-	
Turn-Off Delay Time ^c	t _{d(off)}		-	380	-	
Fall Time ^c	t _f		-	240	-	
Drain-Source Body Diode Ratings and Characteristics ^b (T _C = 25 °C)						
Continuous Source Current	I _S	T _C = 25 °C	-	-	- 12.5	A
Pulsed Current	I _{SM}		-	-	- 36	A
Forward Voltage ^a	V _{SD}	I _S = - 1 A, V _{GS} = 0 V	-	-	- 1.2	V
Reverse Recovery Time	t _{rr}	I _S = - 1 A, di/dt = 100 A/μs	-	311	-	ns
Reverse Recovery Charge	Q _{rr}		-	1.13	-	nC

Notes

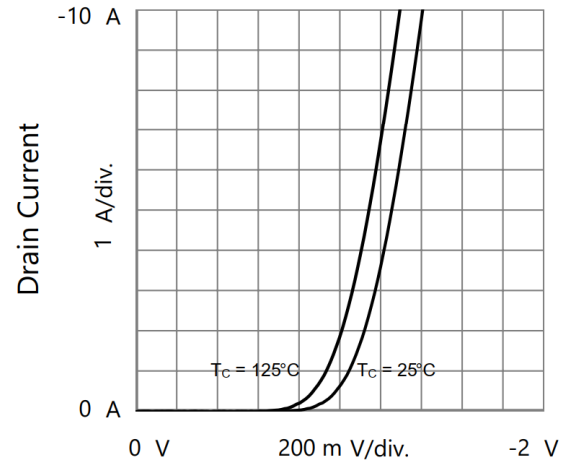
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

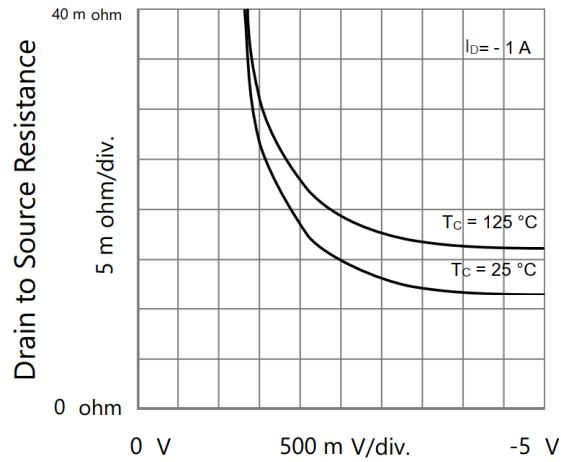
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



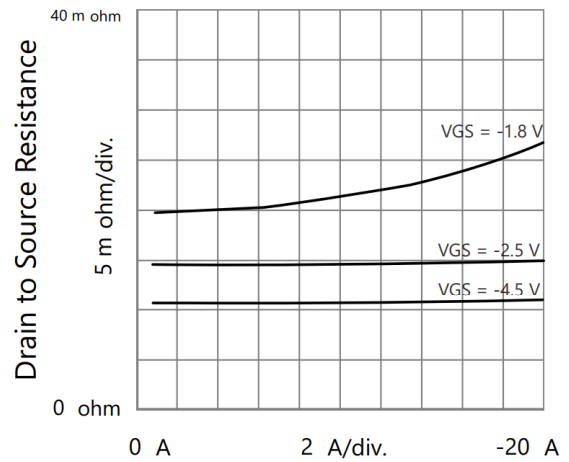
Drain to Source Voltage
Output Characteristics



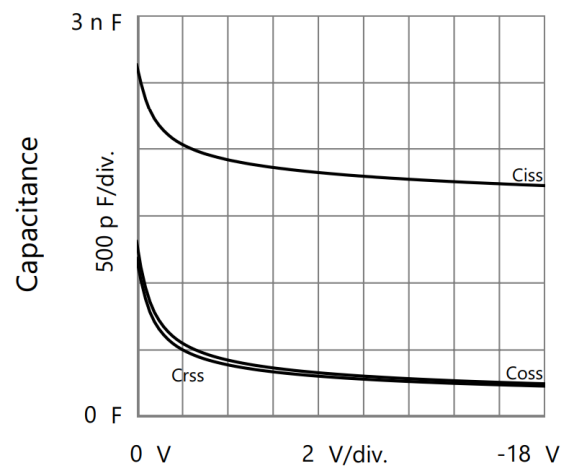
Gate to Source Voltage
Transfer Characteristics



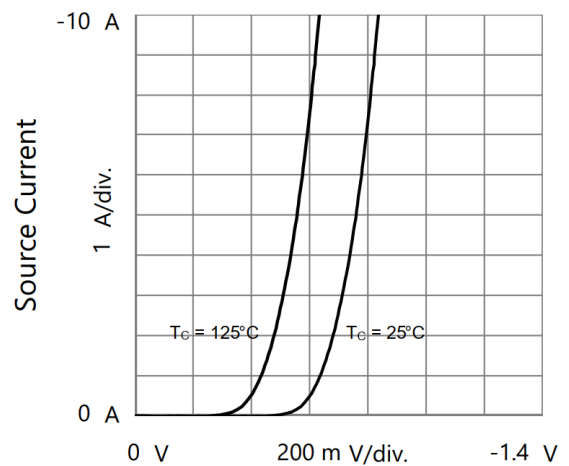
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



Drain Current
Drain to Source Resistance vs. Drain Current

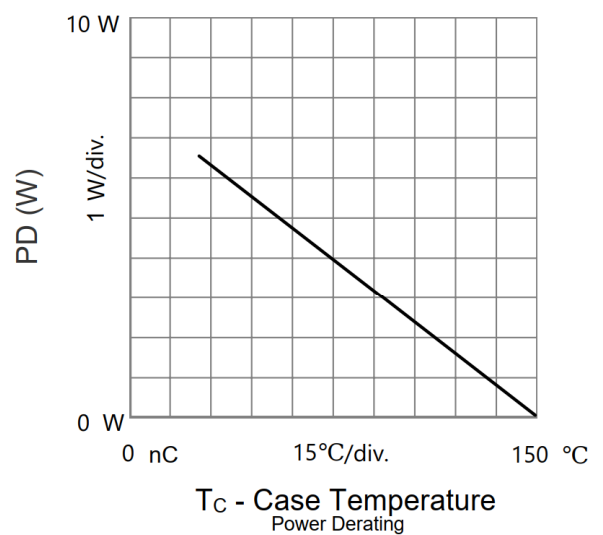
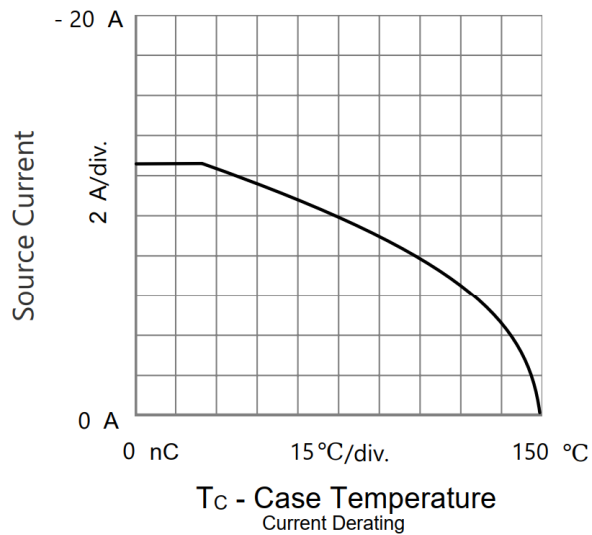
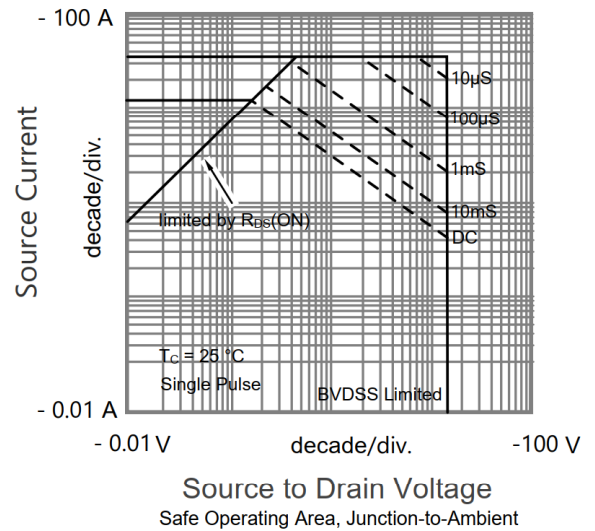
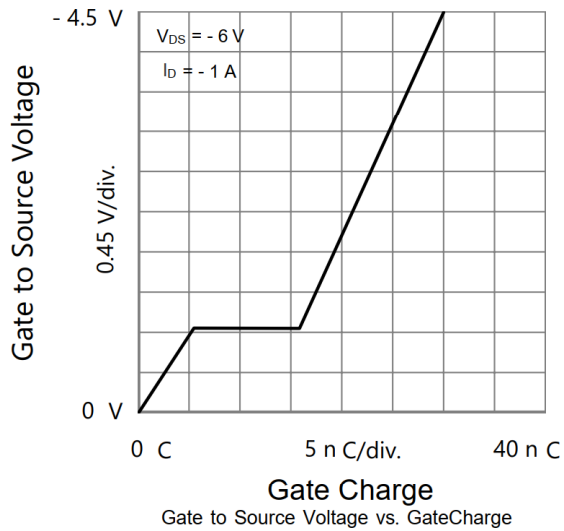


Drain to Source Voltage
Capacitances

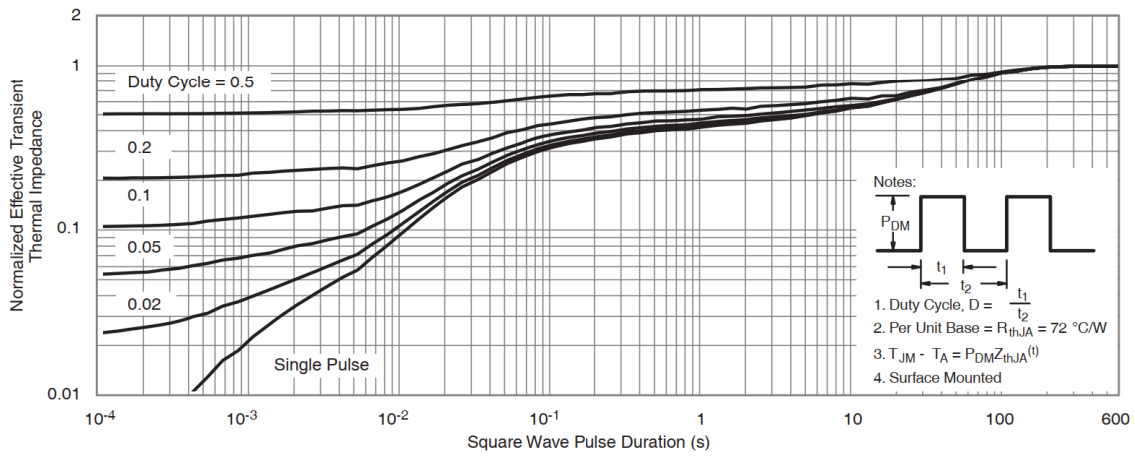


Source to Drain Voltage
Body Diode Forward Characteristics

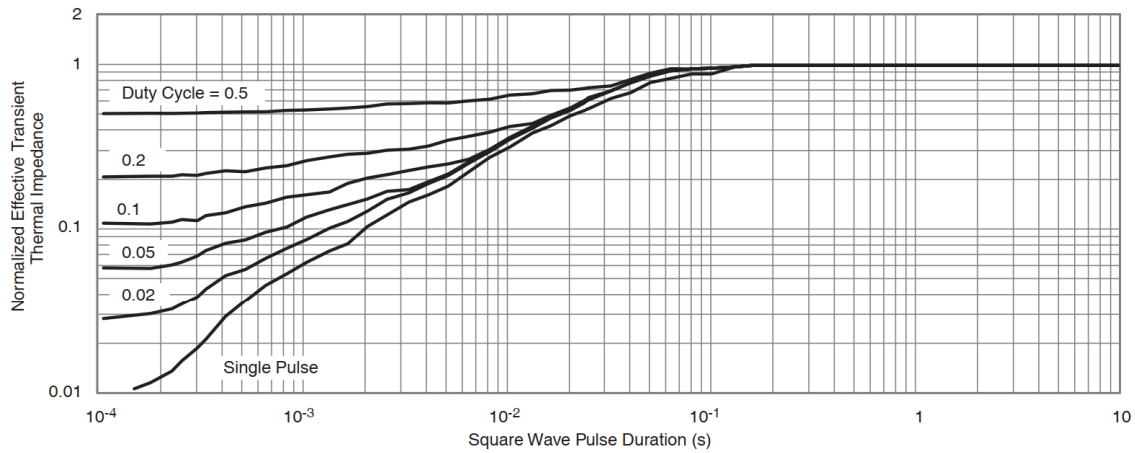
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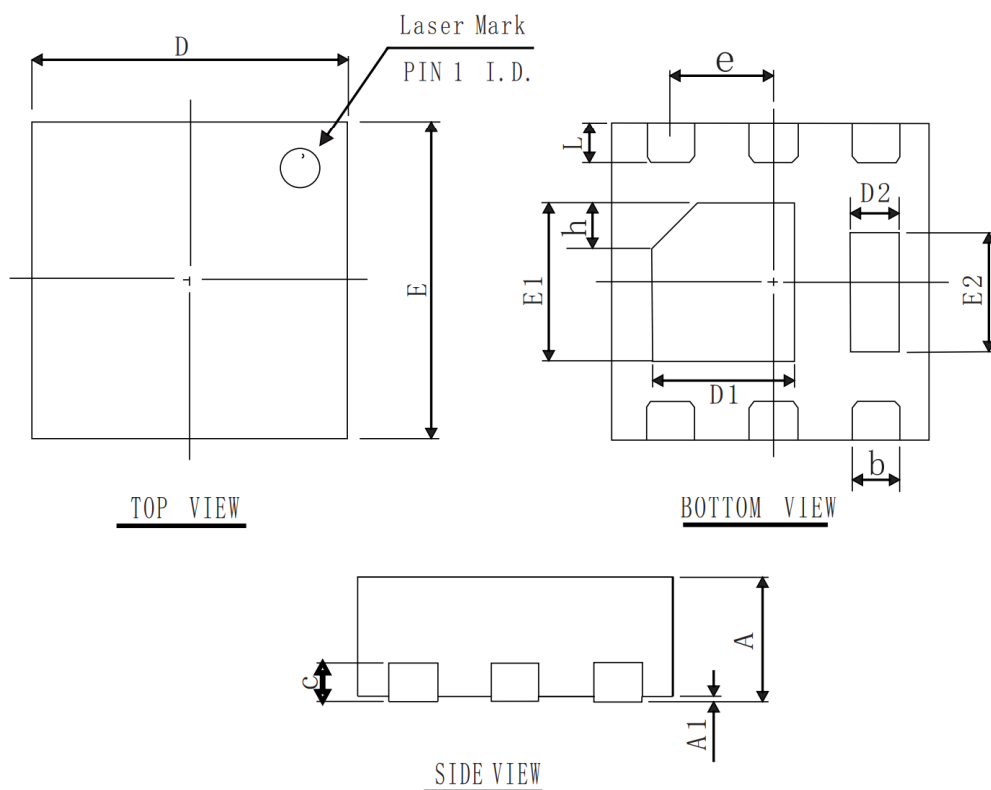


Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

DFN 2X2 PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	0.60	0.75	0.90
A1	0.00	0.02	0.10
b	0.15	0.25	0.40
D	1.80	2.00	2.25
E	1.80	2.00	2.25
D1	0.70	0.90	1.10
E1	0.75	1.00	1.20
D2	0.15	0.30	0.45
E2	0.45	0.75	0.95
L	0.15	0.25	0.40
h	0.15	0.25	0.40
c	0.203 REF		
e	0.65 BSC		

Other thickness dimensions are as follows

A	0.50	0.55	0.60
A	0.40	0.45	0.50

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