

Dual N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)
20	8.5 at V _{GS} = 4.5 V	9.5	10.7 nC
	11.5 at V _{GS} = 2.5 V		

FEATURES

- DT-Trench Power MOSFET
- 100 % Rg and UIS Tested



RoHS
COMPLIANT

APPLICATIONS

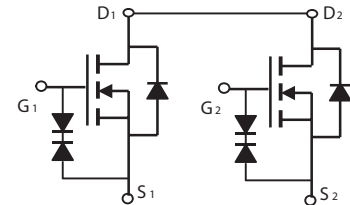
- PWM application
- Load Switch



Pin1 DFN2x3-6 top view



top view



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	20	V	
Gate-Source Voltage	V _{GS}	± 12		
Continuous Drain Current (T _J = 150 °C)	I _D	T _A = 25 °C	A	
		T _A = 70 °C		
Pulsed Drain Current	I _{DM}	60		
Maximum Power Dissipation	P _D	T _A = 25 °C	1.56 ^{b,c}	
		T _A = 70 °C	1.0 ^{b,c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b,d}	R _{thJA}	75	80.1	°C/W	

Notes:

- T_A = 25 °C.
- Surface Mounted on 1" x 1" FR4 board.
- t = 5 s.
- Maximum under steady state conditions is 120°C/W.

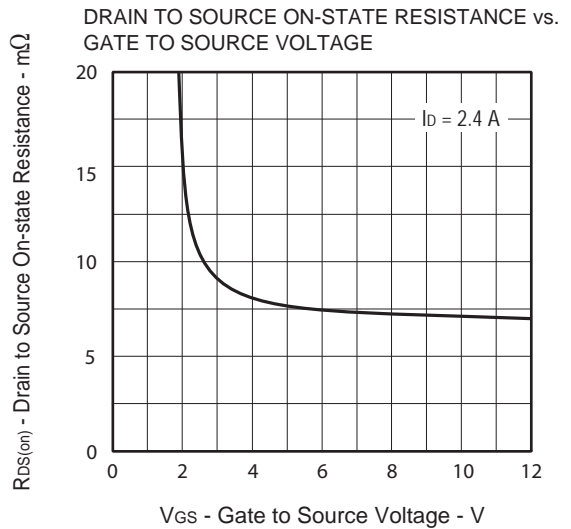
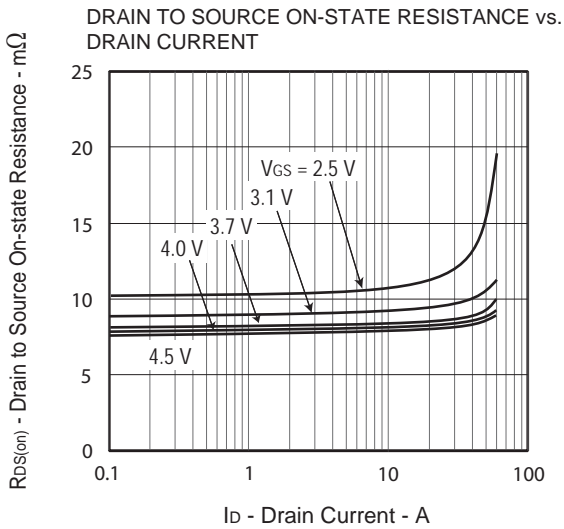
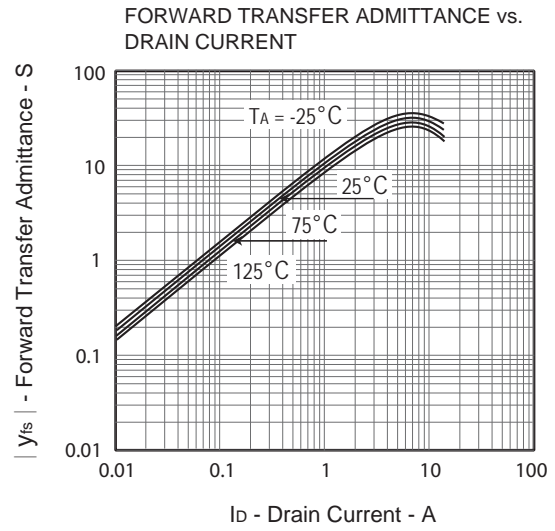
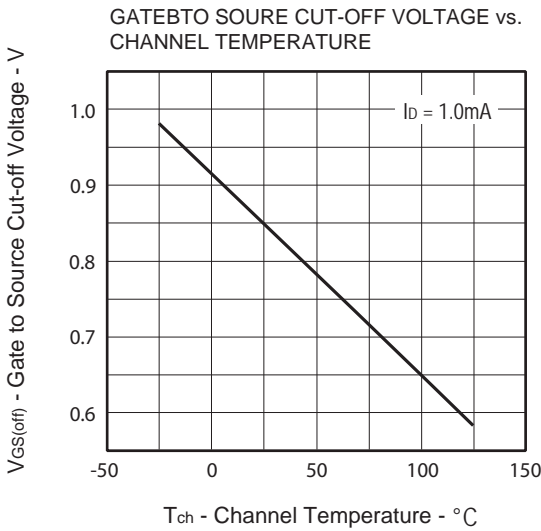
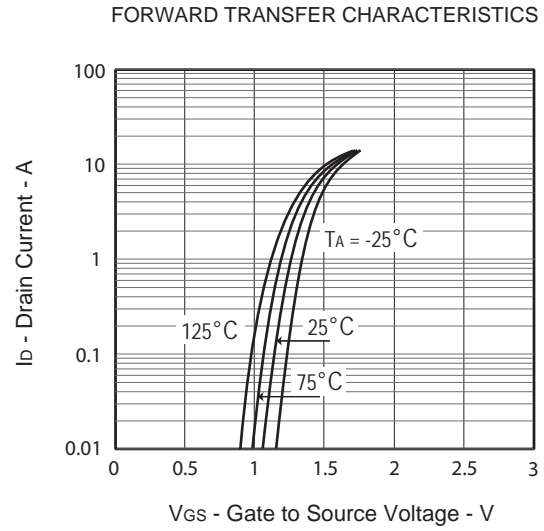
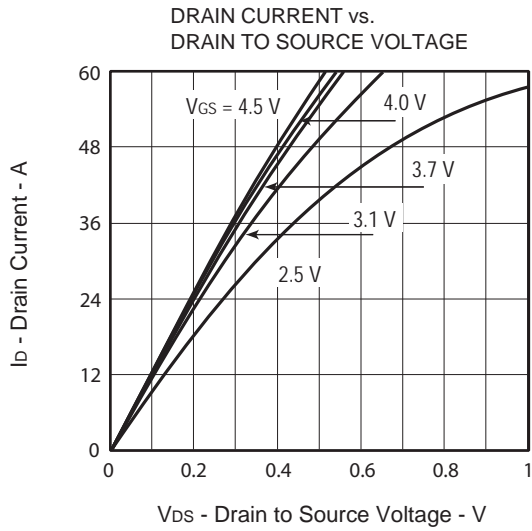
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0, I_D = 250\text{ }\mu\text{A}$	20			V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	0.5		1.5	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$			± 1	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 18\text{ V}, V_{GS} = 0\text{ V}$			1	μA
		$V_{DS} = 16\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 4.5\text{ V}$	9.5			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 4.5\text{ V}, I_D = 2.4\text{ A}$		8.5	9.7	m Ω
		$V_{GS} = 2.5\text{ V}, I_D = 2.4\text{ A}$		11.5	12.7	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 5\text{ V}, I_D = 4.75\text{ A}$		28		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		980		pF
Output Capacitance	C_{oss}			213		
Reverse Transfer Capacitance	C_{rss}			189		
Total Gate Charge	Q_g	$V_{DS} = 16\text{ V}, V_{GS} = 4.5\text{ V}, I_D = 9.5\text{ A}$		10.7		nC
Gate-Source Charge	Q_{gs}			2.1		
Gate-Drain Charge	Q_{gd}			5.4		
Gate Resistance	R_g	$f = 1\text{ MHz}$		5		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 16\text{ V}, R_L = 6\text{ }\Omega$ $I_D \cong 4.75\text{ A}, V_{GEN} = 4.5\text{ V}$		24		ns
Rise Time	t_r			66		
Turn-Off Delay Time	$t_{d(off)}$			116		
Fall Time	t_f			46		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			9.5	A
Pulse Diode Forward Current	I_{SM}				60	
Body Diode Voltage	V_{SD}	$I_S = 1\text{ A}$			1.2	V

Notes:

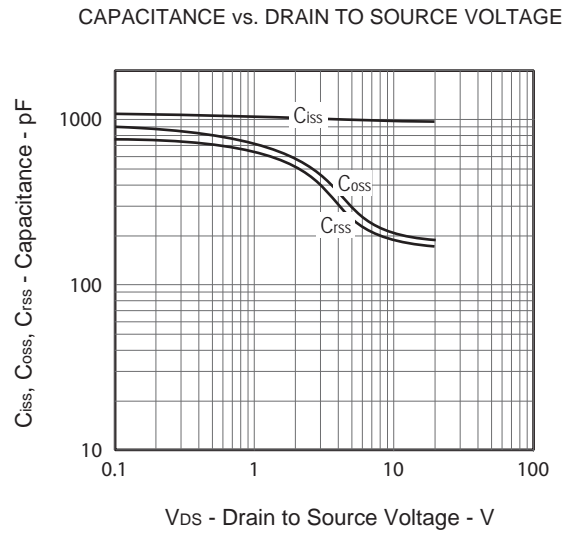
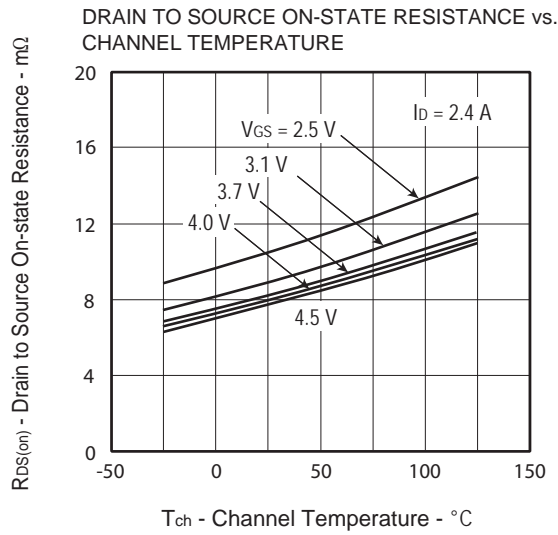
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

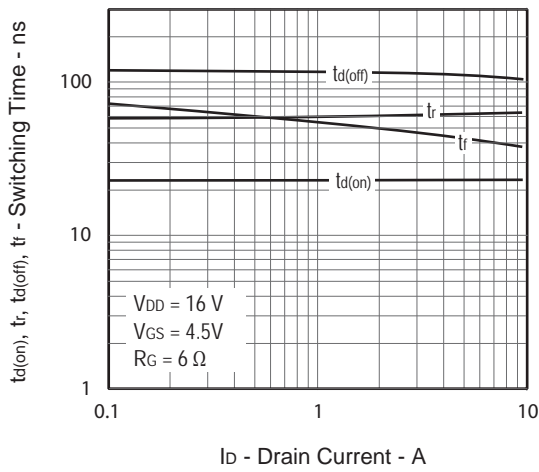
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



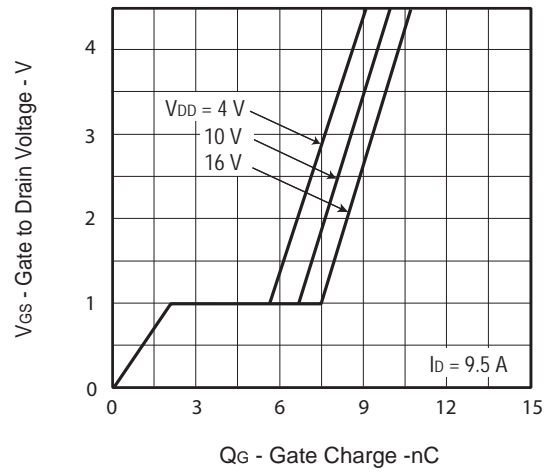
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



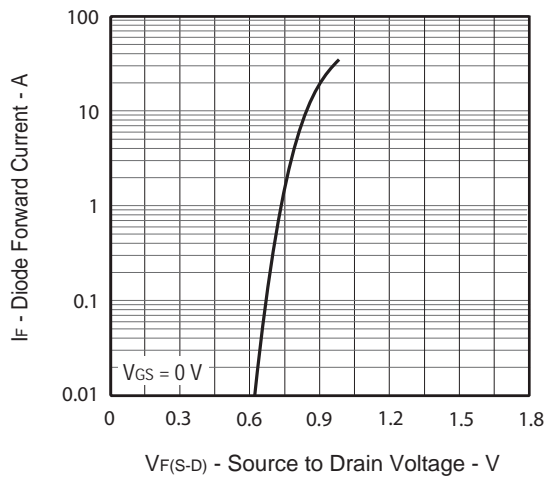
SWITCHING CHARACTERISTICS



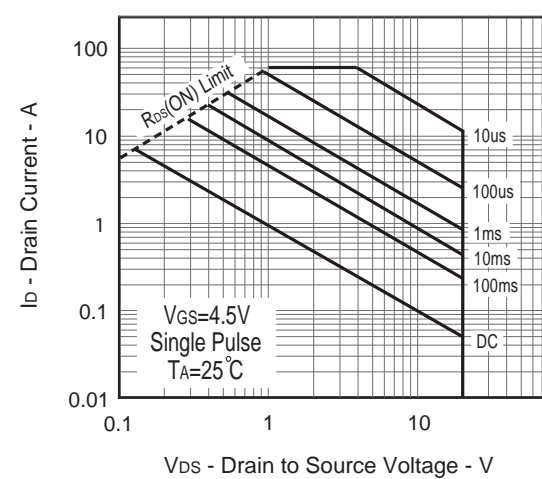
DYNAMIC INPUT CHARACTERISTICS



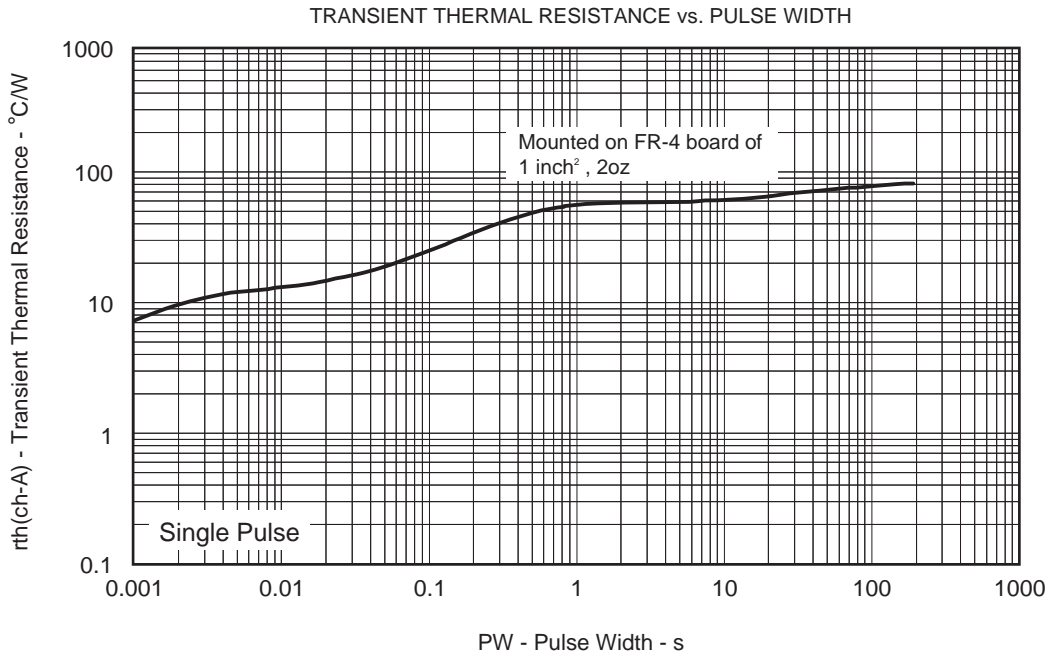
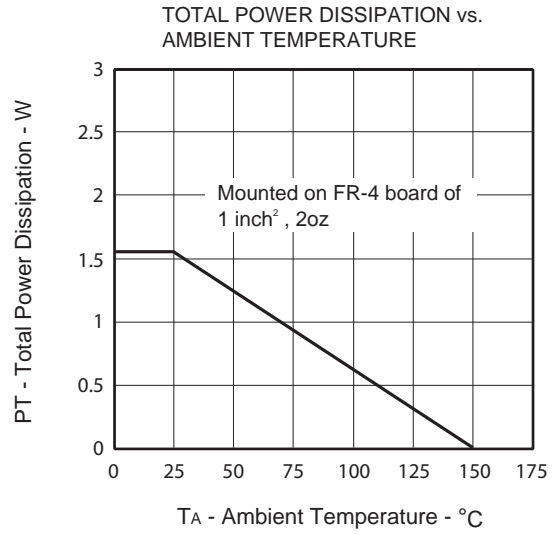
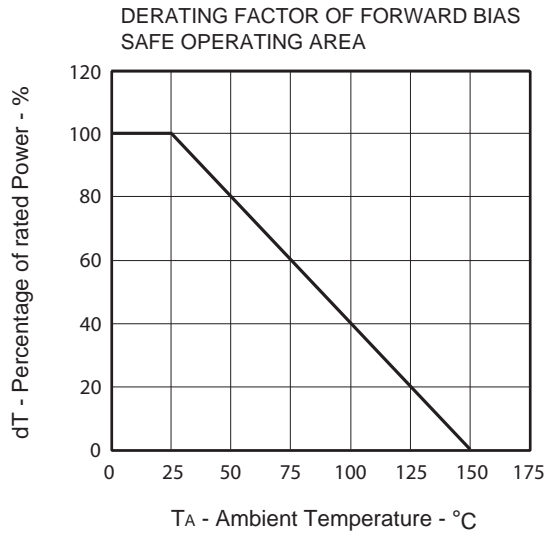
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



FORWARD BIAS SAFE OPERATING AREA



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



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