Dual N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$R_{DS(on)}$ (mΩ)	I_D (A)	Q_g (Typ.)
30	23 at V_{GS} = 10 V	4.5	3.6 nC
	36 at V_{GS} = 4.5 V		

Pb-free
RoHS COMPLIANT

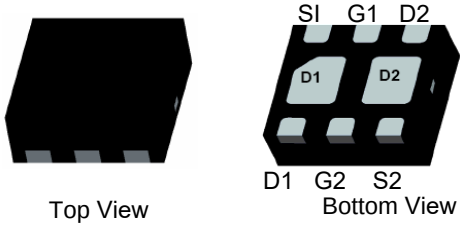
FEATURES

- TrenchFET II Power MOSFET
- PWM Optimized
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

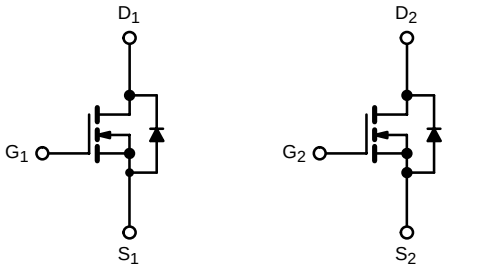
- Portable devices such as smart phones, tablet PCs and mobile computing
  - Load switch
  - DC/DC converter
  - Power management

DFN 2X2-6



Top View

Bottom View



N-Channel MOSFET

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	4.5[a]	A
	T_C = 70 °C		3.3	
	T_A = 25 °C		1.9[b, c]	
	T_A = 70 °C		0.93[b, c]	
Pulsed Drain Current		I_{DM}	20	
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	4.5	
	T_A = 25 °C		1.7[b, c]	
Maximum Power Dissipation	T_C = 25 °C	P_D	7.5	W
	T_C = 70 °C		4.8	
	T_A = 25 °C		1.78[b, c]	
	T_A = 70 °C		1.14[b, c]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient[b, d]	t ≤ 5 s	R_{thJA}	50	65	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	12	16	

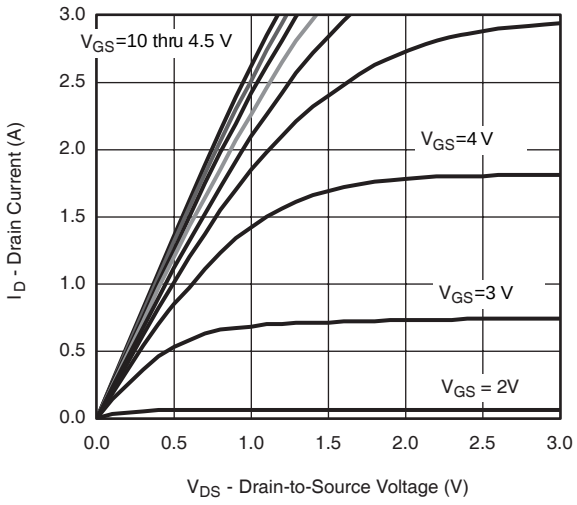
Notes:
a. Package limited.
b. Surface mounted on 1" x 1" FR4 board.
c. t = 5 s.
d. Maximum under steady state conditions is 220 °C/W.

SPECIFICATIONS T_J = 25 °C, unless otherwise noted						
Parameter	**Symbol**	**Test Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	30			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA		15		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			4.6		
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS} , I_D =250 µA	1		3	V
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = 20 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} =30 V, V_{GS} = 0 V			1	µA
		V_{DS} = 30 V, V_{GS} = 0 V, T_J = 85 °C			10	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} ≥ 5 V, V_{GS} = 4.5 V	3			A
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 5 A		23	35	mΩ
		V_{GS} = 4.5 V, I_D = 3 A		36	58	
Forward Transconductance[a]	g_{fs}	V_{DS} = 15 V, I_D = 3 A		20		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		750		pF
Output Capacitance	C_{oss}			116		
Reverse Transfer Capacitance	C_{rss}			20		
Total Gate Charge	Q_g	V_{DS} = 15 V, V_{GS} = 10 V, I_D = 5 A		6.6	12	nC
		V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 3 A		3.6	8	
Gate-Source Charge	Q_{gs}			0.35		
Gate-Drain Charge	Q_{gd}			0.29		
Gate Resistance	R_g	f = 1 MHz		8.5		Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 15 V, R_L = 12 Ω I_D ≅ 5 A, V_{GEN} = 10 V, R_g = 1 Ω		12		ns
Rise Time	t_r			35		
Turn-Off Delay Time	$t_{d(off)}$			16		
Fall Time	t_f			10		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C			4.5	A
Pulse Diode Forward Current[a]	I_{SM}				20	
Body Diode Voltage	V_{SD}	I_S = 5 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 5 A, dI/dt = 100 A/µs, T_J = 25 °C		25	50	ns
Body Diode Reverse Recovery Charge	Q_{rr}			15	30	nC
Reverse Recovery Fall Time	t_a			12		ns
Reverse Recovery Rise Time	t_b			13		

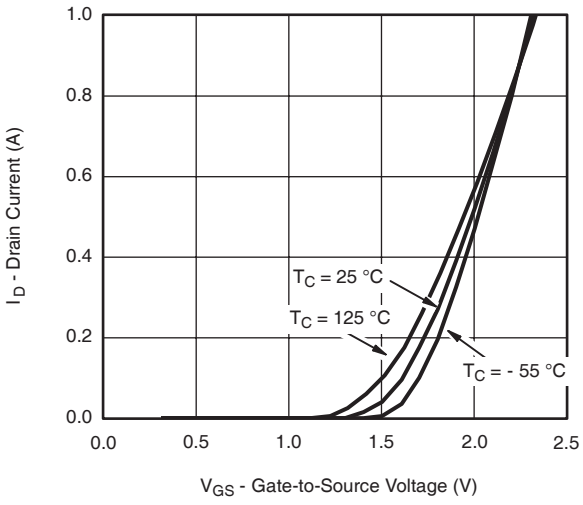
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

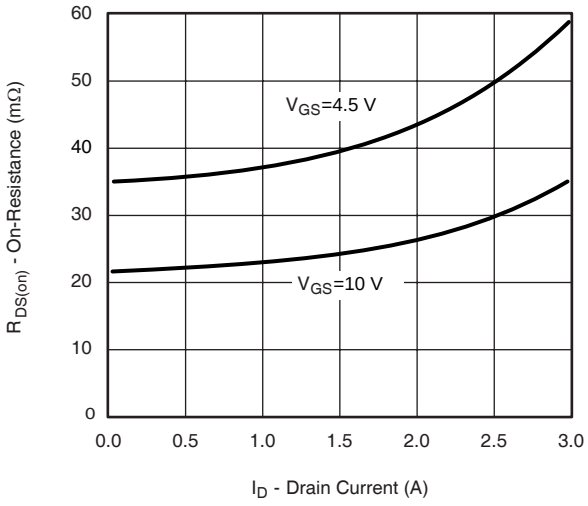
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



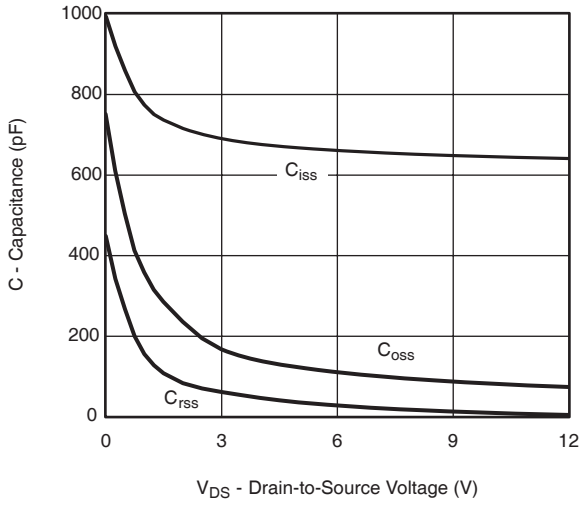
Output Characteristics



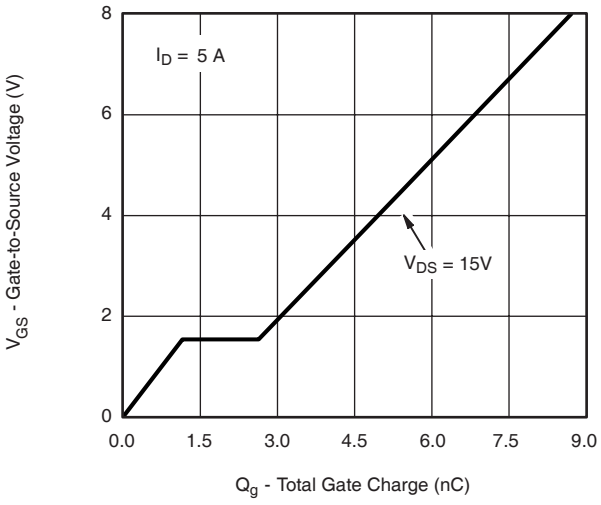
Transfer Characteristics



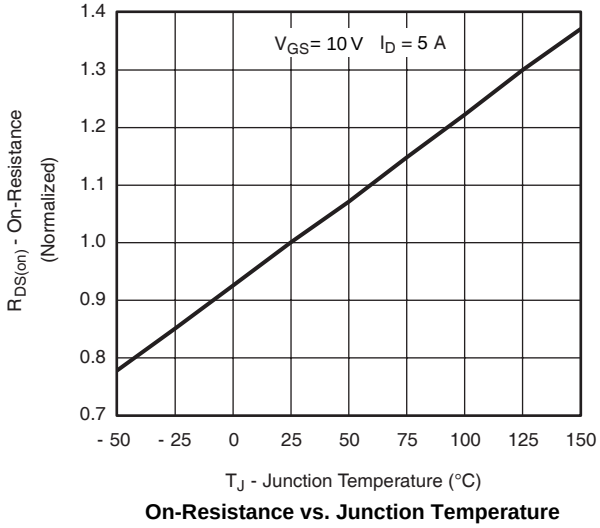
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

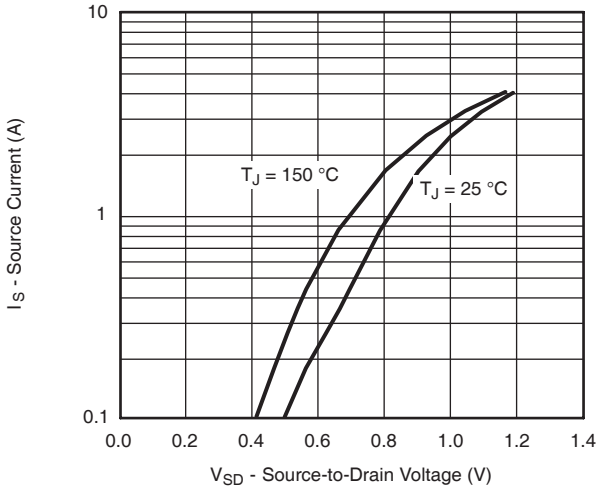


Gate Charge

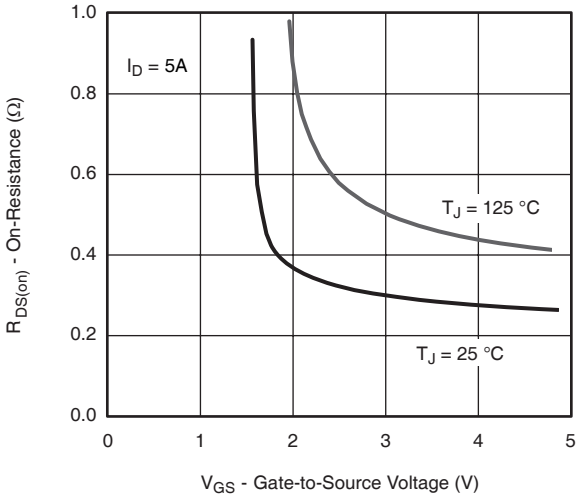


On-Resistance vs. Junction Temperature

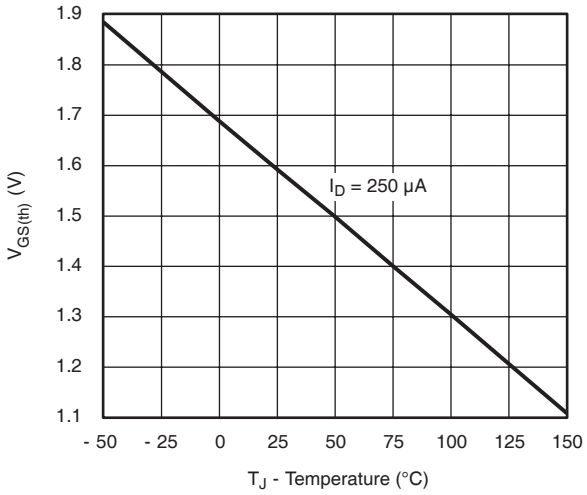
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



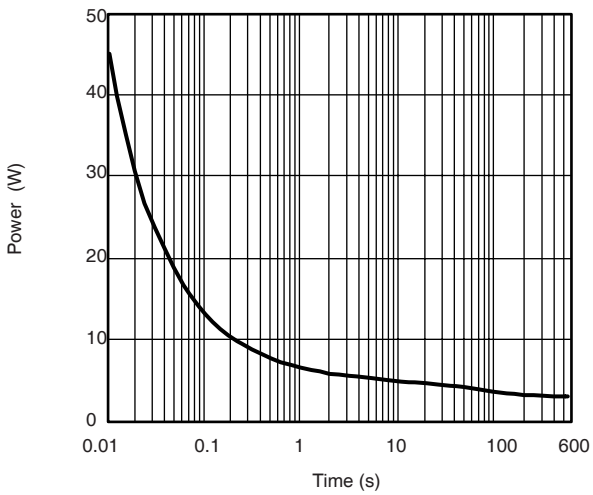
Source-Drain Diode Forward Voltage



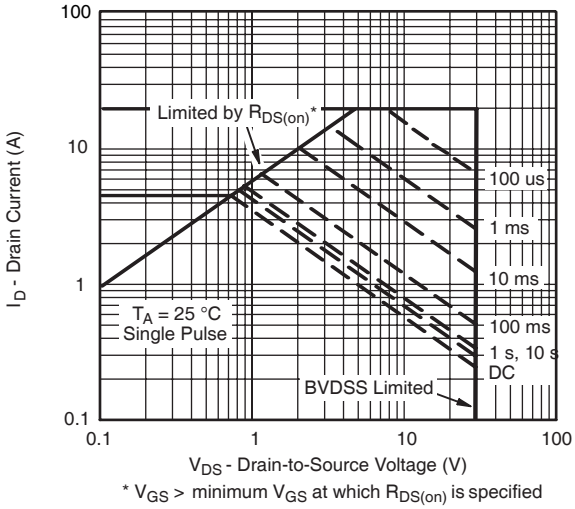
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



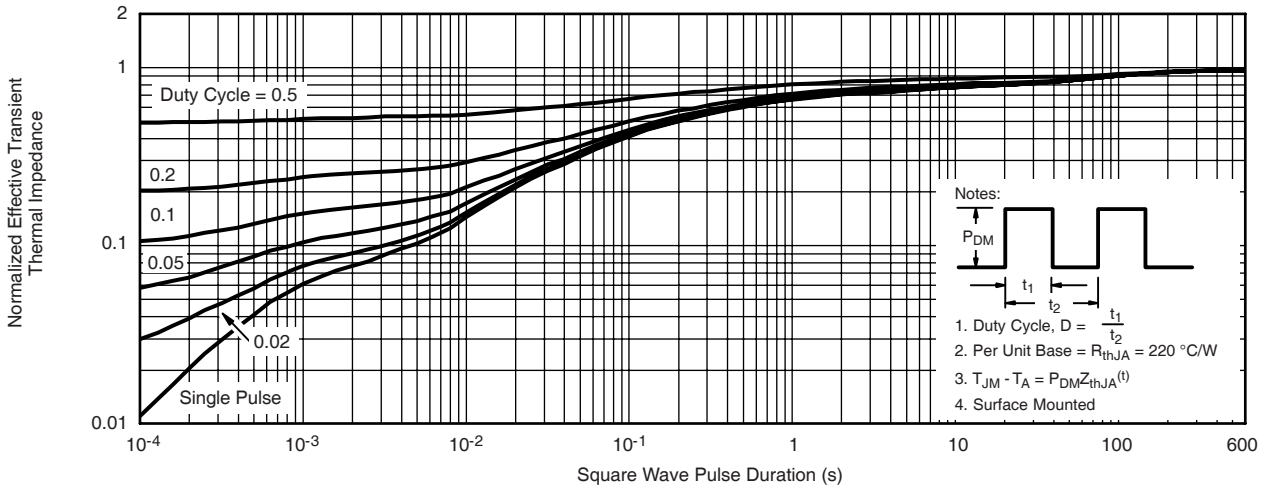
Single Pulse Power



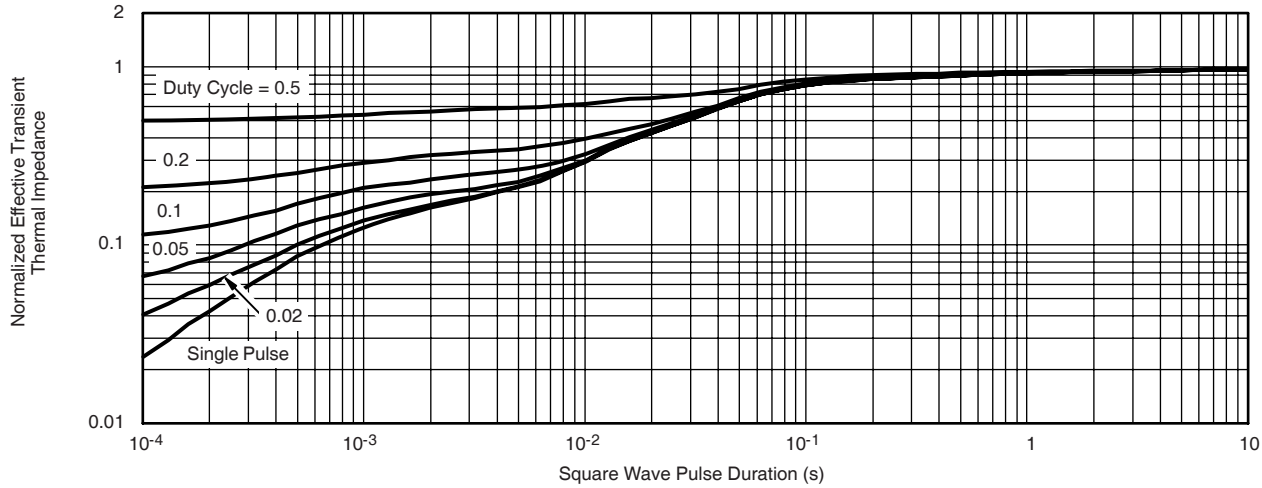
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

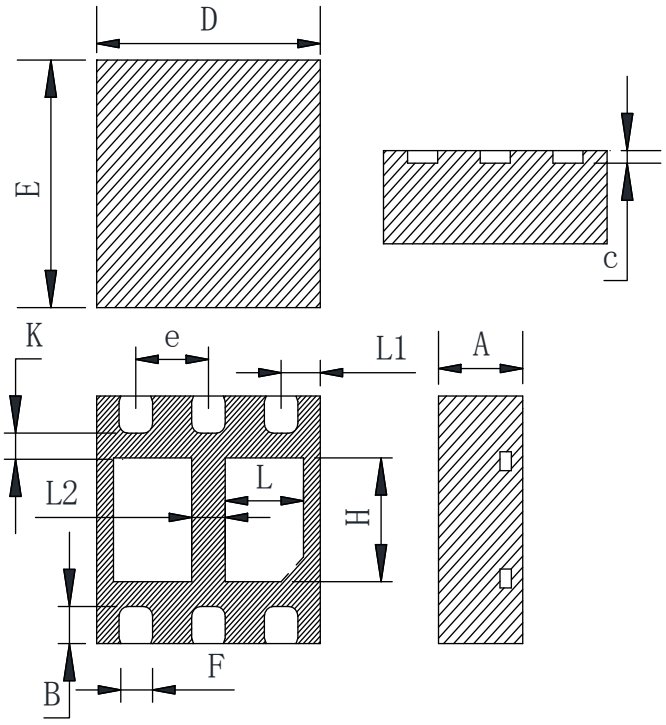


Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

DFN 2X2-D PACKAGE OUTLINE



Symbol	Min	Typ	Max
A	0.70	0.75	0.80
B	0.20	0.30	0.40
C	0.153	0.203	0.253
D	1.90	2.00	2.10
E	1.90	2.00	2.10
e	0.55	0.65	0.70
F	0.20	0.30	0.40
H	0.85	1.00	1.10
L	0.55	0.70	0.80
L1	0.25	0.35	0.45
L2	0.20	0.30	0.40
K	0.15	0.20	0.30

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