

Dual N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY							
V _{DS} (V)	$R_{DS(on)}(m\Omega)(Typ.)$	I _D (A) ^a	Q _g (Typ.)				
20	32 at $V_{GS} = 4.5 \text{ V}$	6	4.75 nC				
20	45 at V _{GS} = 2.5 V	0					

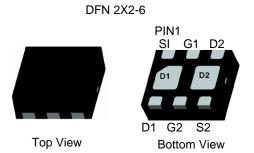
FEATURES

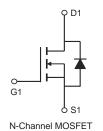
- DT-Trench Power MOSFET
- 100 % Rg and UIS tested
- · Low Gate Charge

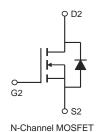
APPLICATIONS

- · Load Switch
- DC/DC Converters









ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V _{DS}	20	V			
Gate-Source Voltage	V _{GS}	± 12	V			
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	ı	6	A		
Continuous Diam Curient (1) = 130°C)	T _A = 100 °C	- I _D	4.2			
Pulsed Drain Current ^b	I _{DM}	24				
Maximum Power Dissipation ^c	T _A = 25 °C	- P _D	2.4	W		
Maximum Fower Dissipation	T _A = 100 °C		0.96			
Operating Junction and Storage Temperature Rar	T _J , T _{stg}	-55 to +150	°C			

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	52	°C/W		

Notes

- a. Calculated continuous current based on maximum allowable junction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-ambient thermal resistance.
- d. The value of R_{0JA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static				<u>. </u>	•		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20	-	-		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.4	-	1.2	V	
Gate-Body Leakage	ody Leakage I_{GSS} $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 12 V, V _{GS} = 0 V	-	-	1	1 100 μA	
Zero Gate Voltage Drain Current		V _{DS} = 12 V, V _{GS} = 0 V, T _J = 55 °C	-	-	100		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le 5 \text{ V}, V_{GS} = 5 \text{ V}$	6	-	-	Α	
Drain Source On State Begintance	D	$V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$	-	32	40	mΩ	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 5 \text{ A}$	-	45	55		
Forward Transconductancea	9 _{fs}	$V_{DS} = 5 \text{ V}, I_{D} = 5 \text{ A}$	-	12	-	S	
Dynamic ^b				<u>. </u>			
Input Capacitance	C _{iss}		-	345	-	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 10 \text{ V}, f = 1 \text{ MHz}$	-	60	-		
Reverse Transfer Capacitance	C _{rss}		-	52	-		
Total Gate Charge ^c	Q_g		-	4.75	-	nC	
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$	-	0.33	-		
Gate-Drain Charge ^c	Q_{gd}		-	1.3	-		
Gate Resistance	Rg	f = 1 MHz	-	3.3	-	Ω	
Turn-On Delay Time ^c	t _{d(on)}		-	5	-		
Rise Time ^c	t _r	$V_{DD} = 10 \text{ V}, I_D = 5 \text{ A}, R_g = 4 \Omega$	-	3.5	-	ns	
Turn-Off Delay Time ^c	t _{d(off)}	V _{GS} = 4.5 V	-	16	-		
Fall Time ^c	t _f		-	4	-		
Drain-Source Body Diode Ratings and	Characterist	ics ^b (T _A = 25 °C)					
Continuous Source-Drain Diode Current	I _S	T _A = 25 °C	-	-	6	Α	
Pulsed Current	I _{SM}		-	-	24	Α	
Forward Voltage ^a	V_{SD}	I _F = 1 A, V _{GS} = 0 V	-	-	1.2	V	
Reverse Recovery Time	t _{rr}	L = 5 A di/dt = 100 A/va	-	6.5	-	ns	
Reverse Recovery Charge	Q _{rr}	$I_F = 5 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$	-	1	-	nC	

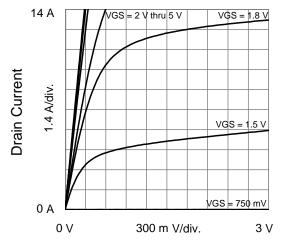
Notes

- a. Pulse test; pulse width $\leq 300 \,\mu\text{s}$, duty cycle $\leq 2 \,\%$.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

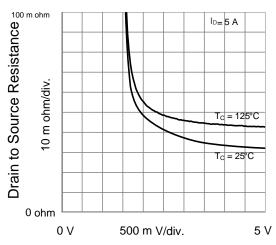
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those in dicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended pe riods may affect device reliability.



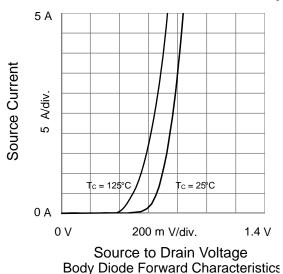
TYPICAL CHARAC TERISTICS (25 °C, unless otherwise noted)

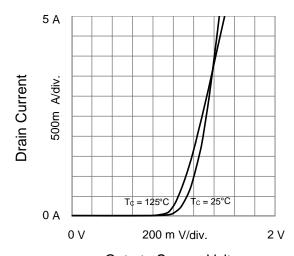


Drain to Source Voltage Output Characteristics

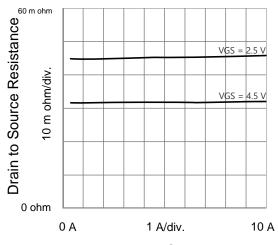


Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltag

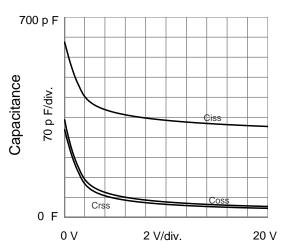




Gate to Source Voltage Transfer Characteristics



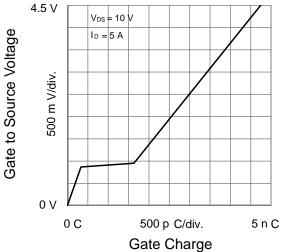
Drain Current

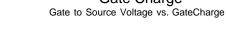


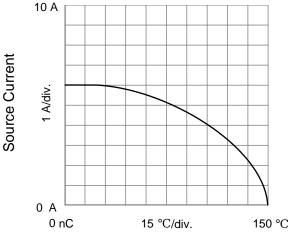
Drain to Source Voltage Capacitances



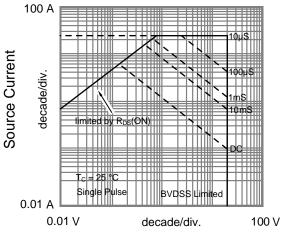
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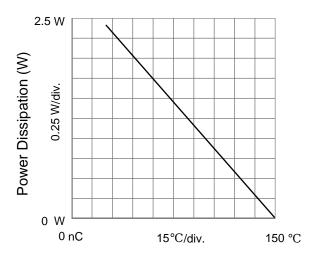




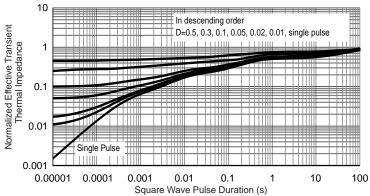
T_A - Case Temperature



Source to Drain Voltage
Safe Operating Area, Junction-to-Ambient



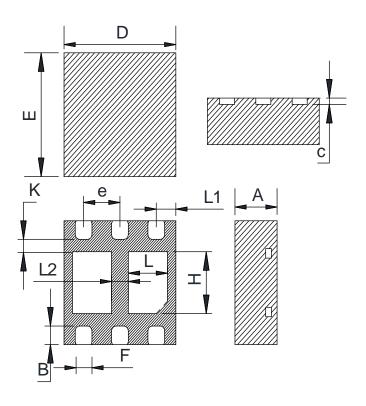
T_A - Case Temperature



Normalized Thermal Transient Impedance, Junction-to-Ambient



DFN 2X2-D PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Symbol	Min	Тур	Max
А	0.70	0.75	0.80
В	0.20	0.30	0.40
С	0.153	0.203	0.253
D	1.90	2.00	2.10
Е	1.90	2.00	2.10
е	0.55	0.65	0.70
F	0.20	0.30	0.40
Н	0.85	1.00	1.10
L	0.55	0.70	0.80
L1	0.25	0.35	0.45
L2	0.20	0.30	0.40
K	0.15	0.20	0.30

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