



Dual Asymmetric N-Channel 18 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(m\Omega)(Typ.)$	I _D (A) ^a	Q _g (Typ.)		
18	3 at V _{GS} = 4.5 V	58	36 nC		
10	3.7 at V _{GS} = 2.5 V	36	30 110		

FEATURES

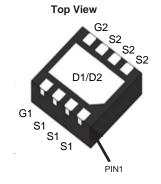
- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- ESD Rating: HBM 2KV
- · High Power and current handing capability

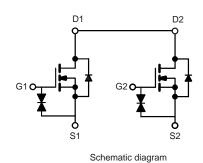


APPLICATIONS

· Load Switch

DFN3X3-8L Pin Configuration





ABSOLUTE MAXIMUM RATINGS ($T_C = 25 ^{\circ}C$, unless otherw	ise noted)			
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V _{DS}	18	V		
Gate-Source Voltage		V_{GS}			± 10
Continuous Drain Current (T _J = 150 °C) ^a	T _C = 25 °C		58	A	
Continuous Diam Current (1) = 150 °C)	T _C = 100 °C	l _D	36		
Pulsed Drain Current ^b	I _{DM}	232			
Single Avalanche Energy		E _{AS}	110	mJ	
Maximum Dawer Discinations	T _C = 25 °C	D	22	- W	
Maximum Power Dissipation ^c	T _C = 100 °C	P _D	8.7		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 150	°C	

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Case (Drain)	R _{thJC}	5.7	°C/W

Notes

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-case thermal resistance.



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	18	-	-	V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.5	-	1	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}$	-	-	± 10	μΑ	
Zero Gate Voltage Drain Current	1	V _{DS} = 18 V, V _{GS} = 0 V	-	-	1	1 100 μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 18 V, V _{GS} = 0 V, T _J = 55 °C	-	-	100		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	58	-	-	Α	
Drain-Source On-State Resistance ^a	В	V _{GS} = 4.5 V, I _D = 5 A	-	3	3.9	mΩ	
Dialii-Source Oil-State Resistance	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 4 \text{ A}$	-	3.7	4.9		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 5 \text{ V}, I_{D} = 5 \text{ A}$	-	13.5	-	S	
Dynamic ^b							
Input Capacitance	C _{iss}		-	2727	-		
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 9 \text{ V}, f = 1 \text{ MHz}$	-	330	-	pF	
Reverse Transfer Capacitance	C _{rss}		-	305	-		
Total Gate Charge ^c	Qg		-	36	-		
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 9 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5 \text{ A}$	-	6	-	nC	
Gate-Drain Charge ^c	Q _{gd}		-	10	-		
Turn-On Delay Time ^c	t _{d(on)}		-	11	-		
Rise Time ^c	t _r	$V_{DD} = 9 \text{ V}, I_D = 5 \text{ A}, R_g = 3 \Omega$	-	34	-		
Turn-Off Delay Time ^c	t _{d(off)}	$V_{GS} = 4.5 \text{ V,R}_{L} = 1.8\Omega$	-	72	-	ns	
Fall Time ^c	t _f		-	92	-		
Drain-Source Body Diode Ratings and	l Characterist	ics ^b (T _C = 25 °C)					
Continuous Source-Drain Diode Current		T _C = 25 °C	-	-	58	Α	
Pulsed Current	I _{SM}		-	-	232	Α	
Forward Voltage ^a	V_{SD}	I _F = 1 A, V _{GS} = 0 V	_	-	1.2	V	

Notes

- a. Pulse test; pulse width \leq 180 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those in dicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended pe riods may affect device reliability.



TYPICAL CHARAC TERISTICS (25 °C, unless otherwise noted)

Figure 1. Output Characteristics

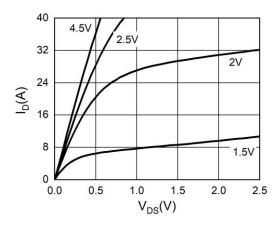


Figure 3. Power Dissipation

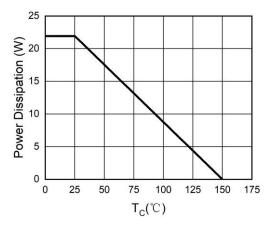


Figure 5. BV_{DSS} vs Junction Temperature

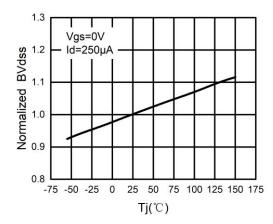


Figure 2. Transfer Characteristics

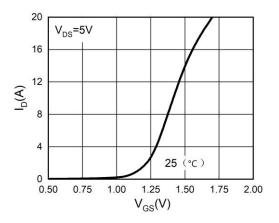


Figure 4. Drain Current

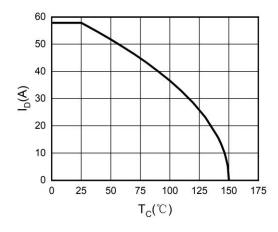
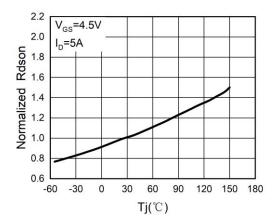


Figure 6. R_{DS(ON)} vs Junction Temperature





TYPICAL CHARAC TERISTICS (25 °C, unless otherwise noted)

Figure 7. Gate Charge Waveforms

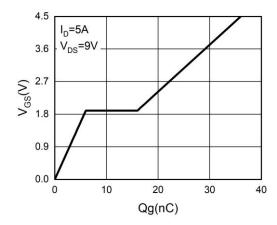


Figure 8. Capacitance

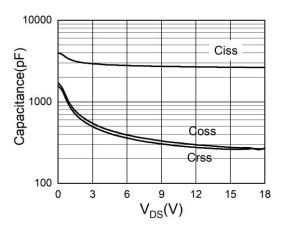


Figure 9. Body-Diode Characteristics

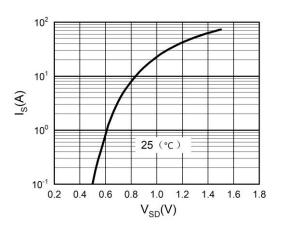
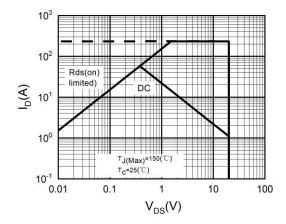
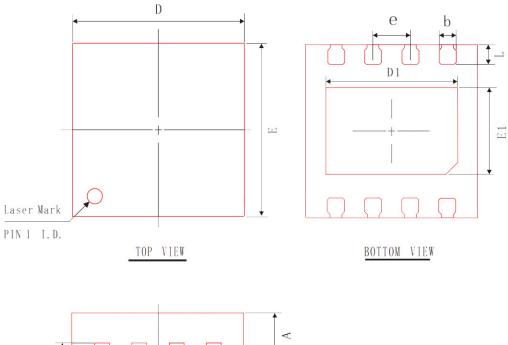


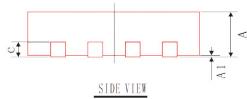
Figure 10. Maximum Safe Operating Area





DFN3X3-8L Package Information





COMMON DIMENSIONS (UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
b	0.25	0.30	0.35
D	2.95	3.00	3.07
Е	2. 95	3.00	3.07
D1	2.25	2.30	2.35
E1	1.40	1.50	1.60
L	0.25	0.35	0.45
С	0.203 REF		
е	0.65 BSC		





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