

## N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}$ (m $\Omega$ )(Typ.)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
30	8.6 at V <sub>GS</sub> = 10 V	40	15 nC		
30	10.7 at V <sub>GS</sub> = 4.5 V	40	15 110		

#### **FEATURES**

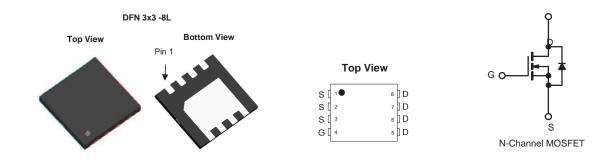
- DT-Trench MOSFET
- 100 % R<sub>g</sub> and UIS Tested
- Low RDS(ON)

# Pb free

RoHS

#### **APPLICATIONS**

- · Notebook AC-in load switch Battery
- · protection charge/discharge



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage	V <sub>DS</sub>	30	V		
Gate-Source Voltage		V <sub>GS</sub>	± 20	- V	
Continuous Drain Current (T <sub>.1</sub> = 175 °C) <sup>a</sup>	T <sub>C</sub> = 25 °C		40		
Continuous Drain Current (1 <sub>J</sub> = 175 C) <sup>a</sup>	T <sub>C</sub> = 100 °C	I <sub>D</sub>	23	А	
Pulsed Drain Current <sup>b</sup>	I <sub>DM</sub>	120			
Single Avalanche Energy		E <sub>AS</sub>	55	mJ	
Maximum Bower Dissinations	T <sub>C</sub> = 25 °C	В	48	w	
Maximum Power Dissipation <sup>c</sup>	T <sub>C</sub> = 100 °C	P <sub>D</sub>	19	v	
Operating Junction and Storage Temperature Ra	T <sub>.I</sub> , T <sub>sta</sub>	- 55 to +150	°C		

THERMAL RESISTANCE RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) <sup>d</sup>	R <sub>thJA</sub>	50	°C/W		
Junction-to-Case (Drain)	$R_{thJC}$	2.6	- C/W		

#### Notes

- a. Calculated continuous current based on maximum allowablejunction temperature.
- b. Repetitive rating; pulse width limited by max. junction temperature.
- c. Pd is based on max. junction temperature, using junction-case thermal resistance.
- d. The value of R<sub>BJA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper,in a still air environment with Ta=25 °C.



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static					ļ		
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30	-	-	.,,	
Gate Threshold Voltage	$V_{GS(th)}$ $V_{DS} = V_{GS}$ , $I_D = 250 \mu A$		1.0	-	2.5	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
Zava Cata Valtaga Dvain Current		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	-	-	1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 24 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	-	-	10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 5 \text{ V}$	40	-	-	Α	
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A	-	8.6	11	mΩ	
Drain-Source On-State nesistance "	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 10 A	-	10.7	15		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 15 A	-	53	-	S	
Dynamic <sup>b</sup>				-		•	
Input Capacitance	C <sub>iss</sub>		-	898	-		
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 15 V, f = 1 MHz	-	153	-	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	89	-		
Total Gate Charge <sup>c</sup>	$Q_g$		=	15	-		
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$	-	1.8	-	nC	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	2.5	-		
Gate Resistance	$R_g$	f = 1 MHz	-	3.5	-	Ω	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		=	8	-		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 15 \text{ A}, R_g = 2.5 \Omega$	-	10	-		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	V <sub>GS</sub> = 10 V	=	22	-	ns	
Fall Time <sup>c</sup>	t <sub>f</sub>		-	6	-		
Drain-Source Body Diode Ratings and	Characterist	ics <sup>b</sup> (T <sub>C</sub> = 25 °C)					
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	-	-	40	Α	
Pulsed Current (t = 100 μs)	I <sub>SM</sub>		-	-	120	Α	
Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>F</sub> = 1 A, V <sub>GS</sub> = 0 V	-	-	1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	1 45 4 31/41 400 4/	-	10.5	-	ns	
Reverse Recovery Charge	$Q_{rr}$	l <sub>F</sub> = 15 A, di/dt = 100 A/μs	-	15	_	nC	

#### Notes

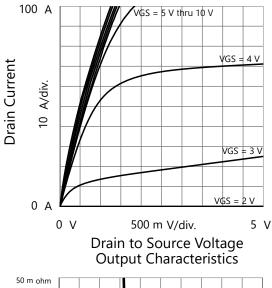
- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

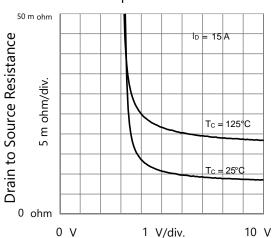
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



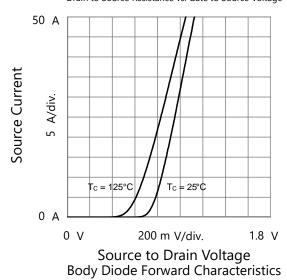


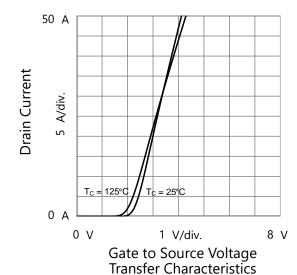
#### **TYPICAL CHARACTERISTICS** (25°C, unless otherwise noted)

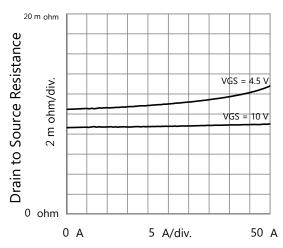




Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage







**Drain Current** 

Capacitances

Drain to Source Resistance vs. Drain Current

1.4 n F

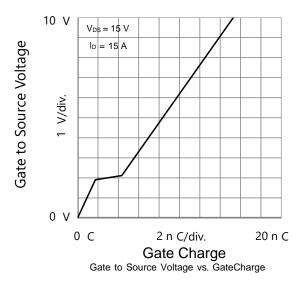
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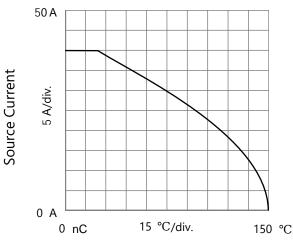
5 V/div.

Drain to Source Voltage

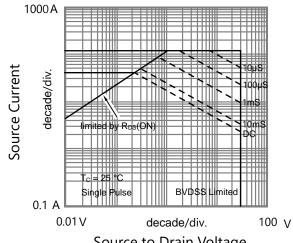


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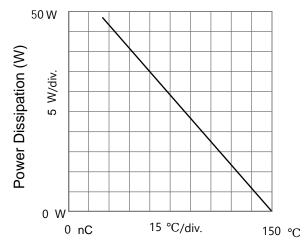




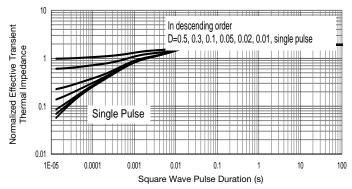
T<sub>C</sub> - Case Temperature



Source to Drain Voltage Safe Operating Area, Junction-to-Ambient

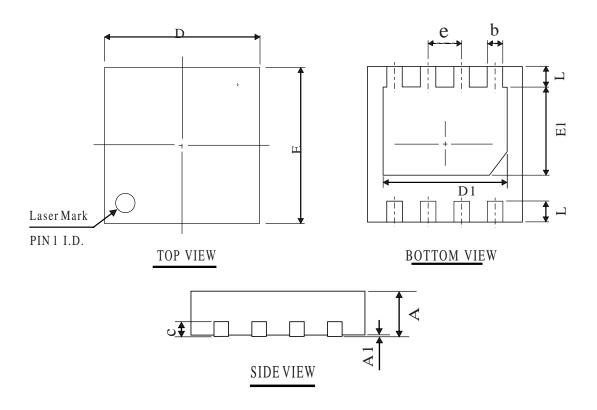


T<sub>C</sub> - Case Temperature



Normalized Thermal Transient Impedance, Junction-to-Case

## DFN3\*3-8L PACKAGE OUTLINE



# COMMON DIMENSIONS (UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
Α	0. 60	0.75	0. 90
A1	0. 00	0.02	0. 08
b	0. 20	0.30	0.45
D	2. 85	3.00	3. 15
Е	2. 85	3.00	3. 15
D1	2. 10	2. 40	2.70
E1	1.50	1.70	2.00
L	0. 20	0.40	0.60
С	0. 203 REF		
e	0. 65 BSC		

#### OTHER DIMENSIONS

A	0. 50	0.55	0.60
A	0.40	0.45	0.50





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