



## P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}$ (m $\Omega$ )(TYP.)	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.)		
- 30	16 at V <sub>GS</sub> = - 10 V	- 39 <sup>a</sup>	33 nC		
- 30	21 at V <sub>GS</sub> = - 4.5 V	- 29 <sup>a</sup>	33110		

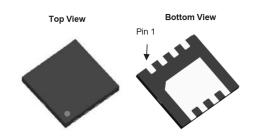
#### **FEATURES**

- DT-Trench Power MOSFET
- Thermally Enhanced DFN3X3 Package
  - Small Footprint Area
  - Low On-Resistance



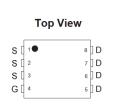
ROHS

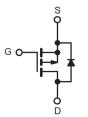
#### DFN 3x3-8L



#### **APPLICATIONS**

 Load Switch, PA Switch, and Battery Switch for Portable Devices





P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		$V_{DS}$	- 30	V	
Gate-Source Voltage		$V_{GS}$	± 20	V	
	T <sub>C</sub> = 25 °C		- 39 <sup>a</sup>		
Continuous Drain Current (T <sub>.I</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	- 26 <sup>a</sup>		
Continuous Brain Guitent (1) = 150 C)	T <sub>A</sub> = 25 °C	טי	- 19 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		- 10 <sup>b, c</sup>	Α	
Pulsed Drain Current (t = 300 μs)		I <sub>DM</sub>	- 160		
Continuous Source-Drain Diode Current	$T_C = 25 ^{\circ}C$	I <sub>S</sub>	- 39 <sup>a</sup>		
Continuous Cource-Brain Blode Current	T <sub>A</sub> = 25 °C	Ď	- 15 <sup>b, c</sup>		
	$T_C = 25 ^{\circ}C$		45	W	
Maximum Power Dissipation	$T_C = 70  ^{\circ}C$	$P_{D}$	38		
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	٠ ل	3.9 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		2.2 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		$T_J,T_stg$	- 55 to 150	°C	
Soldering Recommendations (Peak Temperature) <sup>d, e</sup>			260	J	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, f</sup>	t ≤ 5 s	R <sub>thJA</sub>	25	35	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R <sub>th.IC</sub>	3.3	4.5	] 0///	

#### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 2 s.
- d. See solder profile The DFN3X3 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under steady state conditions is 80 °C/W.





Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 μA		- 10		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	η – 230 μΛ		2.2			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V			- 1		
Zero Gate voltage Drain Current		V <sub>DS</sub> = - 24 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	- 10 μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 39			Α	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10 A		16	19.5		
Drain-Source On-State Resistance <sup>a</sup>	, ,DS(on)	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5 A		21	29	mΩ	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 10 A		40		S	
Dynamic <sup>b</sup>					<b>'</b>		
Input Capacitance	C <sub>iss</sub>			1438		pF	
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		160			
Reverse Transfer Capacitance	C <sub>rss</sub>			152			
Total Gate Charge	Qq			33			
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 10 A		4		nC	
Gate-Drain Charge	Q <sub>gd</sub>			5.5			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		12		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			15			
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 0.75 \Omega$		17		1	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -5 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		20		1	
Fall Time	t <sub>f</sub>			40			
Turn-On Delay Time	t <sub>d(on)</sub>			8		ns	
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 0.75 \Omega$		11			
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 10 A, $V_{GEN}$ = -10 V, $R_g$ = 1 $\Omega$		15			
Fall Time	t <sub>f</sub>			23			
Drain-Source Body Diode Characterist	ics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 39	A	
Pulse Diode Forward Current	I <sub>SM</sub>				160		
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 10 A, V <sub>GS</sub> = 0 V		- 0.7	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			40		ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = - 10 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		20		nC	
Reverse Recovery Fall Time	t <sub>a</sub>	1571, direct = 10070po, 1j = 20 0		15		ns	
Reverse Recovery Rise Time	t <sub>b</sub>			26		7 115	

### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

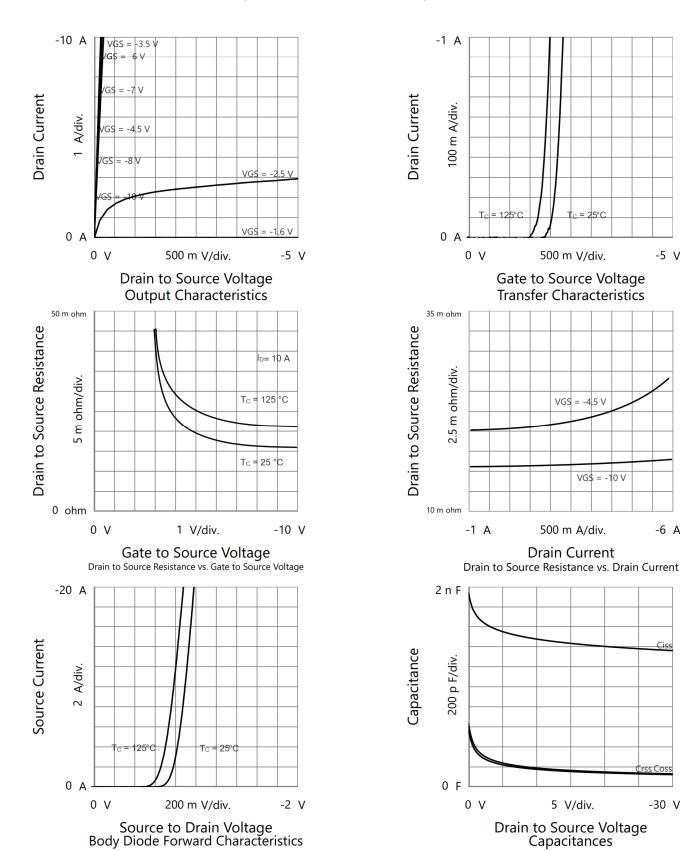
-5 V

-6 A

VG\$ = -10 V



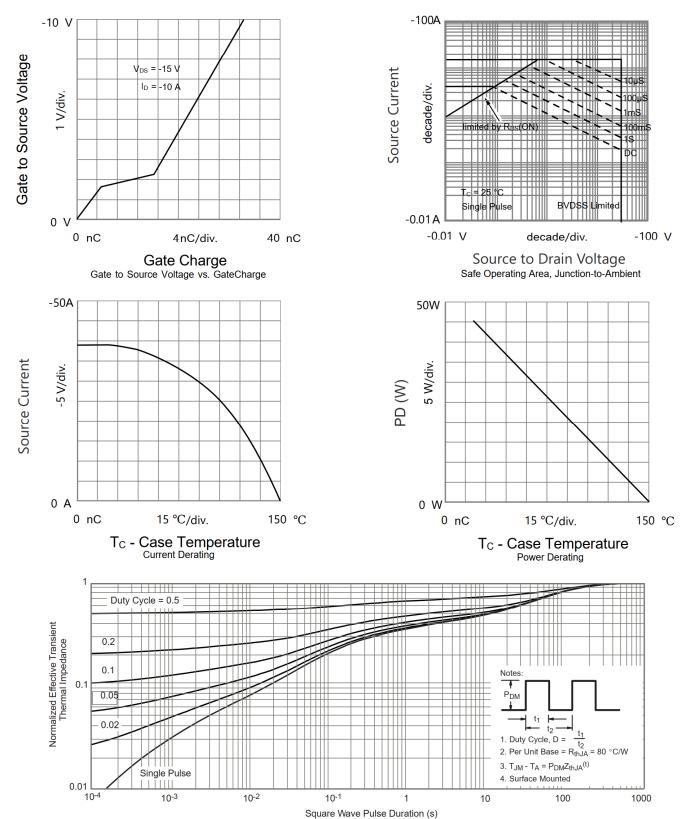
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



-30 V



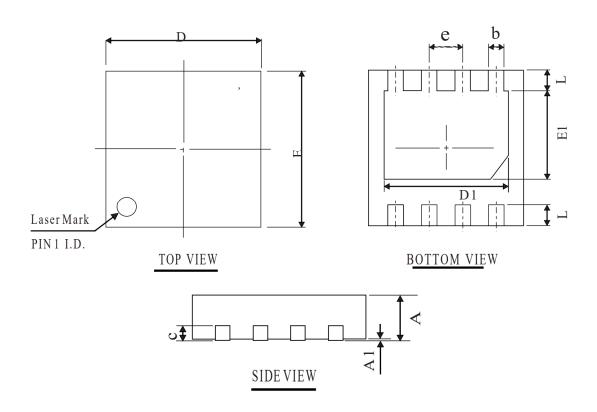
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



# DFN3\*3-8L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	0. 60	0.75	0. 90
A1	0. 00	0.02	0. 08
ь	0. 20	0.30	0.45
D	2.85	3.00	3. 15
E	2. 85	3.00	3. 15
D1	2. 10	2.40	2.70
E1	1.50	1.70	2.00
L	0. 20	0.40	0.60
C	0. 203 REF		
e	0. 65 BSC		

### OTHER DIMENSIONS

A	0. 50	0.55	0.60
A	0.40	0.45	0.50





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