

N-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ)(Typ.)	I _D (A) ^a	Q _g (Typ.)
40	15 at V _{GS} = 10 V	28	19.3 nC
	17 at V _{GS} = 4.5 V		

FEATURES

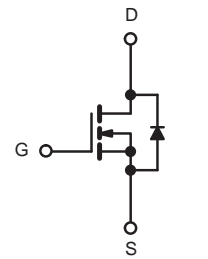
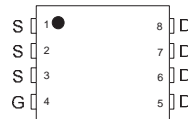
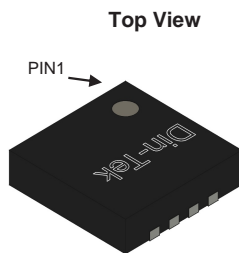
- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- Low Gate Charge
- RoHS and Halogen-Free Compliant



APPLICATIONS

- Load Switch
- PWM Application
- Power Management

DFN3X3-8L Pin Configuration



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 175 °C) ^a	I _D	T _C = 25 °C	A
		T _C = 100 °C	
Pulsed Drain Current ^b	I _{DM}	112	
Single Avalanche Energy	E _{AS}	43	mJ
Maximum Power Dissipation ^c	P _D	T _C = 25 °C	W
		T _C = 100 °C	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^d	R _{thJA}	62	°C/W
Junction-to-Case (Drain)	R _{thJC}	6	

Notes

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- P_D is based on max. junction temperature, using junction-case thermal resistance.
- The value of R_{thJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.

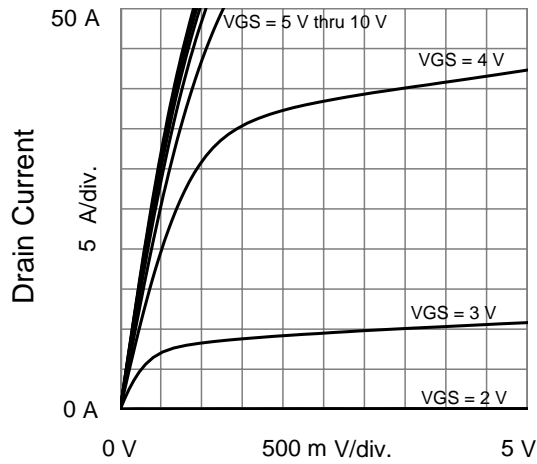
SPECIFICATIONS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	40	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1	-	3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 40\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	μA
		$V_{DS} = 32\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$	-	-	10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	28	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$	-	15	18	m Ω
		$V_{GS} = 4.5\text{ V}, I_D = 8\text{ A}$	-	17	22	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 5\text{ V}, I_D = 10\text{ A}$	-	67	-	S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 20\text{ V}, f = 1\text{ MHz}$	-	939	-	pF
Output Capacitance	C_{oss}		-	77	-	
Reverse Transfer Capacitance	C_{rss}		-	67	-	
Total Gate Charge ^c	Q_g	$V_{DS} = 20\text{ V}, V_{GS} = 10\text{ V}, I_D = 10\text{ A}$	-	19.3	-	nC
Gate-Source Charge ^c	Q_{gs}		-	1.8	-	
Gate-Drain Charge ^c	Q_{gd}		-	3.25	-	
Gate Resistance	R_g	$f = 1\text{ MHz}$	-	4.6	-	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 20\text{ V}, I_D = 10\text{ A}, R_g = 3\text{ }\Omega$ $V_{GS} = 10\text{ V}, R_L = 1\text{ }\Omega$	-	12	-	ns
Rise Time ^c	t_r		-	22	-	
Turn-Off Delay Time ^c	$t_{d(off)}$		-	42	-	
Fall Time ^c	t_f		-	15	-	
Drain-Source Body Diode Ratings and Characteristics ^b ($T_C = 25\text{ }^\circ\text{C}$)						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$	-	-	28	A
Pulsed Current	I_{SM}		-	-	112	A
Forward Voltage ^a	V_{SD}	$I_F = 1\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$	-	15	-	ns
Reverse Recovery Charge	Q_{rr}		-	21	-	nC

Notes

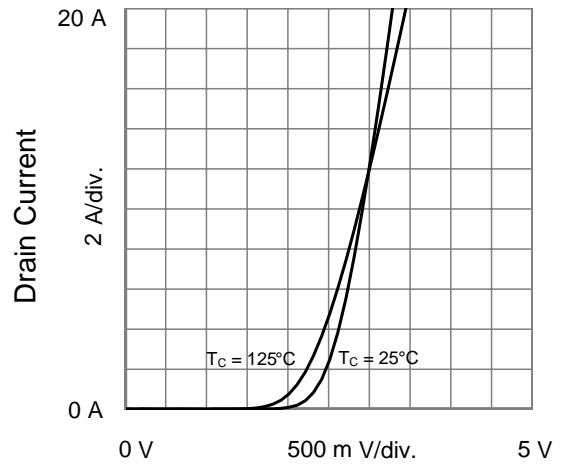
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.
 c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

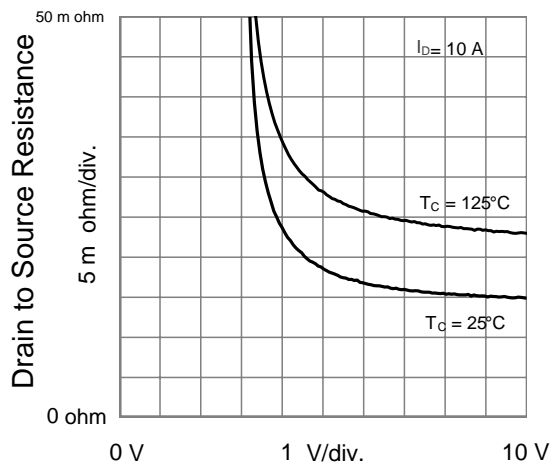
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



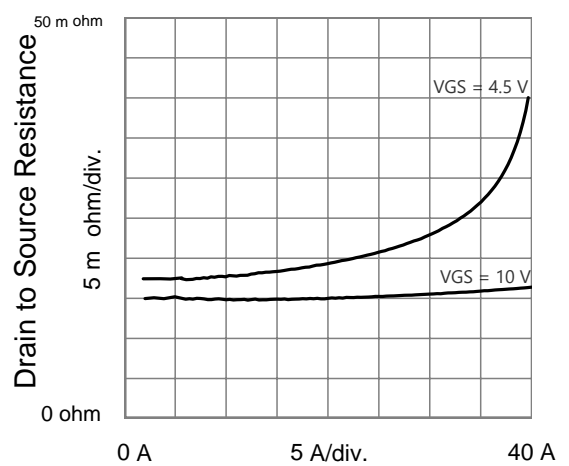
Drain to Source Voltage Output Characteristics



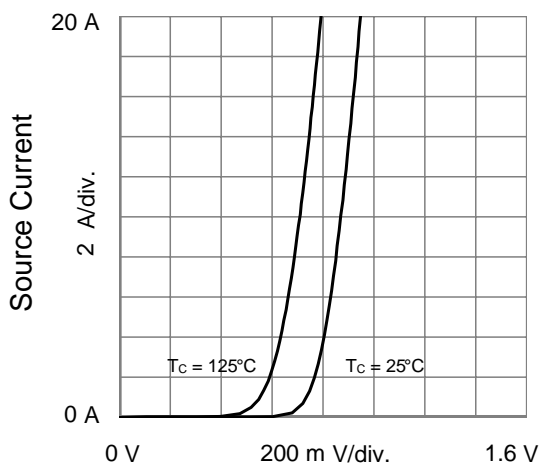
Gate to Source Voltage Transfer Characteristics



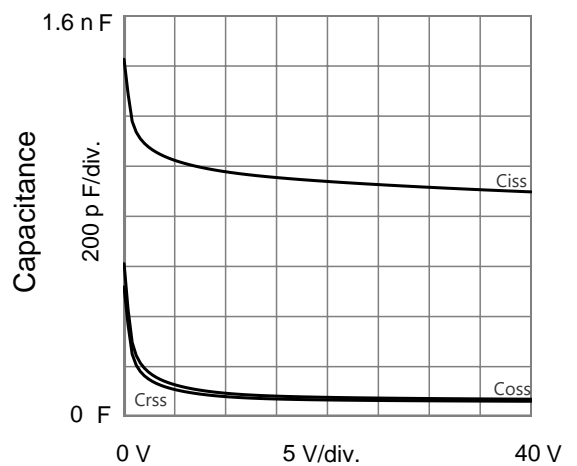
Gate to Source Voltage Drain to Source Resistance vs. Gate to Source Voltage



Drain Current

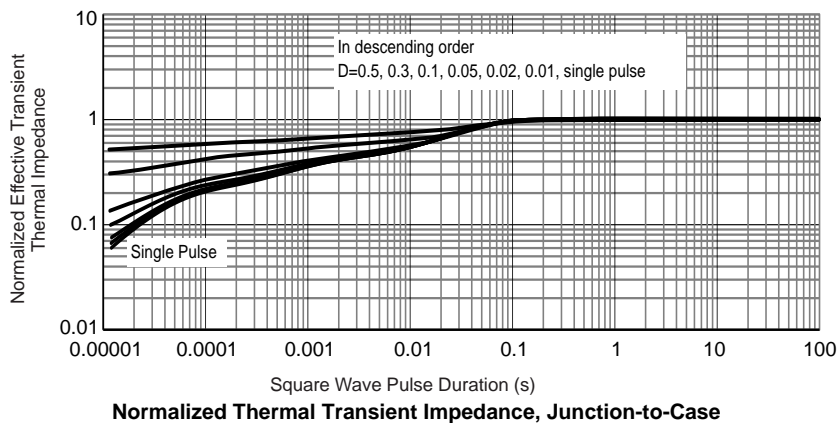
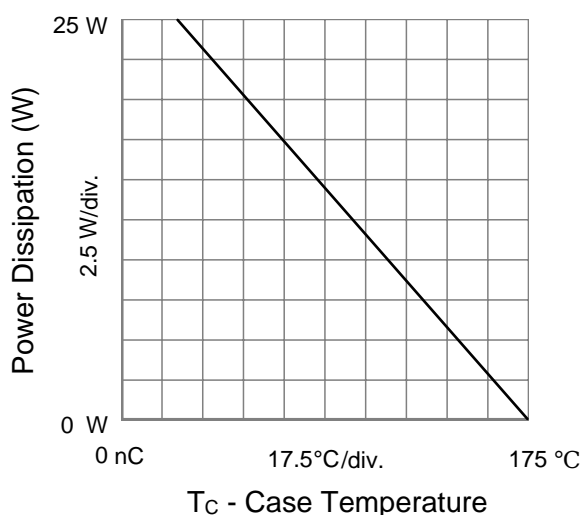
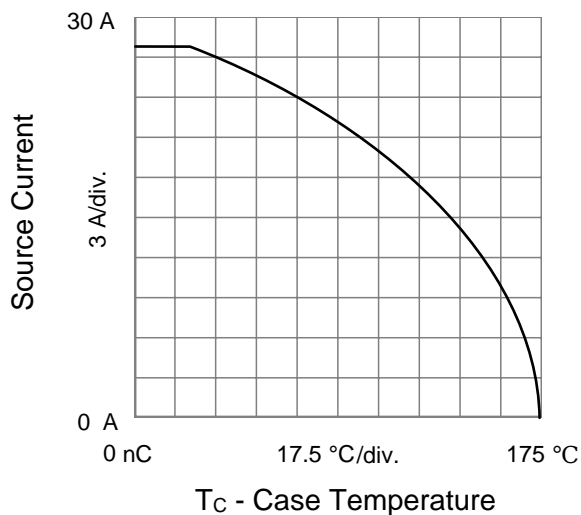
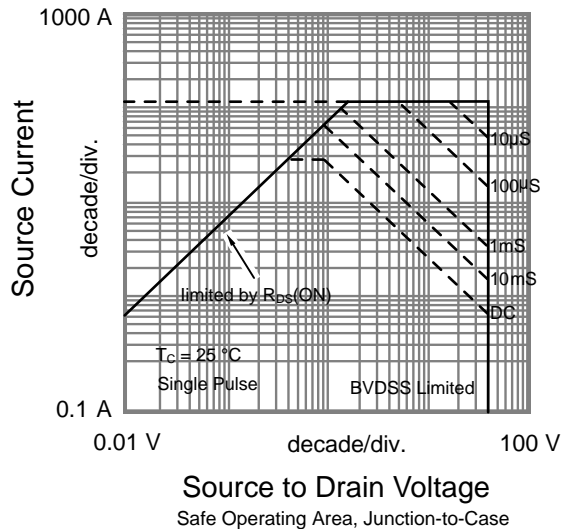
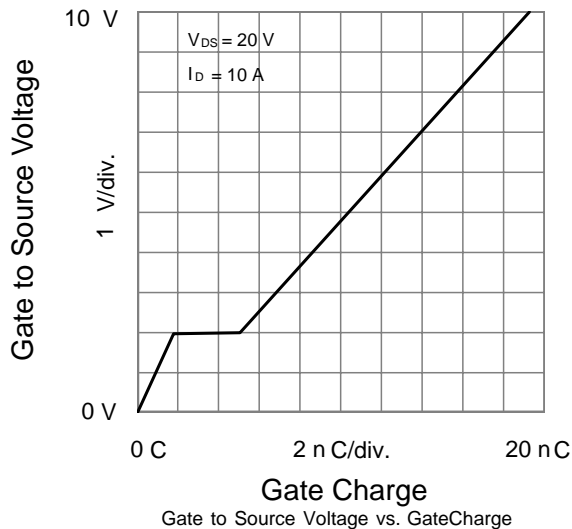


Source to Drain Voltage Body Diode Forward Characteristics

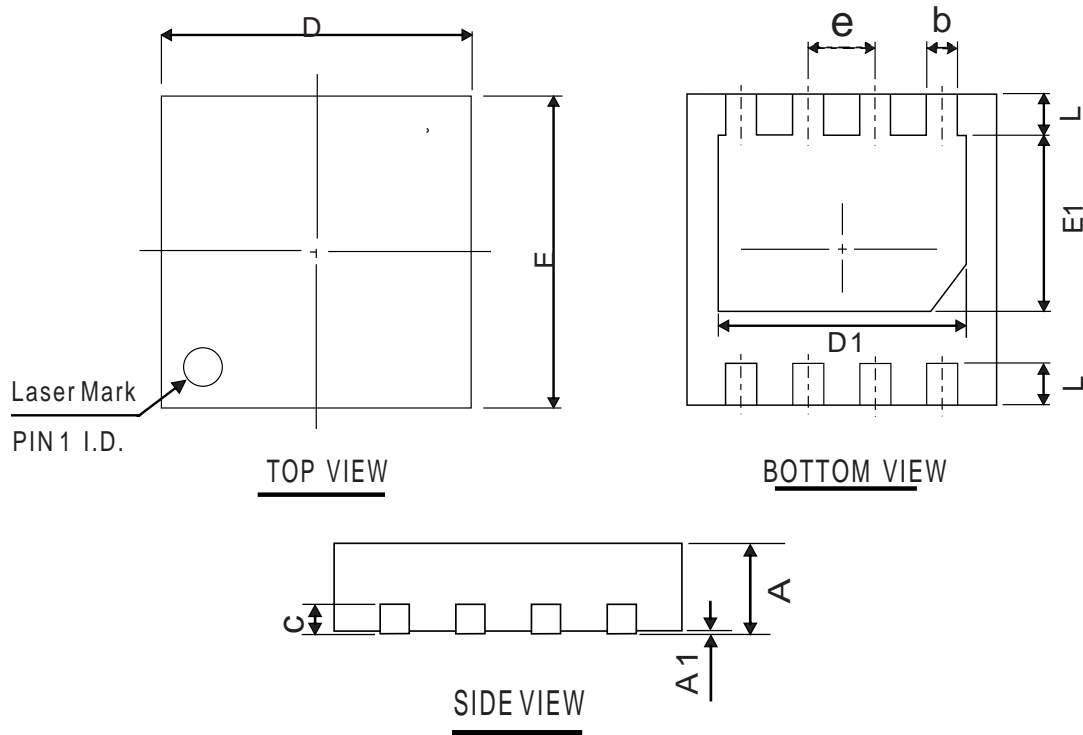


Drain to Source Voltage Capacitances

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



DFN3*3-8L PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	0.60	0.75	0.90
A1	0.00	0.02	0.08
b	0.00	0.30	0.45
D	2.85	3.00	3.15
E	2.85	3.00	3.15
D1	2.10	2.40	2.70
E1	1.50	1.70	2.00
L	0.20	0.40	0.60
c	0.203REF		
e	0.65BSC		

OTHER DIMENSIONS

A	0.50	0.55	0.60
A	0.40	0.45	0.50

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