

N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ) (Typ.)	I _D (A) ^{a,e}	Q _g (Typ.)
60	13 at V _{GS} = 10 V	35	15.3 nC
	15.5 at V _{GS} = 4.5 V	30	

FEATURES

- DT-Trench Power MOSFET
- 100 % R_g and UIS Tested
- AEC-Q101 Qualified for Automotive Applications



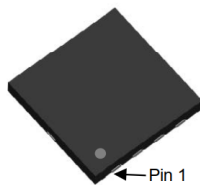
RoHS
COMPLIANT

APPLICATIONS

- Notebook PC Core
- VRM/POL

DFN 3x3 EP

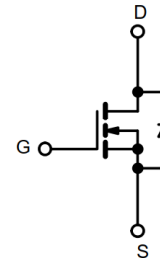
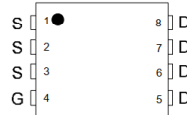
Top View



Bottom View



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 175 °C)	I _D	T _C = 25 °C	35 ^{a, e}
		T _C = 70 °C	32 ^e
		T _A = 25 °C	12 ^{b, c}
		T _A = 70 °C	11 ^{b, c}
Pulsed Drain Current	I _{DM}	110	
Avalanche Current Pulse	I _{AS}	26	
Single Pulse Avalanche Energy	E _{AS}	45	mJ
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	35 ^{a, e}
		T _A = 25 °C	30 ^{b, c}
Maximum Power Dissipation	P _D	T _C = 25 °C	30
		T _C = 70 °C	19
		T _A = 25 °C	3.45 ^{b, c}
		T _A = 70 °C	2.3 ^{b, c}
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R _{thJA}	33	43	°C/W
Maximum Junction-to-Case	R _{thJC}	3	5	

Notes:

- Based on T_C = 25 °C.
- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under steady state conditions is 90 °C/W.
- Calculated based on maximum junction temperature.

SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min .	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	60			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$		35		mV/ $^\circ\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			-5.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	1		3	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}$			1	μA
		$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, T_J = 55\text{ }^\circ\text{C}$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}, V_{GS} = 10\text{ V}$	35			A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$		13	15	m Ω
		$V_{GS} = 4.5\text{ V}, I_D = 8\text{ A}$		15.5	20	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15\text{ V}, I_D = 10\text{ A}$		90		S
Dynamic^b						
Input Capacitance	C_{iss}	$V_{DS} = 30\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$		739		pF
Output Capacitance	C_{oss}			185		
Reverse Transfer Capacitance	C_{rss}			16		
Total Gate Charge	Q_g	$V_{DS} = 30\text{ V}, V_{GS} = 10\text{ V}, I_D = 10\text{ A}$		15.3		nC
Gate-Source Charge	Q_{gs}			1.5		
Gate-Drain Charge	Q_{gd}			3.7		
Gate Resistance	R_g	$f = 1\text{ MHz}$		1.5		Ω
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 30\text{ V}, R_L = 1.5\text{ }\Omega$ $I_D \cong 10\text{ A}, V_{GEN} = 10\text{ V}, R_g = 3\text{ }\Omega$		6		ns
Rise Time	t_r			2.8		
Turn-Off Delay Time	$t_{d(off)}$			19		
Fall Time	t_f			3		
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^\circ\text{C}$			35	A
Pulse Diode Forward Current ^a	I_{SM}				110	
Body Diode Voltage	V_{SD}	$I_S = 1\text{ A}$		0.6	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 10\text{ A}, di/dt = 100\text{ A}/\mu\text{s}, T_J = 25\text{ }^\circ\text{C}$		55	88	ns
Body Diode Reverse Recovery Charge	Q_{rr}			75	110	nC
Reverse Recovery Fall Time	t_a			35		ns
Reverse Recovery Rise Time	t_b			30		

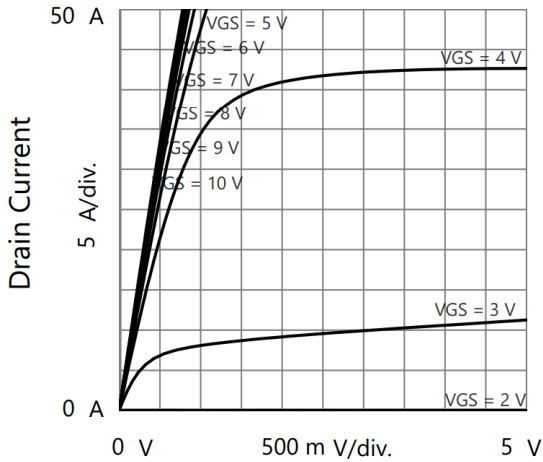
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

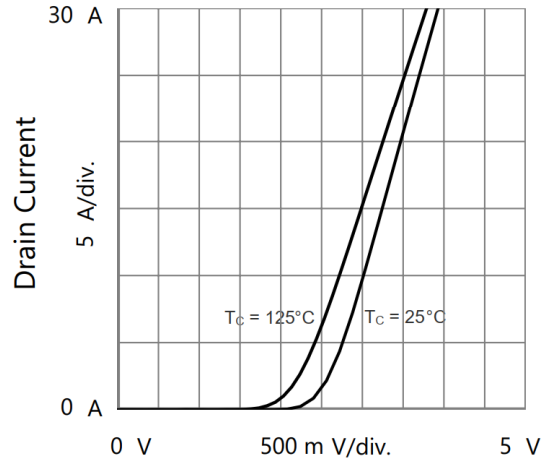
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

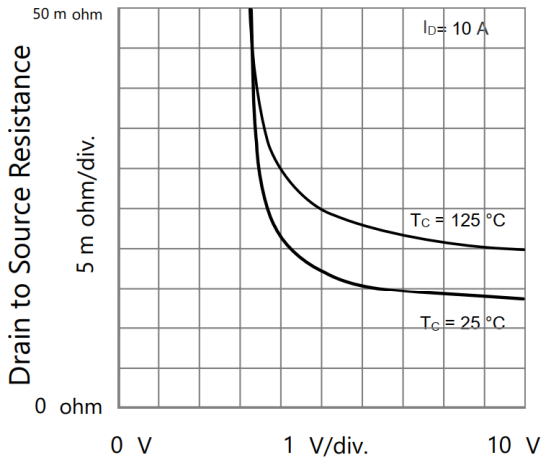
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



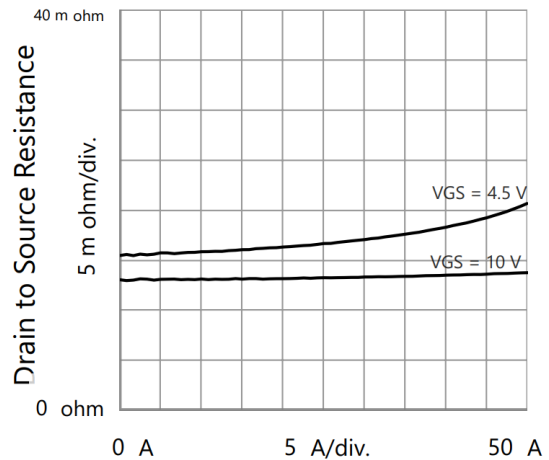
Drain to Source Voltage
Output Characteristics



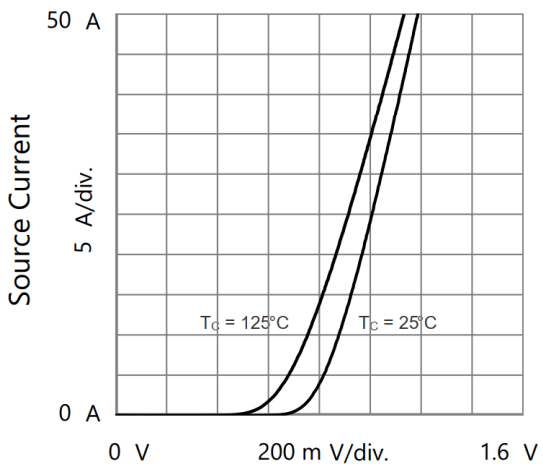
Gate to Source Voltage
Transfer Characteristics



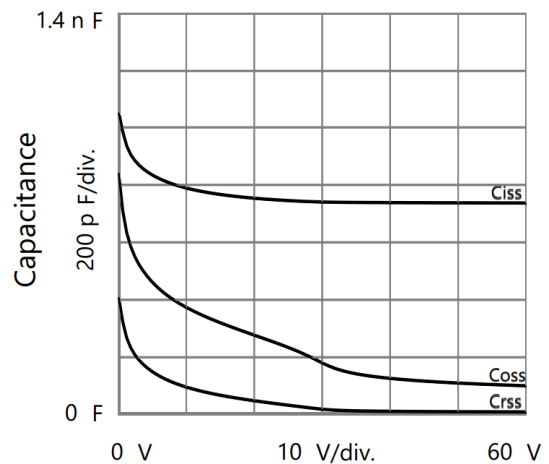
Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage



Drain Current
Drain to Source Resistance vs. Drain Current

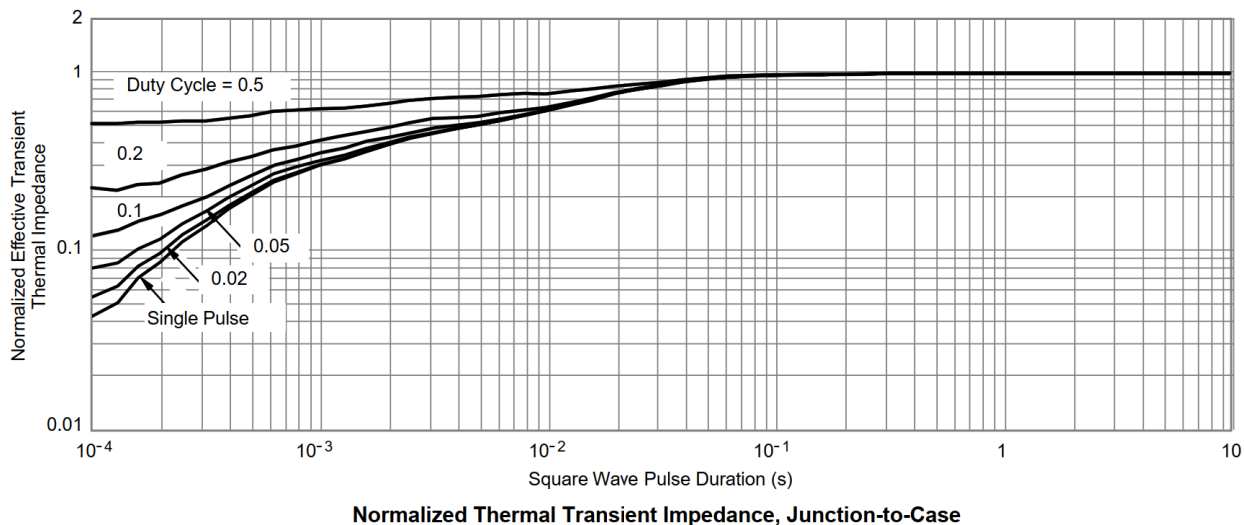
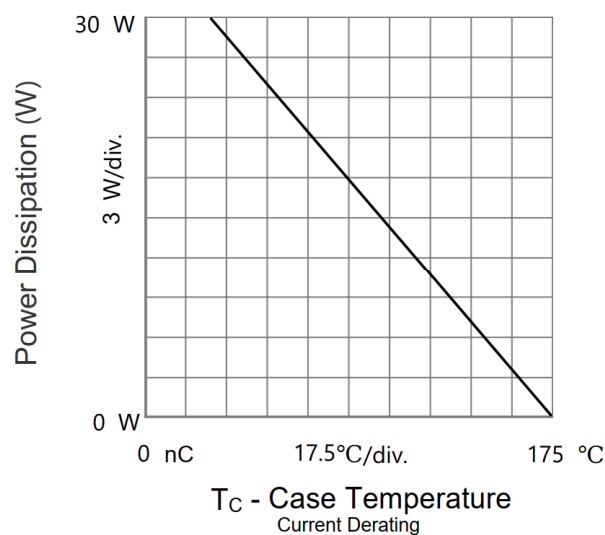
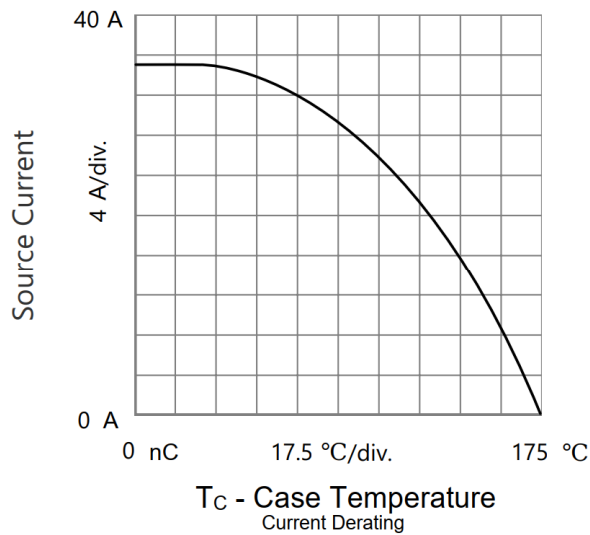
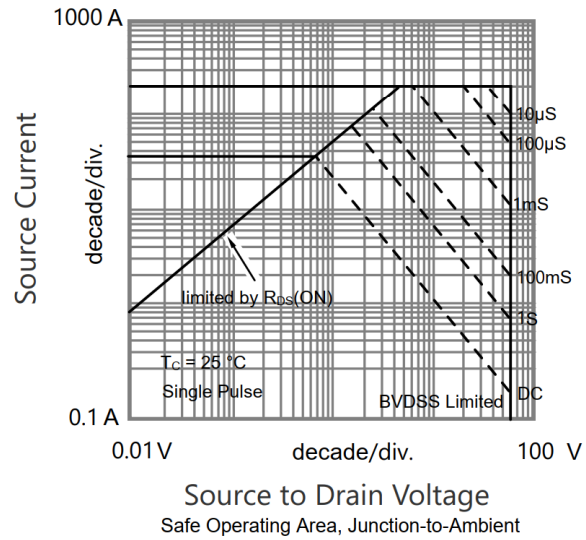
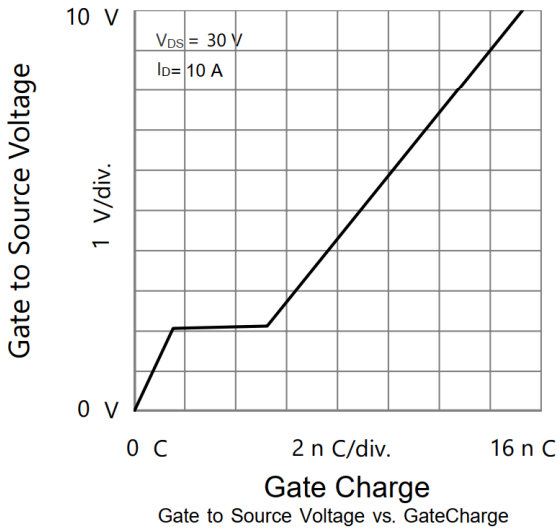


Source to Drain Voltage
Body Diode Forward Characteristics

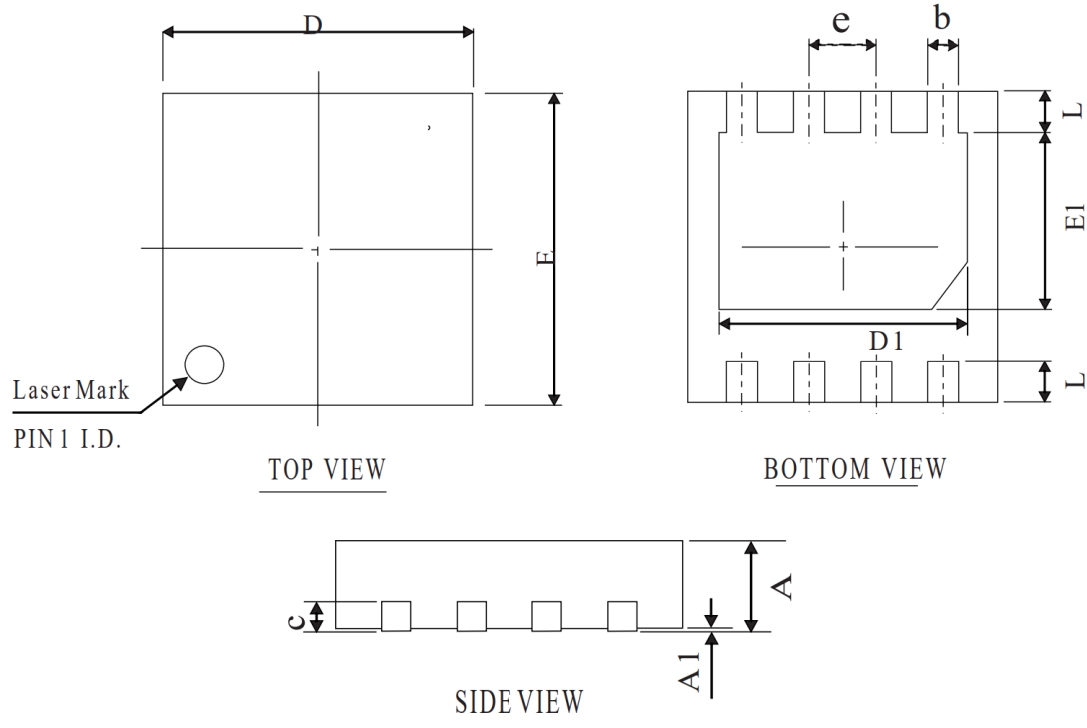


Drain to Source Voltage
Capacitances

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



DFN3*3-8L PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=mm)

SYMBOL	MIN	NOM	MAX
A	0.60	0.75	0.90
A1	0.00	0.02	0.08
b	0.20	0.30	0.45
D	2.85	3.00	3.15
E	2.85	3.00	3.15
D1	2.10	2.40	2.70
E1	1.50	1.70	2.00
L	0.20	0.40	0.60
c	0.203 REF		
e	0.65 BSC		

OTHER DIMENSIONS

A	0.50	0.55	0.60
A	0.40	0.45	0.50

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