

DTQ6006L www.din-tek.jp

N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^{a, d}	Q _g (Typ.)	
100	0.0063 at V _{GS} = 10 V	110	125nC	

DFN5X6 **Top View Bottom View** - PIN1

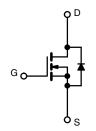
FEATURES

- TrenchFET IIPower MOSFET
- 100 % Rgand UIS Tested

APPLICATIONS

- Synchronous rectification
- · Primary side switch
- DC/DC converters
- OR-ing
- Power supplies
- Motor drive control
- Battery and load switch





PARAMETER	SYMBOL	LIMIT			
Drain-source voltage Gate-source voltage		V _{DS}		100	
		V _{GS}	± 20		
	T _C = 25 °C		110 ^a		
Continuous durin compart (T. 150.80)	T _C = 70 °C		89.8		
Continuous drain current (T _J = 150 °C)	T _A = 25 °C		41.2 ^{b, c}		
	T _A = 70 °C		27.4 ^{b, c}		
Pulsed drain current (t = 100 µs)		I _{DM}	440	— A	
Operation operation of the design of the summer t	T _C = 25 °C		110 ^a		
Continuous source-drain diode current	T _A = 25 °C	I _S	6.6 ^{b, c}		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	68		
Single pulse avalanche energy	L = 0.1 MH	E _{AS}	113	mJ	
	T _C = 25 °C		175		
Maximum neuror dissinction	T _C = 70 °C		110	w	
Maximum power dissipation	T _A = 25 °C	P _D	7.25 ^{b, c}	VV	
	T _A = 70 °C		5 b, c		
Operating junction and storage temperature	range	T _J , T _{stq}	-55 to +150	°C	
Soldering recommendations (peak temperature) ^c			260	-0	

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient ^b	t ≤ 10 s	R _{thJA}	12	20	
Maximum junction-to-case (drain)	Steady state	R _{thJC}	0.7	1	°C/W
Maximum junction-to-case (source)	Steady state	R _{thJC}	1.0	1.4	

Notes:

a. Based on T_C = 25 °C. b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Calculated based on maximum junction temperature.



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \ \mu\text{A}$	100	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_J$	I _D = 250 μA	-	56	-	mV/°C	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA	-	-6	-		
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$ 1		-	2.5	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	100	nA	
Zara acta valtaga drain averant		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	1		
Zero gate voltage drain current		$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 70 ^{\circ}\text{C}$	-	-	10	μA	
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	110	-	-	Α	
Drain-source on-state resistance a	R _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	-	0.0063	0.0075	Ω	
Forward transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 20 \text{ A}$	-	68	-	S	
Dynamic ^b							
Input capacitance	C _{iss}		-	5266	-	pF	
Output capacitance	C _{oss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	958	-		
Reverse transfer capacitance	C _{rss}		-	30	-		
Total gate charge	Qg		-	125	-		
Gate-source charge	Q _{gs}	V_{DS} = 50 V, V_{GS} = 10 V, I_{D} = 20 A	-	10	-	- nC -	
Gate-drain charge	Q _{gd}		-	10.6	-		
Output charge	Q _{oss}	$V_{DS} = 50 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	67	109		
Gate resistance	R _g	f = 1 MHz	0.3	1	2	Ω	
Turn-on delay time	t _{d(on)}		-	15	28		
Rise time	tr	$V_{DD} = 50 \text{ V}, \text{ R}_{L} = 2.5 \Omega, \text{ I}_{D} \cong 20 \text{ A},$	-	17	29	ns	
Turn-off delay time	t _{d(off)}	$V_{GEN} = 10 \text{ V}, \text{ R}_{g} = 1 \Omega$	-	35	72	- 115	
Fall time	t _f		-	9	18		
Drain-Source Body Diode Characteristic	s						
Continuous source-drain diode current	ا _S	T _C = 25 °C	-	-	110	^	
Pulse diode forward current ($t_p = 100 \ \mu s$)	I _{SM}		-	-	440	A	
Body diode voltage	V _{SD}	$I_{S} = 5 \text{ A}, V_{GS} = 0 \text{ V}$	-	0.7	1.2	V	
Body diode reverse recovery time	t _{rr}		-	54	100	ns	
Body diode reverse recovery charge	Q _{rr}		-	76	140	nC	
Reverse recovery fall time	ta	- I _F = 20 A, di/dt = 100 A/µs, T _J = 25 °C		27	-		
Reverse recovery rise time	t _b		-	27	-	ns	

Notes

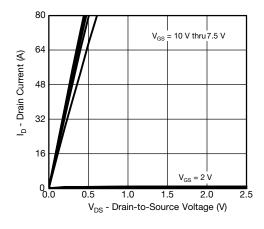
a.P ulse test; pulse width $\leq 300~\mu\text{s},~\text{duty}~\text{cycle} \leq 2~\%$

b. Guaranteed by design, not subject to production testing

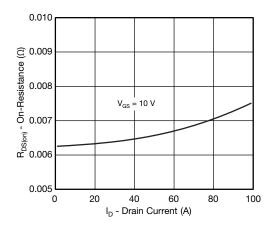
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



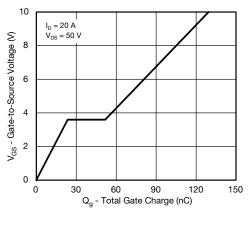
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



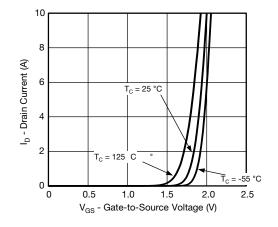
Output Characteristics



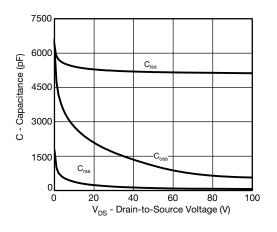
On-Resistance vs. Drain Current and Gate Voltage



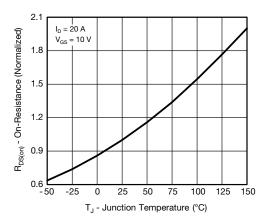
Gate Charge



Transfer Characteristics



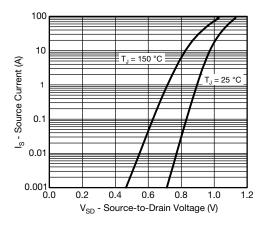
Capacitance



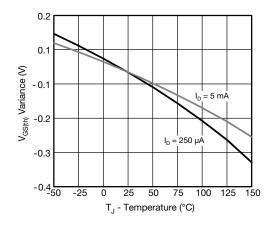
On-Resistance vs. Junction Temperature



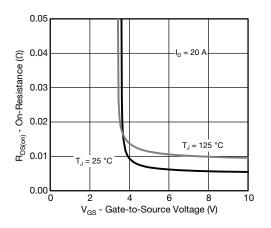
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



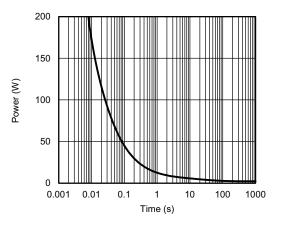
Source-Drain Diode Forward Voltage



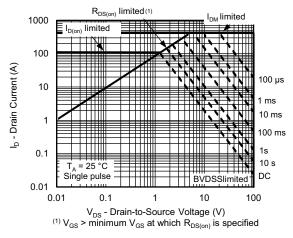
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



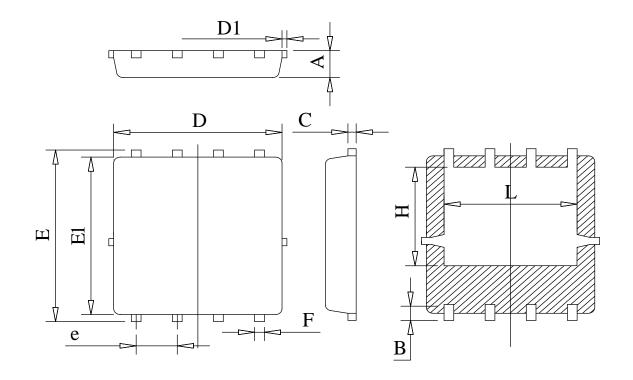
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient



DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit : mm			
Symbol	Min	Тур	Max
Α	0.78	0.95	1.12
В	0.45	0.58	0.78
С	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
Е	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
Н	3.25	3.47	3.70
L	3.75	4.00	4.25



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