

N-Channel 40 V (D-S) Super Junction Power MOSFET



RoHS
COMPLIANT

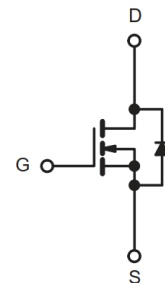
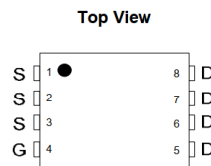
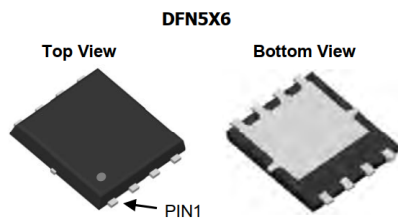
PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (mΩ) (TYP.)	I _D (A) ^{a,d}	Q _g (TYP.)
40	0.85 at V _{GS} = 10 V	240	97 nC

FEATURES

- DT-SJ Power MOSFET
- 100 % R_g and UIS Tested
- Excellent Gate Charge x RDS(ON) Product (FOM)

APPLICATIONS

- Net working
- Load Switch



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C)	I _D	T _C = 25 °C	240 ^{a,d}
		T _C = 70 °C	200
		T _A = 25 °C	54 ^{b,c}
		T _A = 70 °C	40 ^{b,c}
Pulsed Drain Current	I _{DM}	830	A
Avalanche Current	I _{AS}	90 ^a	
Single Avalanche Energy	E _{AS}	760	
Maximum Power Dissipation	P _D	T _C = 25 °C	330 ^b
		T _C = 70 °C	211 ^b
		T _A = 25 °C	8.5 ^{b,c}
		T _A = 70 °C	5.4 ^{b,c}
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, c}	t ≤ 10 s	R _{thJA}	11	14.7	°C/W
Maximum Junction-to-Case	Steady State	R _{thJC}	0.3	0.38	

Notes

- Based on T_C = 25 °C.
- Surface mounted on 1" x 1" FR4 board.
- t = 10 s.
- Package limited.

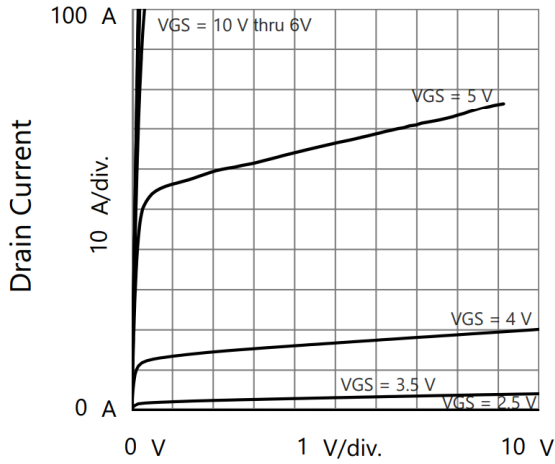
SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	40	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2	-	4	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 40\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	μA
		$V_{DS} = 36\text{ V}, V_{GS} = 0\text{ V}, T_J = 85\text{ }^\circ\text{C}$	-	-	10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 10\text{ V}, V_{GS} = 10\text{ V}$	200	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 30\text{ A}$	-	0.85	1.1	m Ω
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15\text{ V}, I_D = 30\text{ A}$	-	85	-	S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}, V_{DS} = 20\text{ V}, f = 1\text{ MHz}$	-	7000	-	pF
Output Capacitance	C_{oss}		-	2230	-	
Reverse Transfer Capacitance	C_{rss}		-	300	-	
Total Gate Charge ^c	Q_g	$V_{DS} = 20\text{ V}, V_{GS} = 10\text{ V}, I_D = 30\text{ A}$	-	97	-	nC
Gate-Source Charge ^c	Q_{gs}		-	20	-	
Gate-Drain Charge ^c	Q_{gd}		-	12	-	
Gate Resistance	R_g	$f = 1\text{ MHz}$	-	2	-	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 20\text{ V}, R_L = 2\text{ }\Omega$ $I_D = 30\text{ A}, V_{GEN} = 10\text{ V}, R_g = 3\text{ }\Omega$	-	15	-	ns
Rise Time ^c	t_r		-	10	-	
Turn-Off Delay Time ^c	$t_{d(off)}$		-	55	-	
Fall Time ^c	t_f		-	9	-	
Drain-Source Body Diode Ratings and Characteristics ^b ($T_C = 25\text{ }^\circ\text{C}$)						
Continuous Source Current	I_S	$T_C = 25\text{ }^\circ\text{C}$	-	-	240	A
Pulsed Source Current	I_{SM}		-	-	830	A
Forward Voltage ^a	V_{SD}	$I_F = 1\text{ A}, V_{GS} = 0\text{ V}$	-	-	1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 30\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$	-	40	-	ns
Reverse Recovery Charge	Q_{rr}		-	120	-	nC

Notes

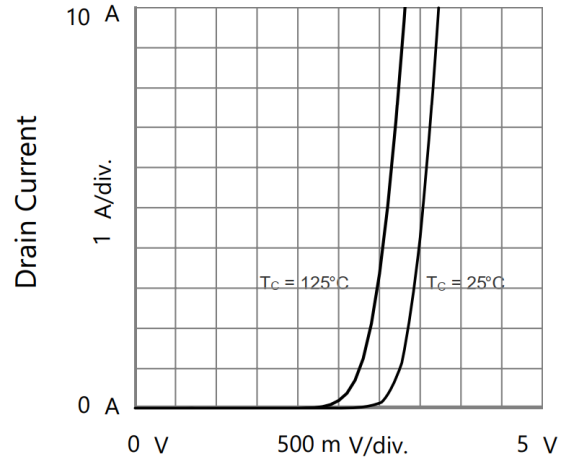
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

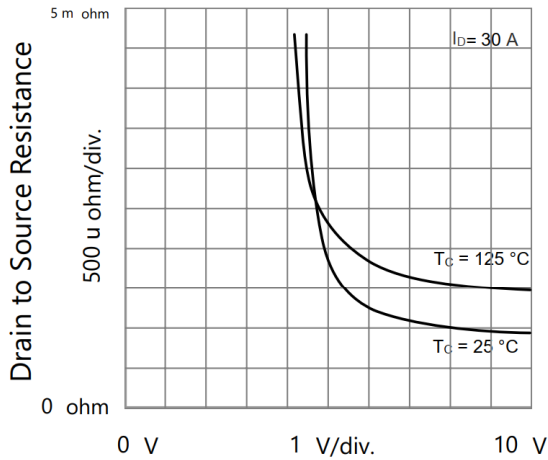
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Drain to Source Voltage
Output Characteristics

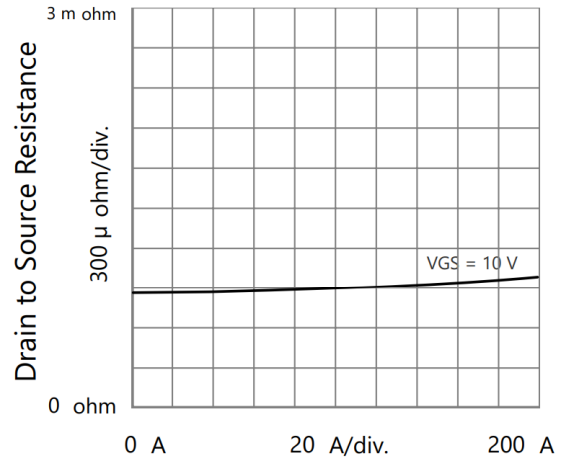


Gate to Source Voltage
Transfer Characteristics



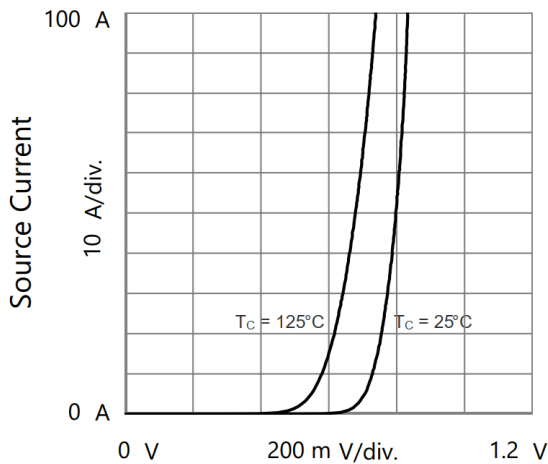
Gate to Source Voltage

Drain to Source Resistance vs. Gate to Source Voltage

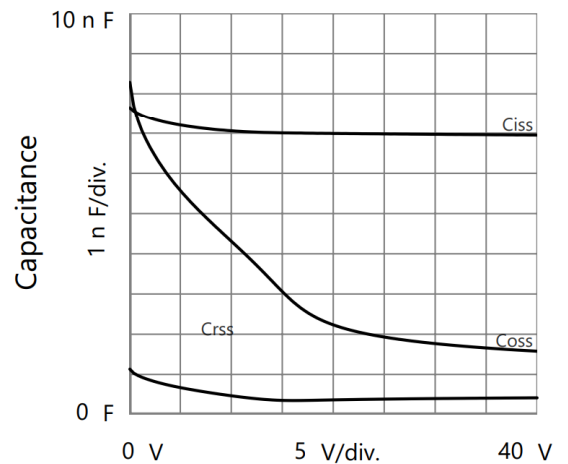


Drain Current

Drain to Source Resistance vs. Drain Current

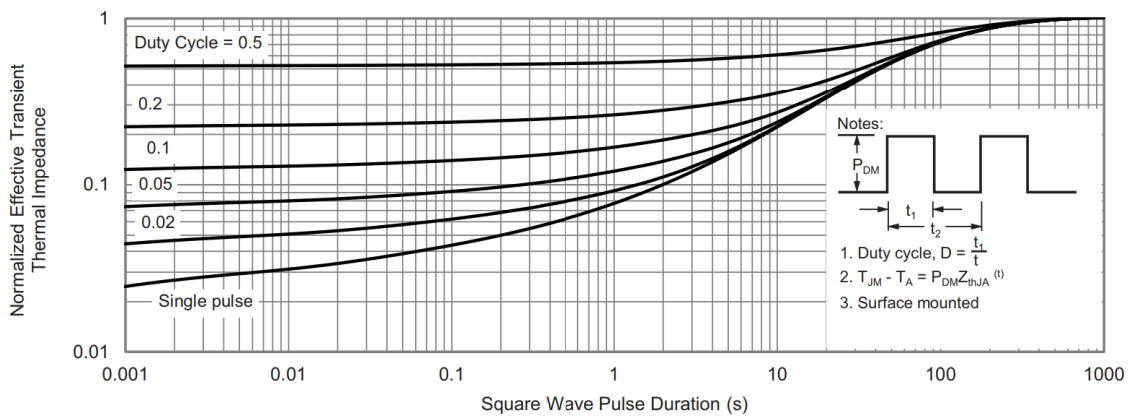
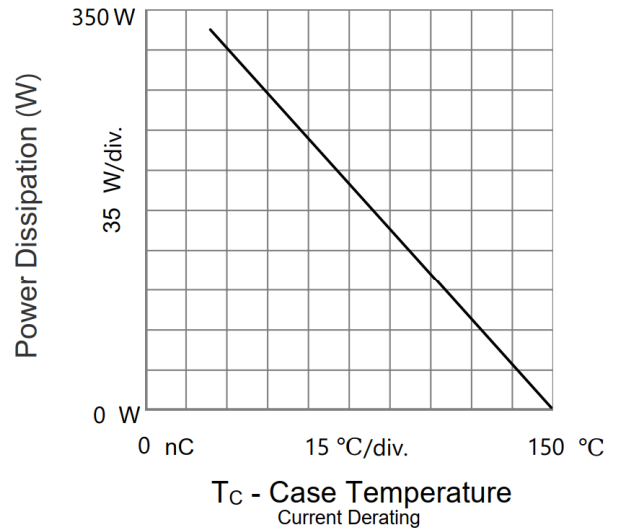
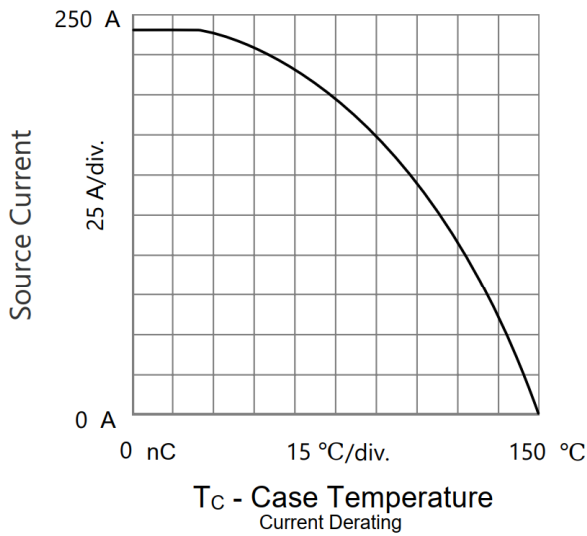
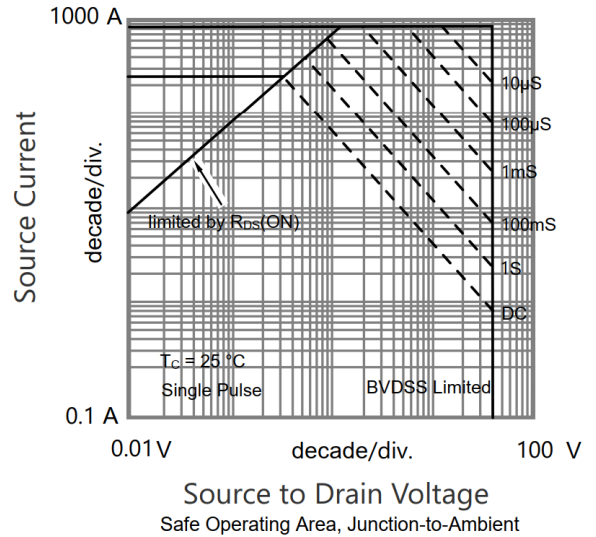
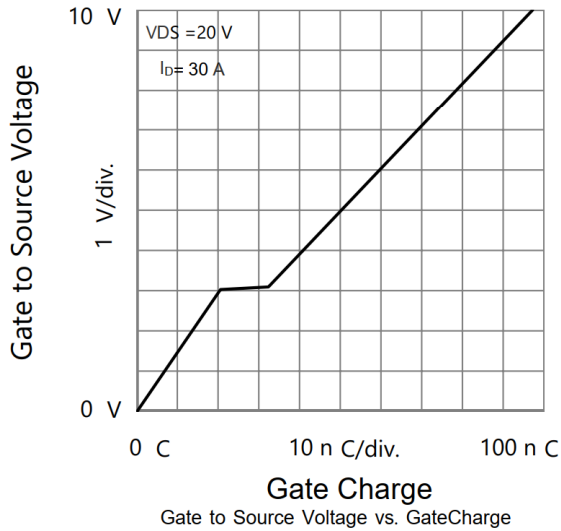


Source to Drain Voltage
Body Diode Forward Characteristics

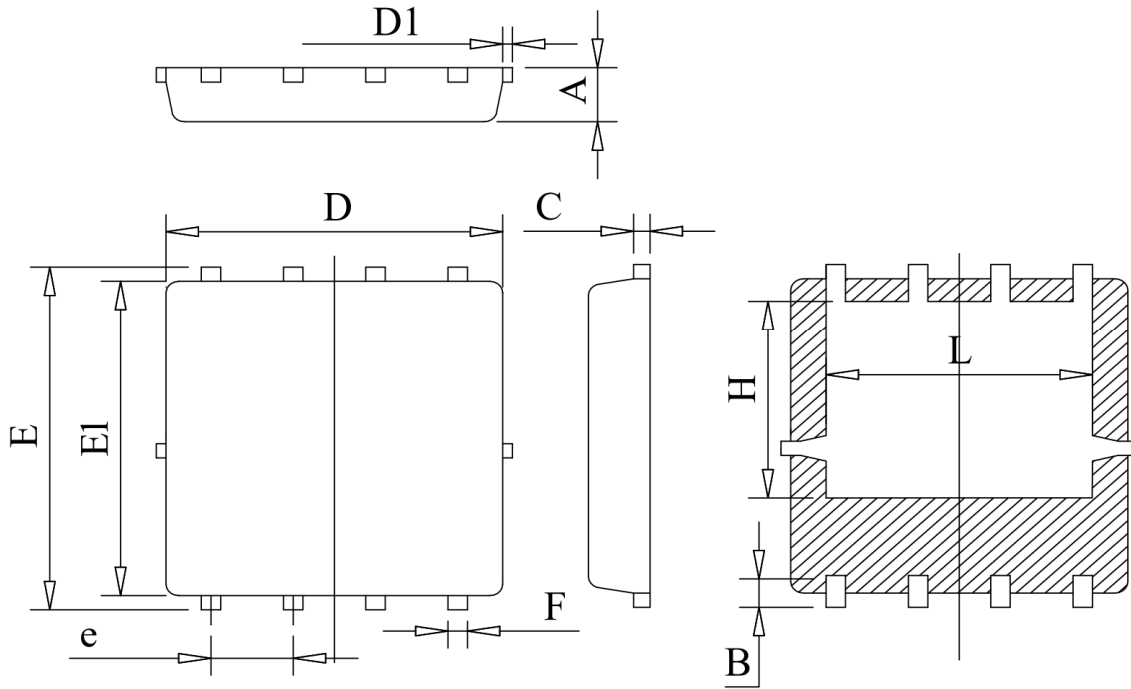


Drain to Source Voltage
Capacitances

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

Unit : mm

Symbol	Min	Typ	Max
A	0.78	0.95	1.12
B	0.45	0.58	0.78
C	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
E	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
H	3.25	3.47	3.70
L	3.75	4.00	4.25

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