

N-Channel 60 V (D-S) Super Junction MOSFET

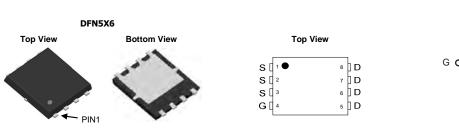
PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (mΩ) (Typ.)	I _D (A) ^a	Q _g (Typ.)		
60	0.98 at V _{GS} = 10 V	200	116 nC		
60	1.75 at V _{GS} = 4.5 V	200	110110		

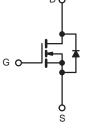
FEATURES

- DT-SJ Power MOSFET
- 100 % R_g and UIS tested
- Extremely Low R_{DS(ON)}

APPLICATIONS

- Synchronous Rectification
- Motor Drives and Uninterruptible Power Supplies





N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	LIMIT	UNIT			
Drain-Source Voltage	V _{DS}	60	V			
Gate-Source Voltage		V _{GS}			± 20	
Continuous Drain Current (T _{.I} = 150 °C)	T _C = 25 °C	1	200	А		
Continuous Drain Current (1j = 150 C)	T _C = 100 °C	- I _D	126			
Pulsed Drain Current (t = 100 μs)	I _{DM}	800				
Single Avalanche Energy ^a	L = 0.5 mH	E _{AS}	506	mJ		
Maximum Dayyar Dissination	T _C = 25 °C	P _D	175 ^{b,c}	W		
Maximum Power Dissipation	T _C = 100 °C	PD	70 ^{b,c}			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +150	°C		
Soldering Recommendations (Peak Temperature)		260				

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-Ambient (PCB Mount) ^{b,d}	t ≤ 10 s	R _{thJA}	50	°C/W		
Junction-to-Case (Drain)	Steady State	R _{thJC}	0.7	- C/VV		

- a. $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 60 °C/W.

SPECIFICATIONS (T _J = 25 °C, t						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1	-	3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	l	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	1	μА
	I _{DSS}	V _{DS} = 48 V, V _{GS} = 0 V, T _J = 125 °C	ı	15	-	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	200	-	-	Α
Drain-Source On-State Resistance a	Б	$V_{GS} = 10 \text{ V}, I_D = 50 \text{ A}$	-	0.98	1.3	mΩ
Diam-Source On-State Hesistance	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 40 A	-	1.75	2.3	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 5 V, I _D = 20 A	-	90	-	S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 30 V, f = 1 MHz	-	7540	-	pF
Output Capacitance	C _{oss}		-	1520	-	
Reverse Transfer Capacitance	C _{rss}		-	65	-	
Total Gate Charge ^c	Qg		-	116	-	nC
Gate-Source Charge ^c	Q_{gs}	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 50 A	-	16	-	
Gate-Drain Charge ^c	Q _{gd}		-	18	-	
Gate Resistance	R_g	f = 1 MHz	-	1.5	-	Ω
Turn-On Delay Time ^c	t _{d(on)}		-	39	-	
Rise Time °	t _r	$V_{DD} = 30 \text{ V}, I_D = 50 \text{ A}, R_g = 2.5 \Omega$	-	34	-	ns
Turn-Off Delay Time ^c	t _{d(off)}	V _{GS} = 10 V	-	52	-	
Fall Time ^c	t _f		-	27	-	
Drain-Source Body Diode Ratings and	Characterist	tics ^b (T _C = 25 °C)				
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	-	-	200	Α
Pulsed Current (t = 100 μs)	I _{SM}		-	-	800	Α
Forward Voltage ^a	V _{SD}	I _F = 50 A, V _{GS} = 0 V	-	-	1.4	V
Reverse Recovery Time	t _{rr}		-	69	-	ns
Reverse Recovery Charge	Q _{rr}	I _F = 50 A, di/dt = 100 A/μs	_	126	_	nC

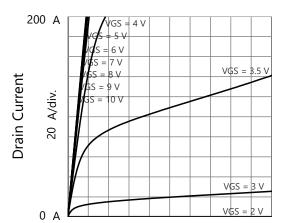
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS (25 C, unless otherwise noted)

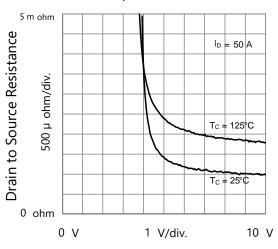


0 V

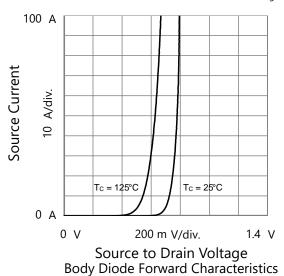
Drain to Source Voltage Output Characteristics

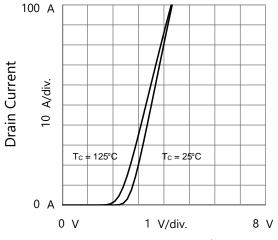
500 m V/div.

5 V

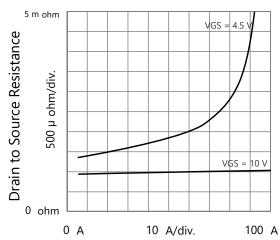


Gate to Source Voltage
Drain to Source Resistance vs. Gate to Source Voltage

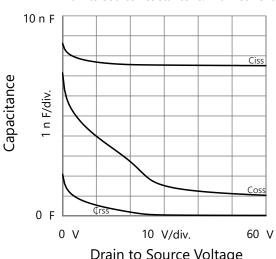




Gate to Source Voltage Transfer Characteristics



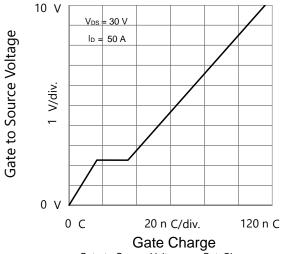
Drain Current
Drain to Source Resistance vs. Drain Current



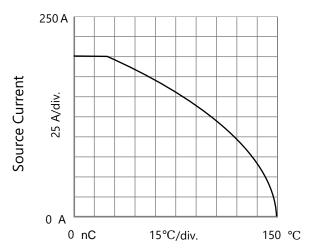
Drain to Source Voltage Capacitances



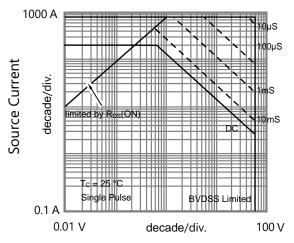
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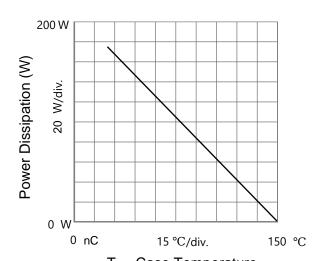




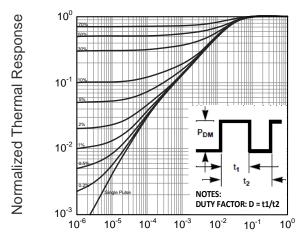
T_C - Case Temperature



Source to Drain Voltage Safe Operating Area, Junction-to-Ambient



T_C - Case Temperature



Square Wave Pulse Duration (s)

Normalized Thermal Transient Impedance





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