

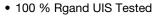
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N-Channel 120 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^{a, d}	Q _g (Typ.)		
120	0.008 at V _{GS} = 10 V	85	79nC		

FEATURES

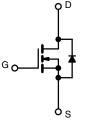




COMPLIANT

APPLICATIONS

- Synchronous rectification
- Primary side switch
- DC/DC converters
- OR-ing
- Power supplies
- Motor drive control
- Battery and load switch



N-Channel MOSFET

DFN5	(6	
Top View	Bottom View	
PIN1	(A)	

ABSOLUTE MAXIMUM RATING	15 $(1_A = 25^{\circ}0, 1)$	iniess otnerv	vise noted)		
PARAMETER Drain-source voltage Gate-source voltage		SYMBOL	LIMIT	UNIT	
		V _{DS}	120	V	
		V_{GS}	± 20	V	
	T _C = 25 °C		85 ^a		
Continuous drain augrent (T. 150 °C)	T _C = 70 °C	1 , [66.1		
Continuous drain current (T _J = 150 °C)	T _A = 25 °C	I _D	20.8 ^{b, c}		
	T _A = 70 °C	Ī [9.5 b, c	^	
Pulsed drain current (t = 100 μs)		I _{DM}	340	Α	
Oction and advantage of the second	T _C = 25 °C	,	85 ^a		
Continuous source-drain diode current	T _A = 25 °C	l _S	5.6 b, c		
Single pulse avalanche current		I _{AS}	70		
Single pulse avalanche energy	L = 0.1 mH	E _{AS}	103	mJ	
	T _C = 25 °C		155		
Maximum nauvar disaination	T _C = 70 °C	T _ [93	w	
Maximum power dissipation	T _A = 25 °C	P _D	5.15 ^{b, c}	VV	
	T _A = 70 °C	1	2.9 b, c		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C	
Soldering recommendations (peak temperature) c			260	-0	

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT	
Maximum junction-to-ambient ^b	t ≤ 10 s	R_{thJA}	15	30		
Maximum junction-to-case (drain) Steady s		R_{thJC}	0.8	1.2	°C/W	
Maximum junction-to-case (source)	R _{thJC}	1.0	1.5			

- a. Based on T_C = 25 °C.
 b. Surface mounted on 1" x 1" FR4 board.
- d. Calculated based on maximum junction temperature.

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	120	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA	- 56		-	mV/°C	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$			-6	-	mv/-C	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		-	4	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	100	nA	
7		V _{DS} = 100 V, V _{GS} = 0 V	-	-	1	μА	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 70 °C	-	-	10		
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	85	-	-	Α	
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} =10 V, I _D = 20 A	-	0.008	0.010	Ω	
Forward transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_D = 20 \text{ A}$	-	60	-	S	
Dynamic ^b							
Input capacitance	C _{iss}		-	7010	-		
Output capacitance	C _{oss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	1610	-	pF	
Reverse transfer capacitance	C _{rss}		-	186	-		
Total gate charge	Qg		-	79	-		
Gate-source charge	Q_{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$	-	11	-	nC	
Gate-drain charge	Q _{qd}		-	7	-		
Output charge	Q _{oss}	V _{DS} = 50 V, V _{GS} = 0 V	-	30	-		
Gate resistance	R_{g}	f = 1 MHz	0.4	1.2	2	Ω	
Turn-on delay time	t _{d(on)}		-	16	-		
Rise time	t _r	$V_{DD} = 50 \text{ V}, R_L = 2.5 \Omega, I_D \cong 20 \text{ A},$	-	22	-	ns	
Turn-off delay time	t _{d(off)}	$V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$	-	45	-	- 115	
Fall time	t _f		-	10	-		
Drain-Source Body Diode Characteristics							
Continuous source-drain diode current	I _S	T _C = 25 °C		-	85	_	
Pulse diode forward current ($t_p = 100 \mu s$)	I _{SM}		-	-	340	A	
Body diode voltage	V_{SD}	I _S = 5 A, V _{GS} = 0 V	-	0.7	1.2	V	
Body diode reverse recovery time	t _{rr}		-	55	112	ns	
		1 00 A di/dt 100 A/:- T 05 00	-	72	137	nC	
		$I_F = 20 \text{ A, di/dt} = 100 \text{ A/µs, T}_J = 25 ^{\circ}\text{C}$	-	25	-		
Reverse recovery rise time	t _b		-	20	-	ns	

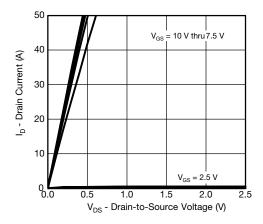
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing

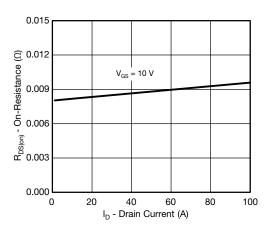
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



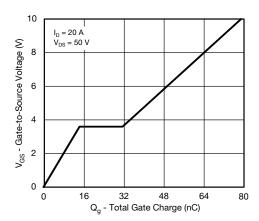
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



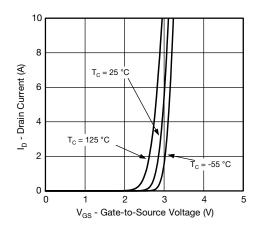
Output Characteristics



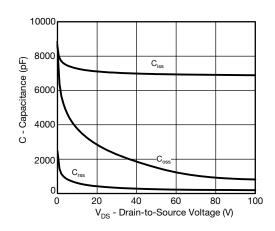
On-Resistance vs. Drain Current and Gate Voltage



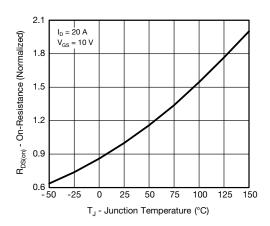
Gate Charge



Transfer Characteristics



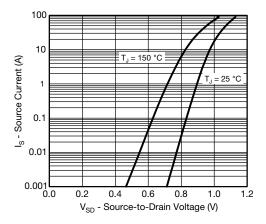
Capacitance



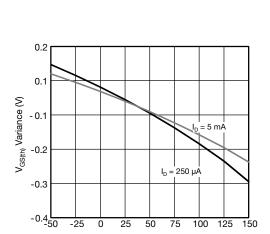
On-Resistance vs. Junction Temperature



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Source-Drain Diode Forward Voltage



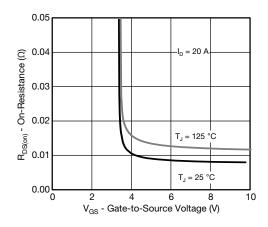
On-Resistance vs. Gate-to-Source Voltage

 T_J - Temperature (°C)

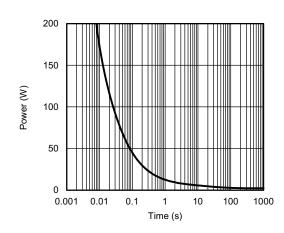
100 125

25 50

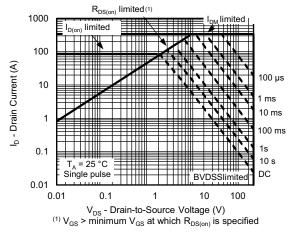
-25



Threshold Voltage



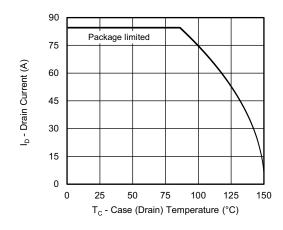
Single Pulse Power, Junction-to-Ambient



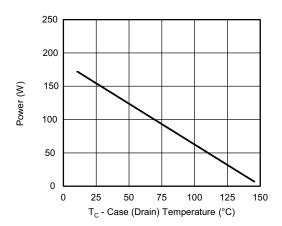
Safe Operating Area, Junction-to-Ambient



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating a

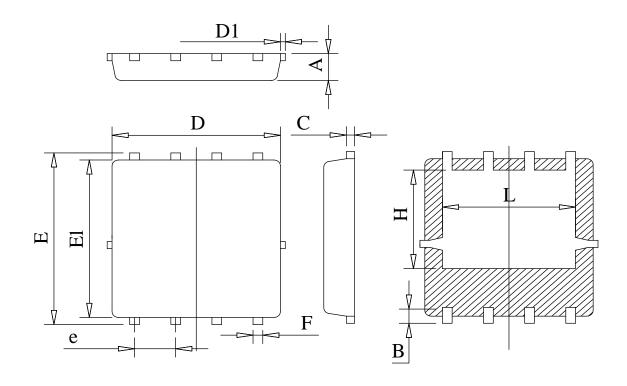


Power, Junction-to-Case

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



DFN5X6-8L PACKAGE OUTLINE



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

Unit: mm

Symbol	Min	Тур	Max
A	0.78	0.95	1.12
В	0.45	0.58	0.78
С	0.18	0.254	0.36
D	4.70	5.20	5.45
D1			0.18
Е	5.85	6.05	6.25
E1	5.38	5.55	5.98
e	1.15	1.27	1.40
F	0.18	0.30	0.52
Н	3.25	3.47	3.70
L	3.75	4.00	4.25





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